DDCO Assignment 3 (Up Counter)

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i) Testbench (upcountertb.v)

```
upcountertb - Notepad
File
      Edit
            View
module synchronouscountertb;
      reg clk, reset;
      wire [3:0] count;
      synchronouscounter synccntr(clk,reset,count);
      initial begin
            clk = 0; //initializing clk to zero
            reset = 1;
                             // change values after 125ns
            #125;
            reset = 0;
            #4500;
            $finish;
      end
      always #50 clk=~clk; //clk negates after 50ns
      always@(posedge clk) //clk is +ve edged
      begin
            $display ("value is %d", count);
      end
  initial begin $dumpfile("dump.vcd");
  $dumpvars;
  end
endmodule
```

ii) upcounter.v file

iii) command prompt

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C:\times C:\
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iv) GTK Wave

