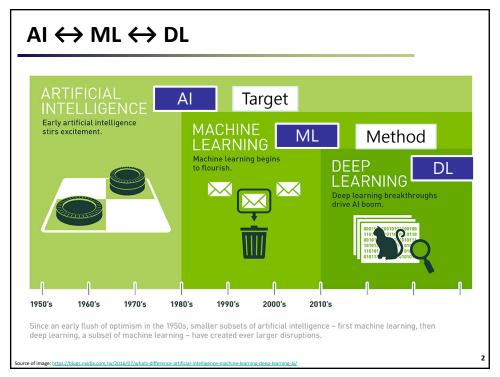


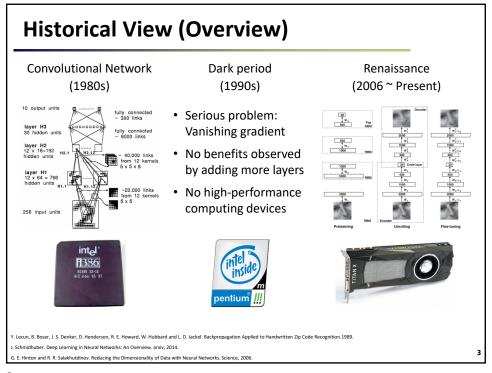
ECE 590-10/11
COMP ENG ML & DEEP NEURAL NETS

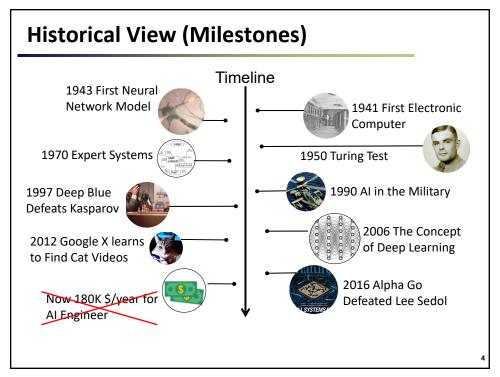
# 1. INTRODUCTION

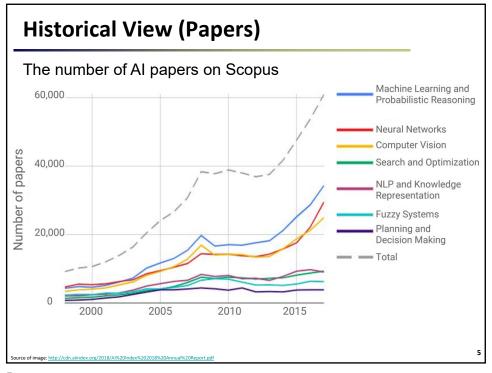
HAI LI & YIRAN CHEN, FALL 2019

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#### **Overview**

- For: MS/MEng students who want to learn computer engineering methods commonly performed in developing and using machine learning and deep neural network models.
- <u>Practice</u> will be the focus of this course, while <u>theoretical understanding</u> is essentially important.

### **Objectives**

This course is designed to improve your ability to:

- Comprehend the mechanisms, applications, and limitations of techniques commonly used in training and inference of machine learning and deep neural networks algorithms;
- **2. Formulate** hypotheses and conduct experiments employing these techniques;
- Analyze experimental results obtained by these techniques and your own practices and derive the conclusions that are supported or not supported by your data;
- **4. Synthesize** and **communicate** the experimental results and data through oral narrative, graphs, figure legends, and result narratives;
- **5. Utilize** proper engineering techniques for novel machine learning algorithms and deep neural network models;
- **6. Propose** new engineering approaches and techniques to further enhance machine learning and deep neural network training and inference execution.

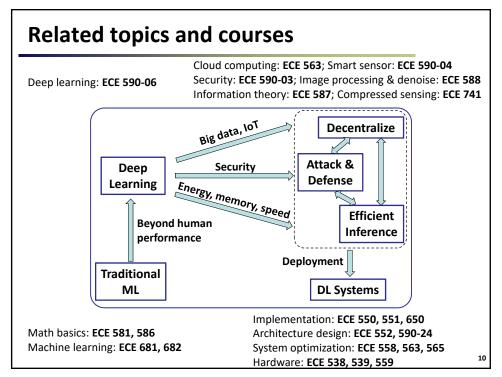
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#### Roadmap of the course Applying machine learning into the real world Federated learning Big data, loT Model Training & Decentralize Privacy design evaluation Robustness evaluation Attack & Security Deep Robustness enhancement Energy, memory, speed Defense Learning Interpretability Sparsity **Efficient** Quantization Inference **Beyond human** performance Deployment **DL Systems Traditional** ML Distributed Computational Accelerator computing infrastructure architecture

#### What we will learn

- DNN fundamentals
  - Convolutional neural network (CNN), recurrent neural network (RNN), forward/backward propagation, training, network architecture, ...
- DNN acceleration
  - Compact neural architecture, model compression, pruning, quantization, sparsification, ...
- Machine learning security
- Hardware systems
  - GPUs, CPUs, cloud servers, accelerators, etc.
- Advanced topics
  - Distributed computing, neural architecture search (NAS), generative adversarial network (GAN), decentralization and privacy

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### **Prerequisite**

- We expect that students to have basic object-oriented programming experience (e.g. C++, Python) and be familiar with linear algebra and computer hardware fundamentals prior to taking this course, such as
  - For graduate students: ECE 550 + ECE 551
  - For undergraduate students: ECE 381 + CS 308 + ECE/CS 250.
- If you are familiar with a topic that we are covering ...
  - You may learn something new
  - I may present it slightly differently than you are used to
  - You may be able to help other students learn it
- If you do not have these pre-requisites and are unfamiliar with these topics
  - We will **NOT** be slowing down to cover them
  - Please come talk to me or a TA sooner rather than later!

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### "Learn by doing"

- 3-4 homework assignments
- 4 lab assignments
  - Lab 1: Environment setup and CNN visualization
  - Lab 2: Build and train your own CNN
  - Lab 3: Deep compression
  - Lab 4: Adversarial attack and adversarial training

# **Logistics**

	ECE 590-10	ECE 590-11	
Faculty:	Dr. Hai "Helen" Li	Dr. Yiran Chen	
	Room 209B Hudson Hall	Room 209B Hudson Hall	
	Hai.li@duke.edu	Yiran.chen@duke.edu	
Lectures:	Tuesday/Thursday	Tuesday/Thursday	
	10:05-11:20am	1:25-2:40pm	
	Hudson Hall 208	Hudson Hall 207	
Office Hours:	By appointment	By appointment	
Teaching	Tunhou Zhang	Huanrui Yang	
Assistants:	tunhou.zhang@duke.edu	huanrui.yang@duke.edu	
	Qing Yang	Meng Xia	
	qing.yang21@duke.edu	mx41@duke.edu	

TAs are NOT under obligation to bail you out at 3am or debug your code. Your best bet is to get help in a timely and reasonable manner!

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# **Getting Info**

#### • Sakai:

- Syllabus, schedule, slides, assignments, rules/policies, prof/TA info, office hour info
- Links to useful resources
- Just assignment submission and gradebook
- Piazza: questions/answers
  - Signup link: piazza.com/duke/fall2019/ece5901011f19
  - Post all your questions here
  - Questions must be "public" unless good reason otherwise
  - No code in public posts!

### **Getting Answers to Questions**

- What do you do if you have a question?
  - Check Sakai
  - Check Piazza
    - If you have questions about homework, use Piazza then everyone can see the answer(s) posted there by me, a TA, or your fellow classmate
  - Contact TA directly if need additional background materials for prerequisite knowledge
  - Contact professor directly if issue that is specific to you and that can't be posted on Piazza (e.g., regrade)

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### **Textbook & Software**

- There are no designated textbooks for this course.
- The related reading materials (e.g., papers, webpages, etc.) will be distributed through Sakai before the classes.
- We recommend downloading Pytorch (https://pytorch.org/)

### **Homework and Lab Submission**

- Homework assignments and lab reports will be submitted as PDF files through the Assignments tool in Sakai. The code of lab assignments will be submitted to our servers. The details will be given during class.
- Late policy
  - 5 days per individual total for the semester
    - · Days, not classes
  - Used in entire days: 10 min late = on next day
  - After used up: must turn in on time
  - No credit for late work after this

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### **Grading**

Assignment	%
Lab assignments (4)	65%
Homework	30%
In-class assignments/discussion	5%

- Completion of all assignments is required in order to earn a passing grade of D- or better in this course.
- Course grades are determined using an absolute, but adjustable scale (i.e., there is no curve). A final course average (rounded to the nearest 0.1 point) of at least 93.3 = A, 90.0 = A-, 86.7 = B+, 83.3 = B, 80.0 = B-, etc.
- Note: the professors reserve the rights to scale the grades.

### **Grade Appeals**

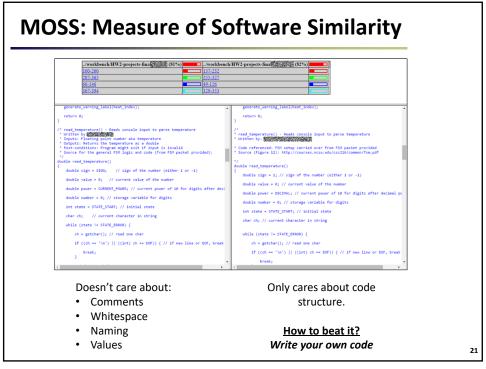
- All re-grade requests must be in writing
  - your assignment in question, with
  - a brief written description of the error, and
  - your Duke NetID.
- I will respond to your regrade request by email and make arrangement to return your work to you.
- As a matter of policy, when you request a regrade you are agreeing that the grading of the entire assignment may be re-evaluated.
- All regrade requests must be submitted no later than 1 week after the assignment was returned to you.

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#### **Academic Misconduct**

- Academic Misconduct
  - Refer to Duke Community Standard
  - Homework/lab is individual you do your own work
  - Common examples of cheating:
    - Running out of time and using someone else's output
    - Borrowing code from someone who took course before
    - Using solutions found on the Web
    - Having a friend help you to debug your program
- We will not tolerate any academic misconduct!
  - We use software for detecting cheating
- "But I didn't know that was cheating" is not a valid excuse



### **Academic Integrity: General**

Some general guidelines

- If you don't know if something is OK, please ask me.
- If you think "I don't want to ask, you will probably say no" that is a good sign its NOT acceptable.
- If you do something wrong, and regret it, please come forward—I recognize the value and learning benefit of admitting your mistakes. (Note: this does NOT mean there will be no consequences if you come forward).
- If you are aware of someone else's misconduct, you should report it to me or another appropriate authority.

#### **Course Problems**

- Struggling in course
  - Come to see me/TAs: We are here to help
- Other problems:
  - Feel free to talk to the instructor, who generally understands and will try to work with you
  - Some problems may extend well beyond my course
    - Academic Advisor
    - DGS Team

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### **Our Responsibilities**

- The instructor and TAs will...
  - Provide lectures/recitations at the stated times
  - Set clear policies on grading
  - Provide timely feedback on assignments
  - Be available out of class to provide reasonable assistance
  - Respond to comments or complaints about the instruction provided
- Students are expected to...
  - Receive lectures/recitations at the stated times
  - Turn in assignments on time
  - Seek out of class assistance in a timely manner if needed
  - Provide frank comments about the instruction or grading as soon as possible if there are issues
  - Assist each other within the bounds of academic integrity

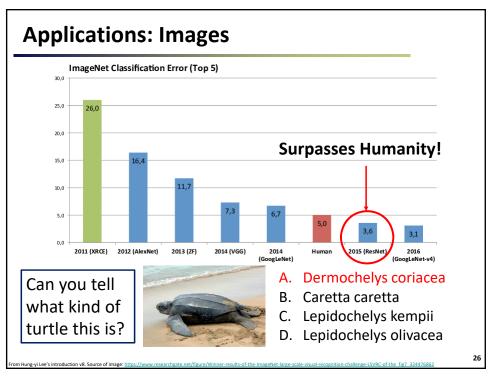
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### **Outline**

- Course introduction
- Machine learning & deep neural networks
  - Applications
  - Categories
  - Important metrics
  - Platforms & frameworks

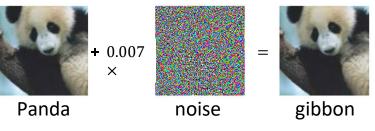
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# **Applications: Images**

• However, surprisingly weak...

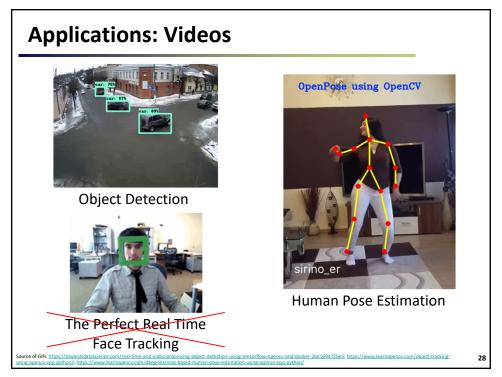


57.7% confidence

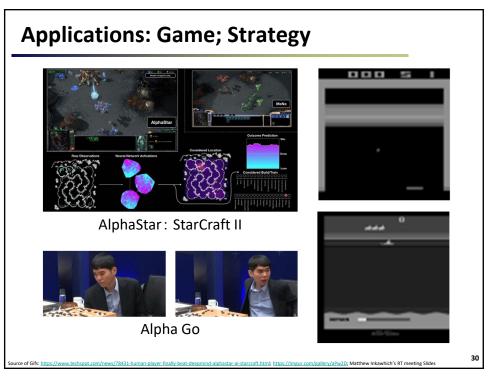
99.3% confidence

Source of this Pic: https://arxiv.org/pdf/1412.6572.pd

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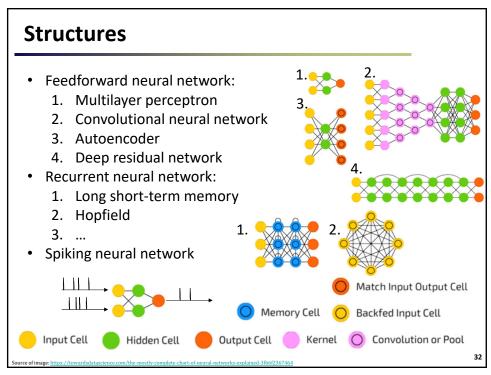


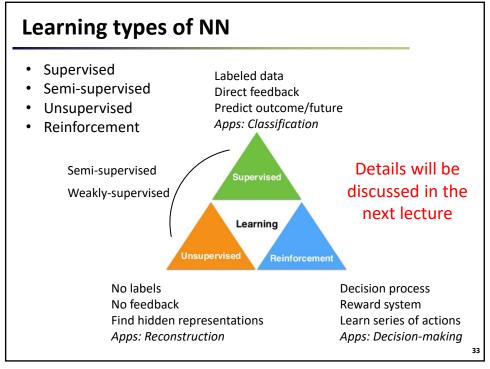
### **Outline**

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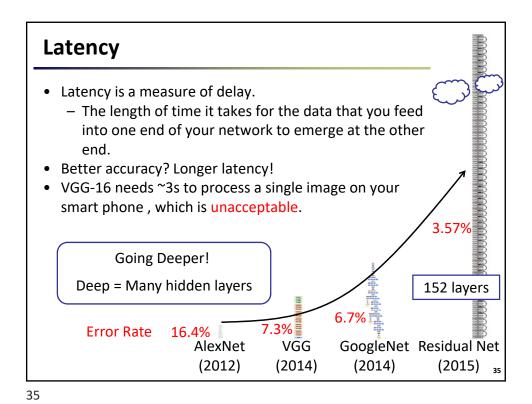
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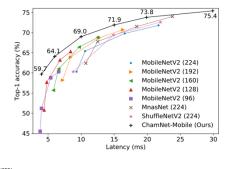
#### **Outline**

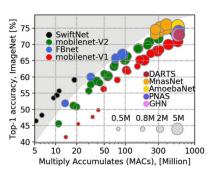
- Course introduction
- Machine learning & deep neural networks
  - Applications
  - Categories
  - Important metrics (LASER)
    - Latency
    - Accuracy
    - Size of model
    - Energy efficiency
    - Robustness
  - Platforms & frameworks



Accuracy

- Accuracy is a metric for classification problem
- We call it: "Top-K Accuracy"
- Higher accuracy is good, but we need to pay for it
  - Everything is a trade-off.





source:

1. Dai, Xiaoliang, et al. "Chamnet: Towards efficient network design through platform-aware model adaptation." (2019)

2. Cheng, Hsin-Pai et al. "SwiftNet: Using Graph Propagation as Meta-knowledge to Search Highly Representative Neural Architectures" (2019)

#### Size of model

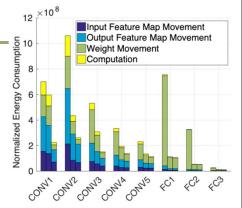
- # FLOP: Number of floating point operations.
- # MAC: Number of multiply-and-accumulate operations
  - Usually, 1 floating-point multiply-and-accumulate is considered equivalent to 2 FLOPs.
- # Parameters
- Area [mm²]

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# **Energy efficiency**

- Power consumption [mW]
- Energy is mainly used for
  - Calculation
  - Data movement

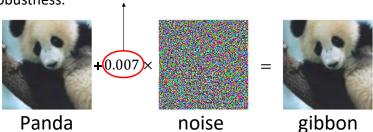


- Energy is a different thing:
  - A lower number of MACs does not necessarily lead to lower energy consumption.
  - Convolutional layers consume more energy than fullyconnected layers.
  - Deeper CNNs with fewer weights do not necessarily consume less energy than shallower CNNs with more weights.

ource of image: http://eyeriss.mit.edu/

#### **Robustness**

 This parameter, is used to evaluate a neural network's robustness.



57.7% confidence

99.3% confidence

- Usually, a high accuracy model is not robust.
- Compare to the size of a neural network, the structure has more impact towards robustness.

Everything is a trade-off

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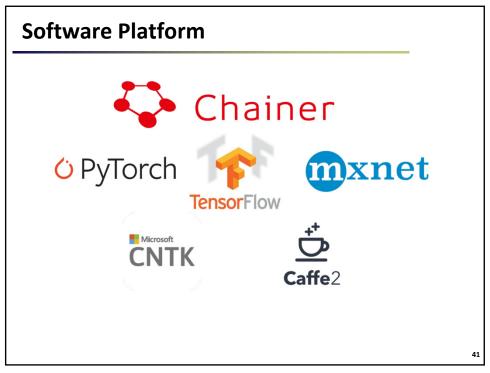
Source of image: Explaining and harnessing adversarial example

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### **Outline**

- Course introduction
- Machine learning & deep neural networks
  - Applications
  - Categories
  - Important metrics
  - Platforms & frameworks
    - Software platforms
    - Hardware computing devices

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#### **Comparison of Machine Learning Framework** O PyTorch Chainer mxnet TensorFlow C++, Python, C++, Python, Supported C++, Python, Scala, Julia, C++, Python Go, Java, Swift, interfaces Java, Rust, Go Perl, MATLAB JavaScript **Multi-GPU:** Yes Yes Yes Yes Data parallel **Multi-GPU:** Yes Yes Yes Yes Model parallel facebook > Preferred Networks Google APACHE **INVIDIA**. Developed by AI research **Brain team** group (intel) IRM

# **Tensorflow Pros/Cons**

#### **Pros**

- TensorBoard for visualization
- Data AND model parallelism; best of all frameworks
- Bigger developer community

#### Cons

- Steep learning curve
- Usually slower than other frameworks right now
- Much "fatter" than Pytorch

#### Example TensorFlow operation types.

Examples		
Add, Sub, Mul, Div, Exp, Log, Greater, Less, Equal,		
Concat, Slice, Split, Constant, Rank, Shape, Shuffle,		
MatMul, MatrixInverse, MatrixDeterminant,		
Variable, Assign, AssignAdd,		
SoftMax, Sigmoid, ReLU, Convolution2D, MaxPool,		
Save, Restore		
Enqueue, Dequeue, MutexAcquire, MutexRelease,		
Merge, Switch, Enter, Leave, NextIteration		

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# **Pytorch Pros/Cons**

### **Pros**

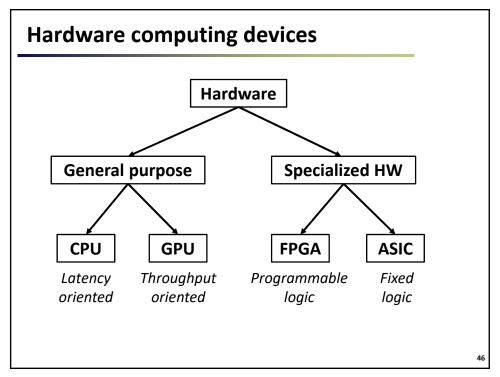
- Dynamic approach to graph computation
- Usually faster than other DNN toolkits
- More convenient than Tensorflow

#### **Cons**

- Relatively smaller developer community compared to Tensorflow
- Less product oriented compared to Tensorflow or MXNet

#### **Pytorch vs. Tensorflow: MNIST** 'Model function for CNN input\_layer = tf.reshape(features["x"], [-1, 28, 28, 1]) conv1 = tf.layers.conv2d( inputs=input layer. class Net(nn.Module): filters=32, kernel\_size=[5, 5], def \_\_init\_\_(self): super(Net, self).\_\_init\_\_() self.conv1 = nn.Conv2d(1, 20, 5, 1) padding="same", activation=tf.nn.relu) self.conv2 = nn.Conv2d(20, 50, 5, 1) pool1 = tf.layers.max\_pooling2d(inputs=conv1, pool\_size=[2, 2], strides=2) conv2 = tf.layers.conv2d( self.fc1 = nn.Linear(4\*4\*50, 500) inputs=pool1, filters=64, self.fc2 = nn.Linear(500, 10) kernel\_size=[5, 5], def forward(self, x): padding="same", activation=tf.nn.relu) x = F.relu(self.conv1(x)) $x = F.max_pool2d(x, 2, 2)$ pool2 = tf.layers.max\_pooling2d(inputs=conv2, pool\_size=[2, 2], strides=2) x = F.relu(self.conv2(x)) pool2\_flat = tf.reshape(pool2, [-1, 7 \* 7 \* 64]) dense = tf.layers.dense(inputs=pool2\_flat, units=1824, activation=tf.nn.relu) dropout = tf.layers.dropout( inputs=dense, rate=0.4, training=mode == tf.estimator.ModeKeys.TRAIN) x = F.max\_pool2d(x, 2, 2) x = x.view(-1, 4\*4\*50)x = F.relu(self.fc1(x)) logits = tf.layers.dense(inputs=dropout, units=10) x = self.fc2(x)return F.log\_softmax(x, dim=1) predictions = { "Classes": tf.argmax(input=logits, axis=1), # Add "softmax\_tensor" to the graph. It is used for PREDICT and by the "probabilities": tf.nn.softmax(logits, name="softmax\_tensor") https://github.com/tensorflow/tensorflow/blob/master/tensorflow/examples/tutorials/layers/cnn\_mnist.py https://github.com/pytorch/examples/blob/master/mnist/main.py

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# **Hardware computing devices**

Cost and speed are critical for both training and inference

#### **GPU**

- High power consumption
- Higher demand on data center cooling, power supply, and space utilization

#### **ASIC**

- High Non-recurring engineering
- Long design period, not suitable for fast iteration in NN development

#### CPU

- Medium cost
- Medium power consumption
- Low speed

#### **FPGA**

- · Low power
- Low cost, Hundreds of dollars
- Hardware reconfigurable

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# **ImageNet-1K Classification Performance**

Platform	Inference Throughput	Peak TFLOPs	Effective TFLOPs	Power	Power Efficiency GOPs/J
Intel Xeon E5- 2450	53 images/s	0.27T	0.074T (27%)	~225W	~0.3
Altera Arria 10 GX1150	369 images/s	1.366T	0.51T (38%)	~40W	~12.8
NVIDIA Titan X	4129 images/s	6.1T	5.75T (94%)	~250W	~23.0

Neural network is usually trained in back-end GPU clusters, while FPGA is very suitable for low-power real-time inference job

C. Ovtcharov, et al, Hot Chips Symposium (HCS), 201

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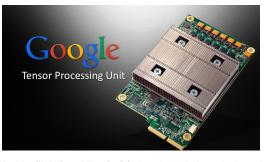
# **Tensor Processing Unit (TPU)**

Unveiled during Google I/O Conference, Mountain View, CA (May 2016).

Tensor Processing Unit (TPU): a custom ASIC built specifically for machine learning — and tailored for TensorFlow.

This unit is designed for dense matrices, sparsity will have higher priority in the future.







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# **Tensor Processing Unit (TPU)**

#### Applications:

- 1. RankBrain: improve the relevancy of search results.
- 2. Street View: improve the accuracy and quality of our maps and navigation.
- 3. AlphaGo: "think" much faster and look farther ahead between moves.







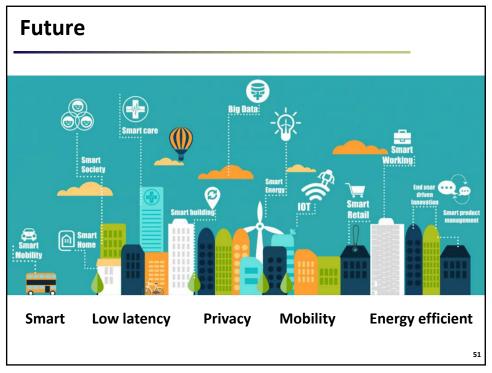






Google, https://cloudplatform.googleblog.com/2016/05/Google-supercharges-machine-learning-tasks-with-custom-chip.htm

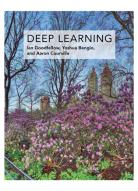
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# **Reading material**

 Deep Learning (2016), Ian Goodfellow and Yoshua Bengio and Aaron Courville <a href="http://www.deeplearningbook.org/">http://www.deeplearningbook.org/</a>

- Chapter "Introduction"



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