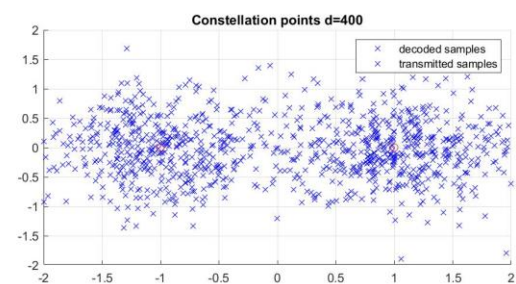
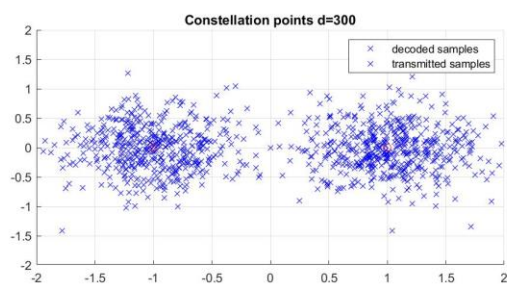
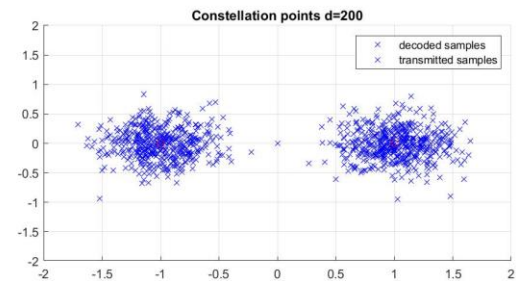
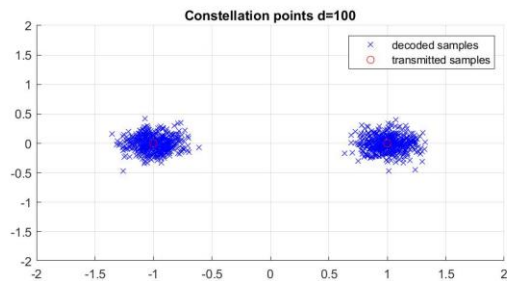


網路系統總整與實作 Lab #7

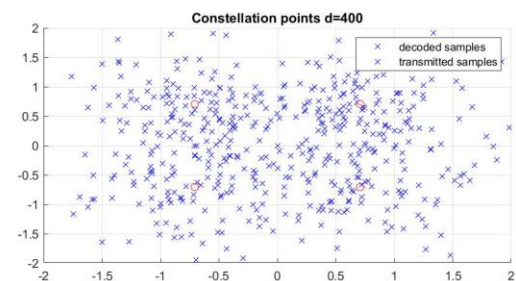
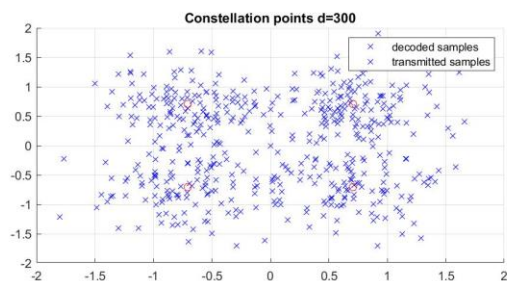
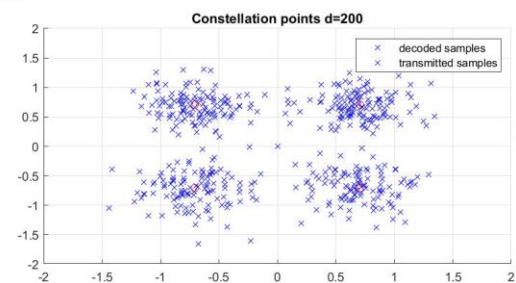
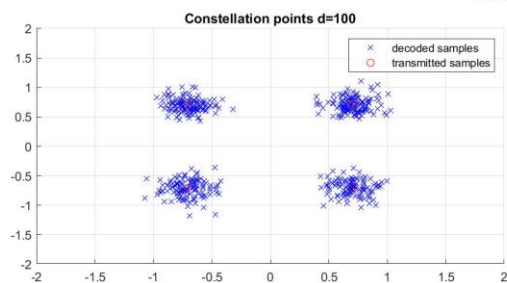
Pre_Lab7_mod

0716236 劉耀文

Modulation order: 1



Modulation order: 2



Modulation order: 4

