Lecture 9. CNN Architectures

김영은

Review

Paddle Last time: Deep learning frameworks (Baidu) Caffe2 Caffe **CNTK** (Facebook) (UC Berkeley) (Microsoft) Torch PyTorch (NYU / Facebook) **MXNet** (Amazon) Theano TensorFlow (U Montreal) (Google) And others...

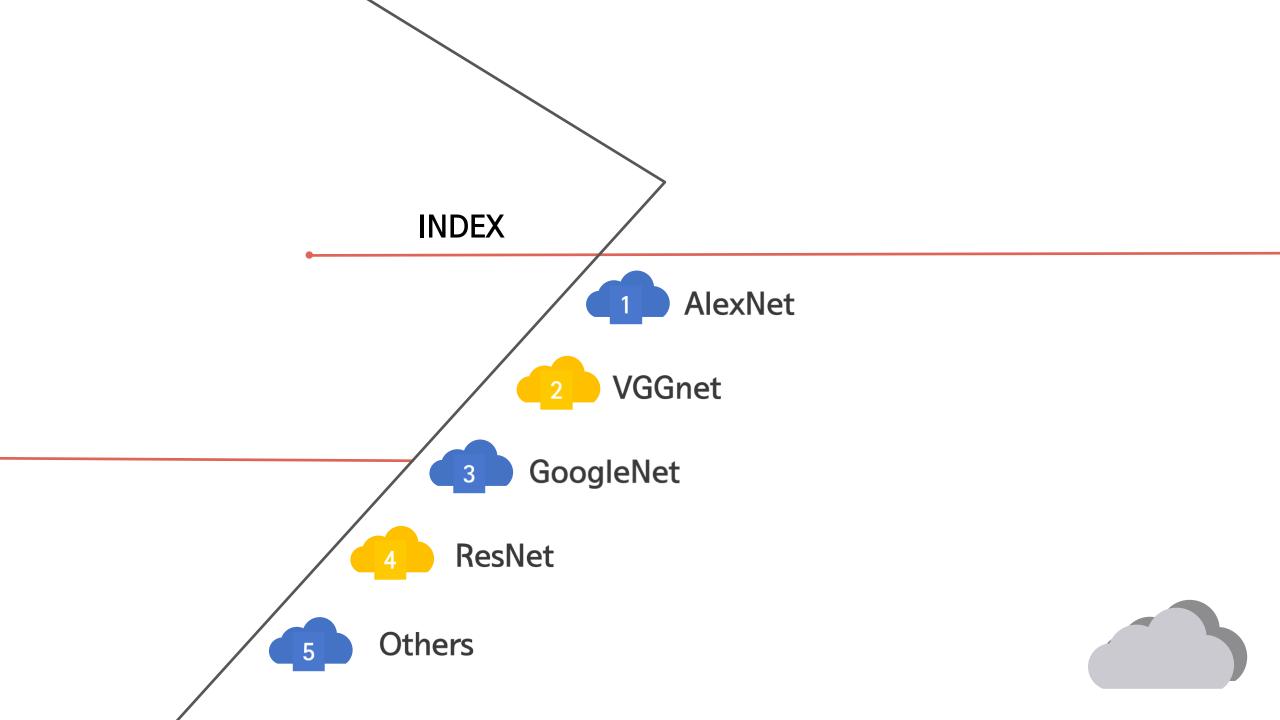
- (1) Easily build big computational graphs
- (2) Easily compute gradients in computational graphs
- (3) Run it all efficiently on GPU (wrap cuDNN, cuBLAS, etc)

Review

```
class ReLU(torch.autograd.Function):
    def forward(self, x):
        self.save_for_backward(x)
        return x.clamp(min=0)

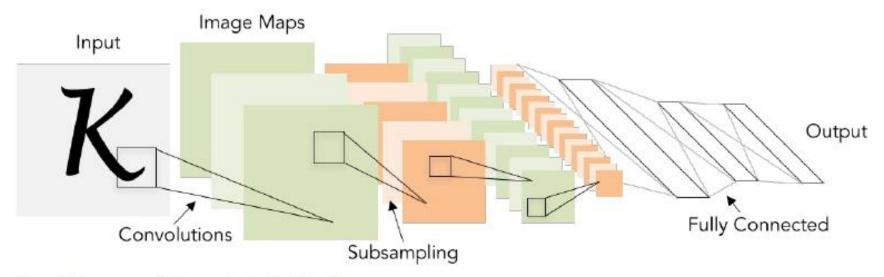
def backward(self, grad_y):
        x, = self.saved_tensors
        grad_input = grad_y.clone()
        grad_input[x < 0] = 0
        return grad_input</pre>
```

```
import torch
from torch.autograd import Variable
N, D in, H, D out = 64, 1000, 100, 10
x = Variable(torch.randn(N, D in))
y = Variable(torch.randn(N, D out), requires grad=False)
model = torch.nn.Sequential(
          torch.nn.Linear(D in, H),
          torch.nn.ReLU(),
          torch.nn.Linear(H, D out))
loss fn = torch.nn.MSELoss(size average=False)
learning rate = 1e-4
for t in range(500):
   y pred = model(x)
   loss = loss fn(y pred, y)
    model.zero grad()
   loss.backward()
    for param in model.parameters():
        param.data -= learning rate * param.grad.data
```



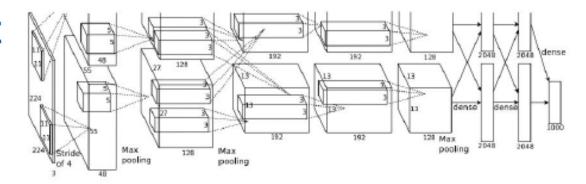
Review: LeNet-5

[LeCun et al., 1998]



Conv filters were 5x5, applied at stride 1 Subsampling (Pooling) layers were 2x2 applied at stride 2 i.e. architecture is [CONV-POOL-CONV-POOL-FC-FC]

[Krizhevsky et al. 2012]



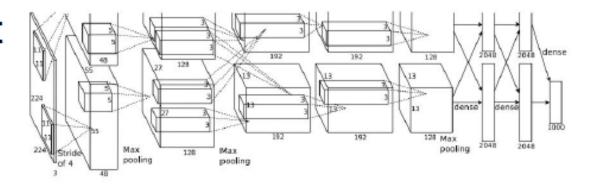
Input: 227x227x3 images

First layer (CONV1): 96 11x11 filters applied at stride 4 =>

Output volume **[55x55x96]** Hint: (227-11)/4+1 = 55

Parameters: (11*11*3)*96 = **35K**

[Krizhevsky et al. 2012]



Input: 227x227x3 images After CONV1: 55x55x96

Second layer (POOL1): 3x3 filters applied at stride 2

Output volume: 27x27x96 Hint: (55-3)/2+1 = 27

Parameters: 0!

[Krizhevsky et al. 2012]

Full (simplified) AlexNet architecture:

[227x227x3] INPUT

[55x55x96] CONV1: 96 11x11 filters at stride 4, pad 0

[27x27x96] MAX POOL1: 3x3 filters at stride 2

[27x27x96] NORM1: Normalization layer

[27x27x256] CONV2: 256 5x5 filters at stride 1, pad 2

[13x13x256] MAX POOL2: 3x3 filters at stride 2

[13x13x256] NORM2: Normalization layer

[13x13x384] CONV3: 384 3x3 filters at stride 1, pad 1

[13x13x384] CONV4: 384 3x3 filters at stride 1, pad 1

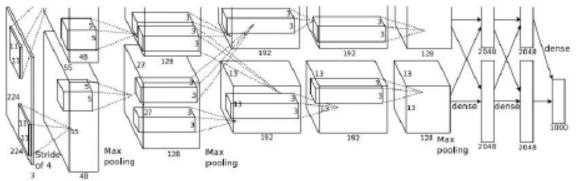
[13x13x256] CONV5: 256 3x3 filters at stride 1, pad 1

[6x6x256] MAX POOL3: 3x3 filters at stride 2

[4096] FC6: 4096 neurons

[4096] FC7: 4096 neurons

[1000] FC8: 1000 neurons (class scores)



Details/Retrospectives:

- first use of ReLU
- used Norm layers (not common anymore)
- heavy data augmentation
- dropout 0.5
- batch size 128
- SGD Momentum 0.9
- Learning rate 1e-2, reduced by 10 manually when val accuracy plateaus
- L2 weight decay 5e-4
- 7 CNN ensemble: 18.2% -> 15.4%

Figure copyright Alex Krizhevsky, Ilya Sutskever, and Geoffrey Hinton, 2012. Reproduced with permission.

[Krizhevsky et al. 2012]

Full (simplified) AlexNet architecture:

[227x227x3] INPUT

[55x55x96] CONV1: 96 11x11 filters at stride 4, pad 0

[27x27x96] MAX POOL1: 3x3 filters at stride 2

[27x27x96] NORM1: Normalization layer

[27x27x256] CONV2: 256 5x5 filters at stride 1, pad 2

[13x13x256] MAX POOL2: 3x3 filters at stride 2

[13x13x256] NORM2: Normalization layer

[13x13x384] CONV3: 384 3x3 filters at stride 1, pad 1

[13x13x384] CONV4: 384 3x3 filters at stride 1, pad 1

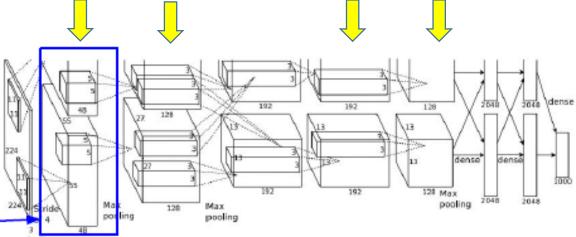
[13x13x256] CONV5: 256 3x3 filters at stride 1, pad 1

[6x6x256] MAX POOL3: 3x3 filters at stride 2

[4096] FC6: 4096 neurons

[4096] FC7: 4096 neurons

[1000] FC8: 1000 neurons (class scores)

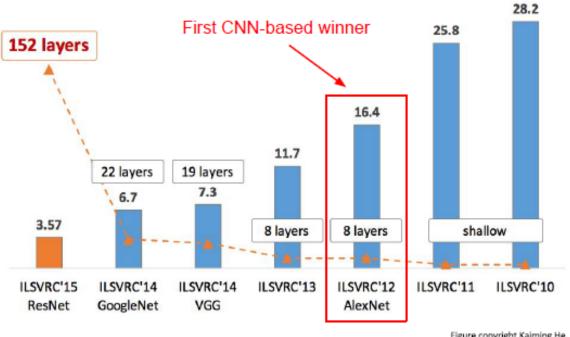


[55x55x48] x 2

Historical note: Trained on GTX 580 GPU with only 3 GB of memory. Network spread across 2 GPUs, half the neurons (feature maps) on each GPU.

Figure copyright Alex Krizhevsky, Ilya Sutskever, and Geoffrey Hinton, 2012. Reproduced with permission.

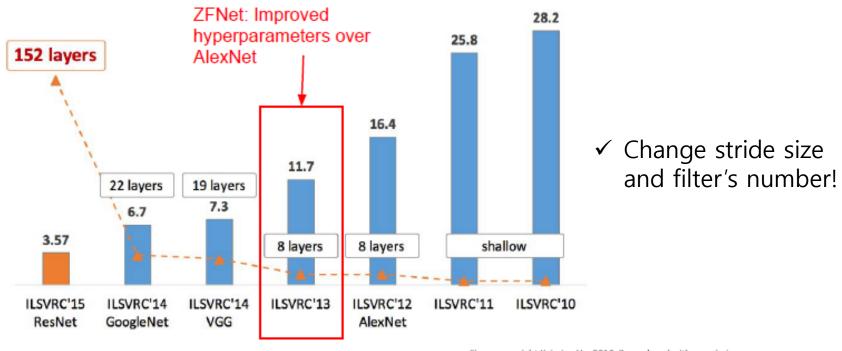
ImageNet Large Scale Visual Recognition Challenge (ILSVRC) winners



- ✓ cut the error rate!
- ✓ first CNN based winner!

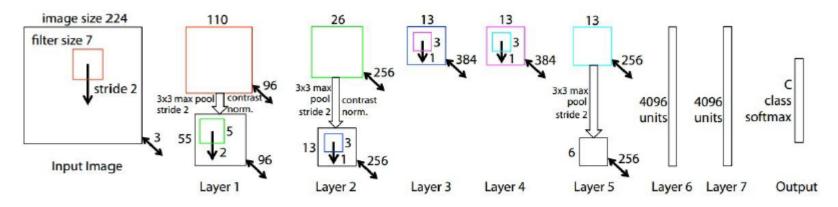
Figure copyright Kalming He, 2016. Reproduced with permission.

ImageNet Large Scale Visual Recognition Challenge (ILSVRC) winners



ZFNet

[Zeiler and Fergus, 2013]



AlexNet but:

CONV1: change from (11x11 stride 4) to (7x7 stride 2)

CONV3,4,5: instead of 384, 384, 256 filters use 512, 1024, 512

ImageNet top 5 error: 16.4% -> 11.7%

TODO: remake figure

ImageNet Large Scale Visual Recognition Challenge (ILSVRC) winners

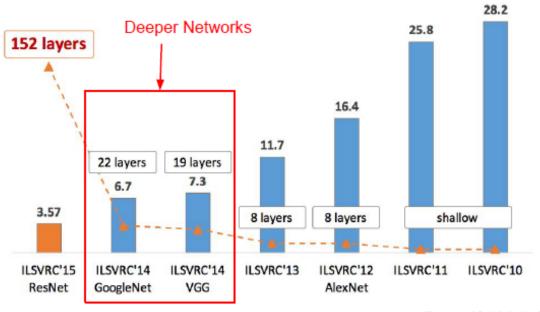


Figure copyright Kaiming He, 2016. Reproduced with permission.

"Much deeper network, Much smaller filters"

Case Study: VGGNet

[Simonyan and Zisserman, 2014]

Small filters, Deeper networks

8 layers (AlexNet)
-> 16 - 19 layers (VGG16Net)

Only 3x3 CONV stride 1, pad 1 and 2x2 MAX POOL stride 2

11.7% top 5 error in ILSVRC'13 (ZFNet)

-> 7.3% top 5 error in ILSVRC'14

Softmax	
FC 1000	
FC 4096	
FC 4096	
Pool	
3x3 conv, 256	
3x3 conv, 384	
Pool	
3x3 conv, 384	
Pool	
5x5 conv, 258	
11x11 conv, 96	
Input	

AlexNet

	SOUTHER
	FC 1000
Softmax	FC 4098
FC 1000	FC 4098
FC 4096	Pool
FC 4098	3x3 conv, 512
Pool	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
3x3 conv, 512	Pool
Pool	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
Pool	Pool
3x3 conv, 256	3x3 conv, 256
3x3 conv, 256	3x3 conv, 256
Pool	Pool
3x3 conv, 128	3x3 conv, 128
3x3 conv, 128	3x3 conv, 128
Pool	Pool
3x3 conv, 64	3x3 conv, 64
3x3 conv, 64	3x3 conv, 64
Input	Input
VGG16	VGG19

Case Study: VGGNet

[Simonyan and Zisserman, 2014]

Q: Why use smaller filters? (3x3 conv)

Stack of three 3x3 conv (stride 1) layers has same **effective receptive field** as one 7x7 conv layer

But deeper, more non-linearities

And fewer parameters: 3 * (3²C²) vs. 7²C² for C channels per layer

Softmax	
FC 1000	
FC 4096	
FC 4096	
Pool	
3x3 conv, 256	
3x3 conv, 384	
Pool	
3x3 conv, 384	
Pool	
5x5 conv, 256	
11x11 conv, 96	
Input	
AlexNet	

	Softmax
	FC 1000
Softmax	FC 4098
FC 1000	FC 4098
FC 4098	Pool
FC 4098	3x3 conv, 512
Pool	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
3x3 conv, 512	Pool
Pool	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
3x3 conv, 512	3x3 conv, 512
Pool	Pool
3x3 conv, 256	3x3 conv, 256
3x3 conv, 256	3x3 conv, 256
Pool	Pool
3x3 conv, 128	3x3 conv, 128
3x3 conv, 128	3x3 conv, 128
Pool	Pool
3x3 conv, 64	3x3 conv, 64
3x3 conv, 64	3x3 conv, 64
Input	Input
VGG16	VGG19

```
(not counting biases)
INPUT: [224x224x3] memory: 224*224*3=150K params: 0
CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*3)*64 = 1,728
                                                                                                        fc8
CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*64)*64 = 36,864
                                                                                                        fc7
POOL2: [112x112x64] memory: 112*112*64=800K params: 0
                                                                                             FC 4096
                                                                                                        fc6
CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*64)*128 = 73,728
                                                                                              Pool
CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*128)*128 = 147,456
                                                                                                      conv5-3
                                                                                                      conv5-2
POOL2: [56x56x128] memory: 56*56*128=400K params: 0
                                                                                                      conv5-1
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*128)*256 = 294,912
                                                                                             Pool
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824
                                                                                                      conv4-3
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824
                                                                                                      conv4-2
POOL2: [28x28x256] memory: 28*28*256=200K params: 0
                                                                                                      conv4-1
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*256)*512 = 1,179,648
                                                                                             Pool
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296
                                                                                                      conv3-2
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296
                                                                                                      conv3-1
POOL2: [14x14x512] memory: 14*14*512=100K params: 0
                                                                                             Pool
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296
                                                                                                      conv2-2
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296
                                                                                                      conv2-1
                                                                                             Pool
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296
                                                                                                      conv1-2
POOL2: [7x7x512] memory: 7*7*512=25K params: 0
                                                                                                      conv1-1
FC: [1x1x4096] memory: 4096 params: 7*7*512*4096 = 102,760,448
                                                                                             Input
FC: [1x1x4096] memory: 4096 params: 4096*4096 = 16,777,216
                                                                                           VGG16
FC: [1x1x1000] memory: 1000 params: 4096*1000 = 4,096,000
TOTAL memory: 24M * 4 bytes ~= 96MB / image (only forward! ~*2 for bwd)
                                                                                         Common names
TOTAL params: 138M parameters
```

```
(not counting biases)
                     memory: 224*224*3=150K params: 0
INPUT: [224x224x3]
CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*3)*64 = 1,728
                                                                                         Note:
CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*64)*64 = 36,864
POOL2: [112x112x64] memory: 112*112*64=800K params: 0
                                                                                         Most memory is in
CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*64)*128 = 73,728
                                                                                         early CONV
CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*128)*128 = 147,456
POOL2: [56x56x128] memory: 56*56*128=400K params: 0
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*128)*256 = 294,912
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824
POOL2: [28x28x256] memory: 28*28*256=200K params: 0
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*256)*512 = 1,179,648
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296
POOL2: [14x14x512] memory: 14*14*512=100K params: 0
                                                                                         Most params are
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296
                                                                                         in late FC
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296
POOL2: [7x7x512] memory: 7*7*512=25K params: 0
FC: [1x1x4096] memory: 4096 params: 7*7*512*4096 = 102,760,448
FC: [1x1x4096] memory: 4096 params: 4096*4096 = 16,777,216
FC: [1x1x1000] memory: 1000 params: 4096*1000 = 4,096,000
TOTAL memory: 24M * 4 bytes ~= 96MB / image (only forward! ~*2 for bwd)
TOTAL params: 138M parameters
```

Case Study: VGGNet

[Simonyan and Zisserman, 2014]

Details:

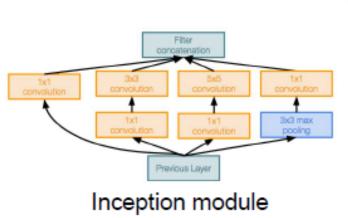
- ILSVRC'14 2nd in classification, 1st in localization
- Similar training procedure as Krizhevsky 2012
- No Local Response Normalisation (LRN)
- Use VGG16 or VGG19 (VGG19 only slightly better, more memory)
- Use ensembles for best results
- FC7 features generalize well to other tasks

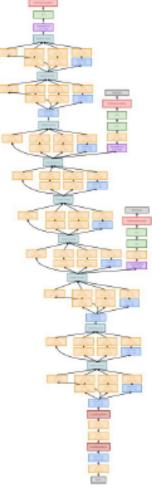


[Szegedy et al., 2014]

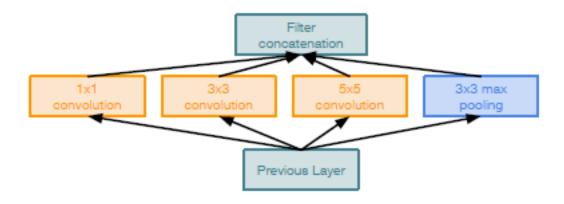
Deeper networks, with computational efficiency

- 22 layers
- Efficient "Inception" module
- No FC layers
- Only 5 million parameters!
 12x less than AlexNet
- ILSVRC'14 classification winner (6.7% top 5 error)





[Szegedy et al., 2014]



Naive Inception module

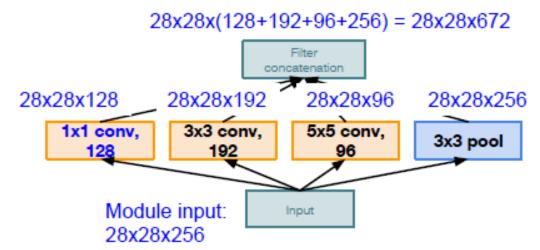
Apply parallel filter operations on the input from previous layer:

- Multiple receptive field sizes for convolution (1x1, 3x3, 5x5)
- Pooling operation (3x3)

Concatenate all filter outputs together depth-wise

[Szegedy et al., 2014]

Example: Q3:What is output size after filter concatenation?



Naive Inception module

Q: What is the problem with this? [Hint: Computational complexity]

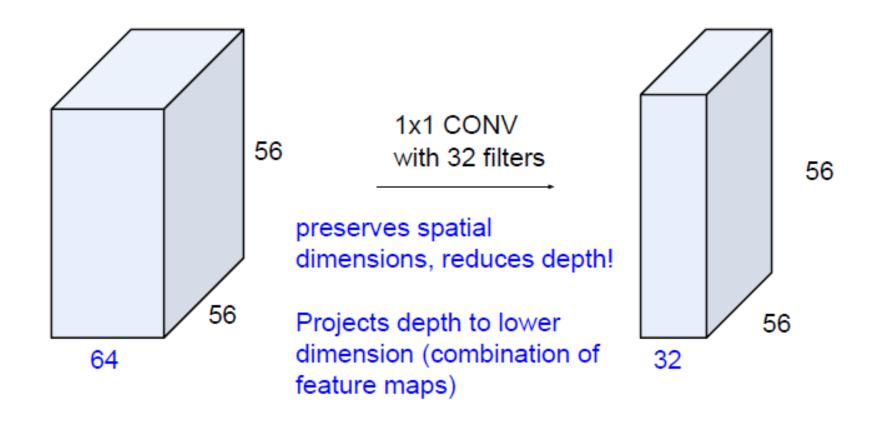
Conv Ops:

[1x1 conv, 128] 28x28x128x1x1x256 [3x3 conv, 192] 28x28x192x3x3x256 [5x5 conv, 96] 28x28x96x5x5x256 **Total: 854M ops**

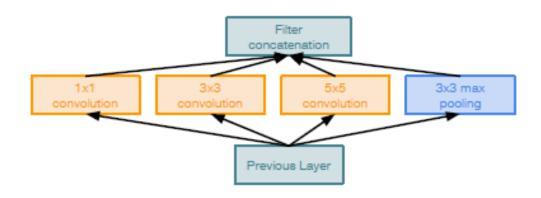
Very expensive compute

Pooling layer also preserves feature depth, which means total depth after concatenation can only grow at every layer!

Reminder: 1x1 convolutions

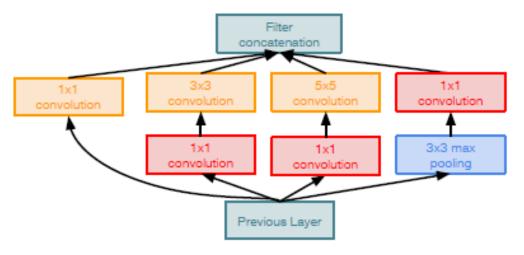


[Szegedy et al., 2014]



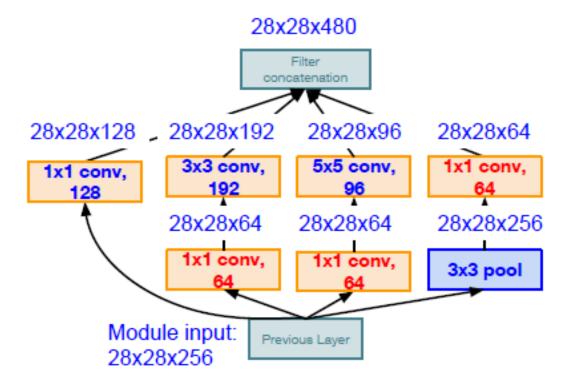
Naive Inception module

1x1 conv "bottleneck" layers



Inception module with dimension reduction

[Szegedy et al., 2014]



Inception module with dimension reduction

Using same parallel layers as naive example, and adding "1x1 conv, 64 filter" bottlenecks:

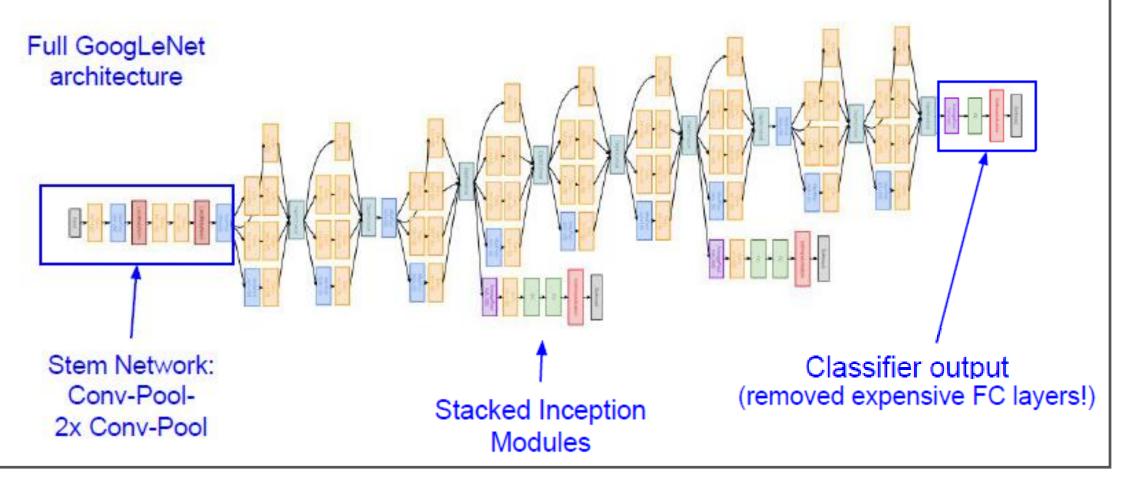
Conv Ops:

[1x1 conv, 64] 28x28x64x1x1x256 [1x1 conv, 64] 28x28x64x1x1x256 [1x1 conv, 128] 28x28x128x1x1x256 [3x3 conv, 192] 28x28x192x3x3x64 [5x5 conv, 96] 28x28x96x5x5x64 [1x1 conv, 64] 28x28x64x1x1x256 Total: 358M ops

Compared to 854M ops for naive version Bottleneck can also reduce depth after pooling layer

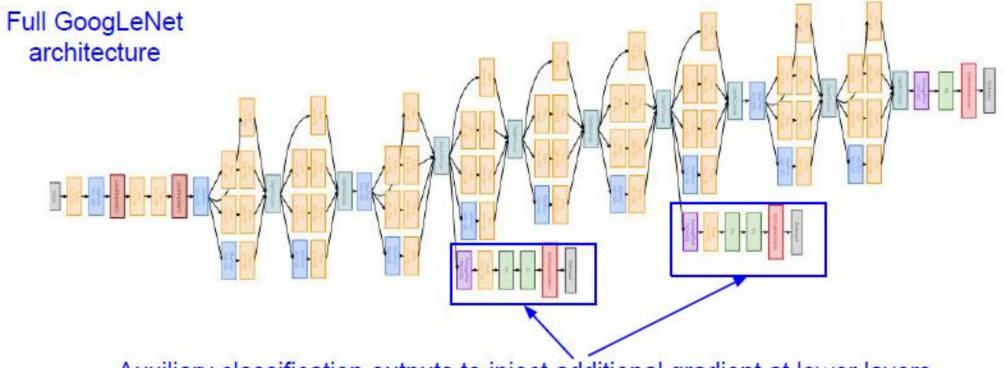
Case Study: GoogLeNet = "Full Inception Architecture"

[Szegedy et al., 2014]



0 • • •

[Szegedy et al., 2014]

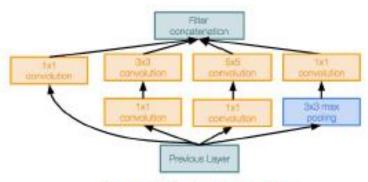


Auxiliary classification outputs to inject additional gradient at lower layers (AvgPool-1x1Conv-FC-FC-Softmax)

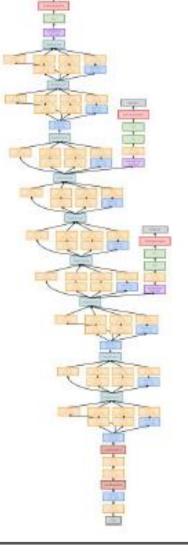
[Szegedy et al., 2014]

Deeper networks, with computational efficiency

- 22 layers
- Efficient "Inception" module
- No FC layers
- 12x less params than AlexNet
- ILSVRC'14 classification winner (6.7% top 5 error)



Inception module



ImageNet Large Scale Visual Recognition Challenge (ILSVRC) winners

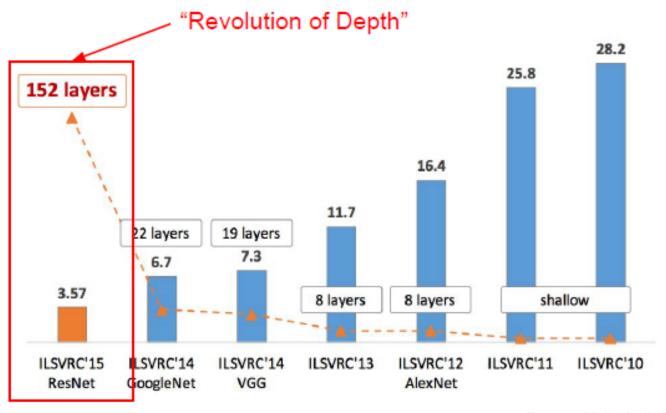
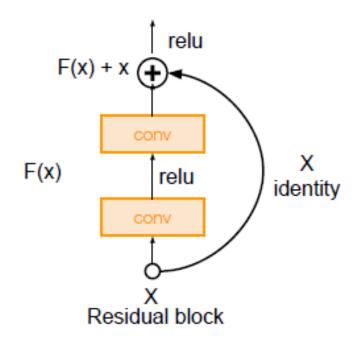


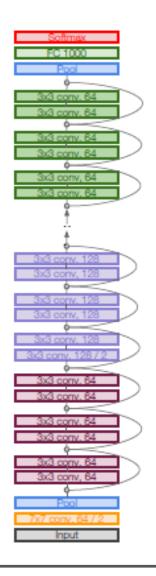
Figure copyright Kaiming He, 2016. Reproduced with permission.

[He et al., 2015]

Very deep networks using residual connections

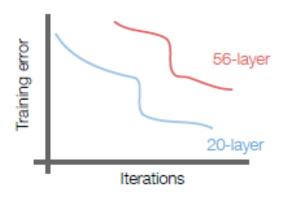
- 152-layer model for ImageNet
- ILSVRC'15 classification winner (3.57% top 5 error)
- Swept all classification and detection competitions in ILSVRC'15 and COCO'15!

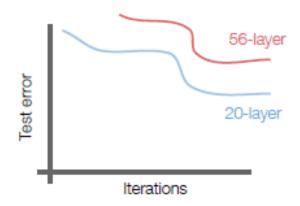




[He et al., 2015]

What happens when we continue stacking deeper layers on a "plain" convolutional neural network?





56-layer model performs worse on both training and test error

-> The deeper model performs worse, but it's not caused by overfitting!

[He et al., 2015]

Hypothesis: the problem is an *optimization* problem, deeper models are harder to optimize

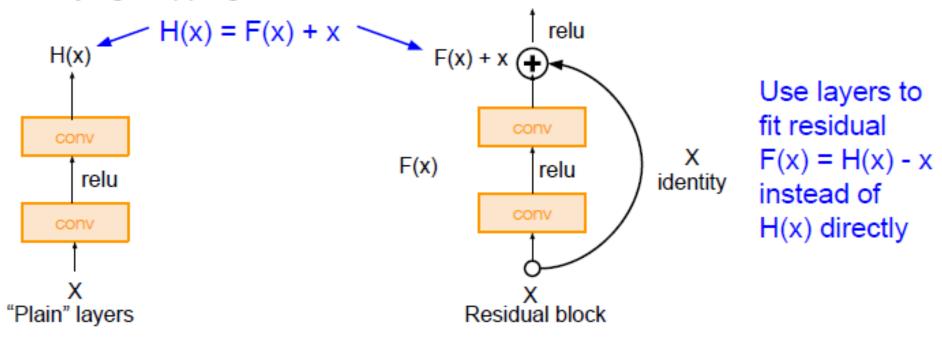
The deeper model should be able to perform at least as well as the shallower model.

A solution by construction is copying the learned layers from the shallower model and setting additional layers to identity mapping.

Case Study: ResNet = "Full Resident Architecture"

[He et al., 2015]

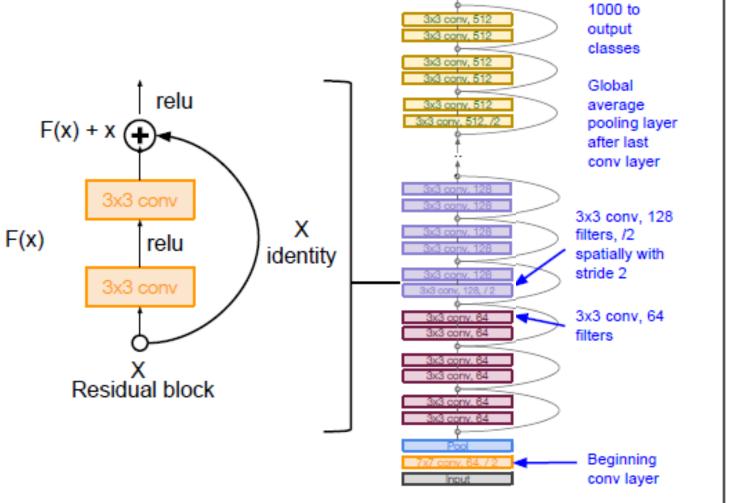
Solution: Use network layers to fit a residual mapping instead of directly trying to fit a desired underlying mapping



[He et al., 2015]

Full ResNet architecture:

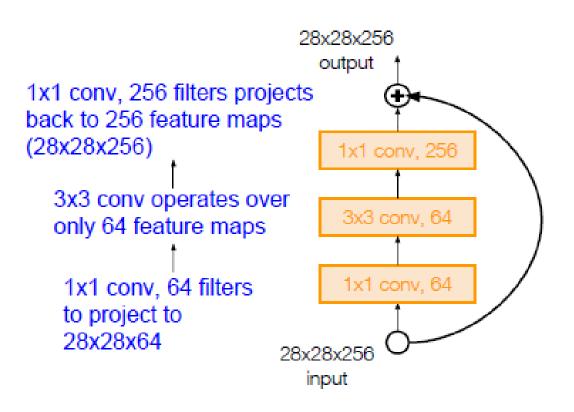
- Stack residual blocks
- Every residual block has two 3x3 conv layers
- Periodically, double # of filters and downsample spatially using stride 2 (/2 in each dimension)
- Additional conv layer at the beginning
- No FC layers at the end (only FC 1000 to output classes)



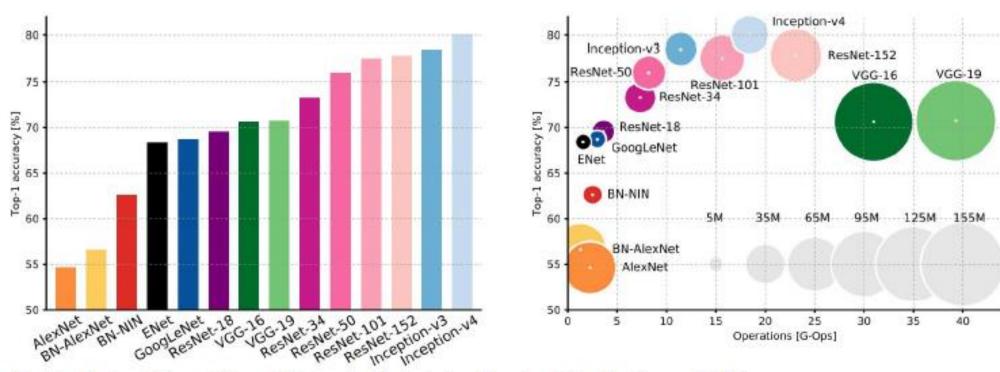
No FC layers besides FC

[He et al., 2015]

For deeper networks (ResNet-50+), use "bottleneck" layer to improve efficiency (similar to GoogLeNet)



Comparing complexity...



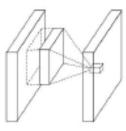
An Analysis of Deep Neural Network Models for Practical Applications, 2017.

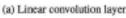
Figures copyright Alfredo Canziani, Adam Paszke, Eugenio Culurciello, 2017. Reproduced with permission.

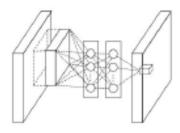
Network in Network (NiN)

[Lin et al. 2014]

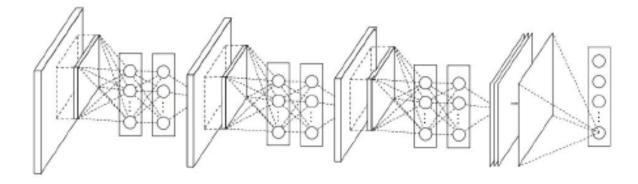
- Mlpconv layer with "micronetwork" within each conv layer to compute more abstract features for local patches
- Micronetwork uses multilayer perceptron (FC, i.e. 1x1 conv layers)
- Precursor to GoogLeNet and ResNet "bottleneck" layers
- Philosophical inspiration for GoogLeNet





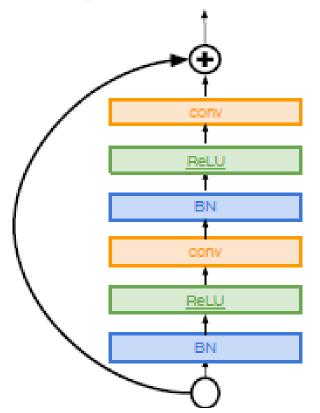


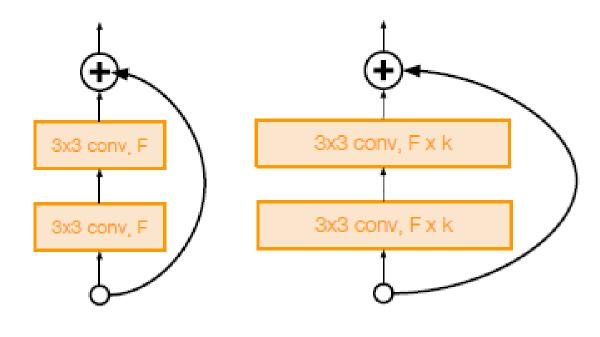
(b) Mlpconv layer



Figures copyright Lin et al., 2014. Reproduced with permission.

Improving ResNets...





Basic residual block

Wide residual block

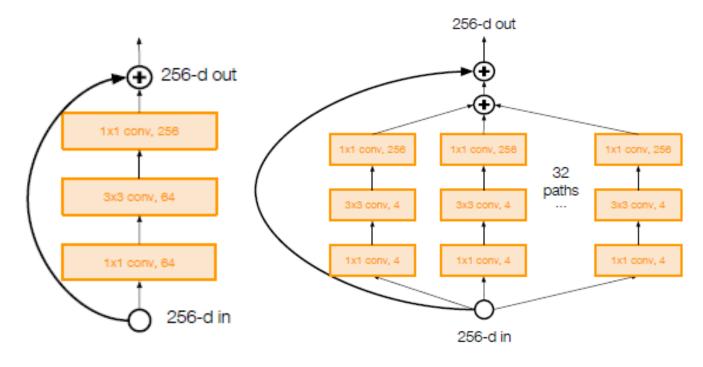
Wide Residual Networks

Identity Mappings in Deep Residual Networks

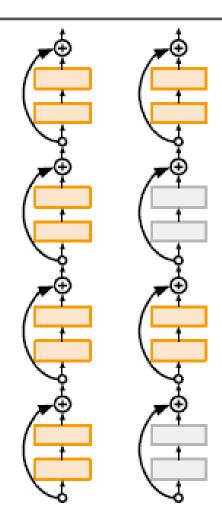
[He et al. 2016] [Zagoruyko et al. 2016]

O • • • •

Improving ResNets...



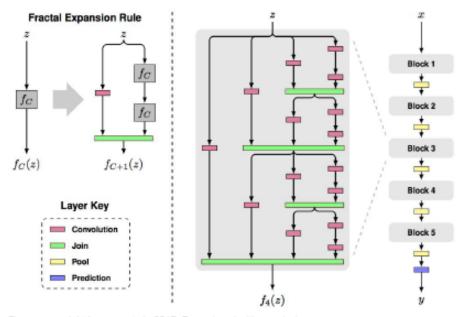
Aggregated Residual Transformations for Deep Neural Networks (ResNeXt)



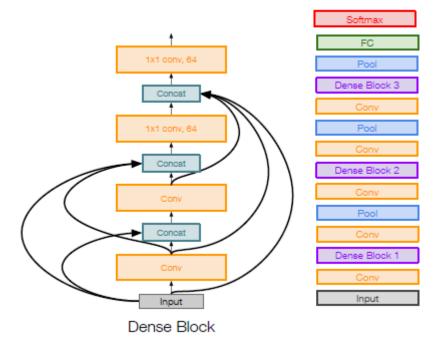
Deep Networks with Stochastic Depth [Huang et al. 2016]

Beyond ResNets...

FractalNet: Ultra-Deep Neural Networks without Residuals



Figures copyright Larsson et al., 2017. Reproduced with permission.

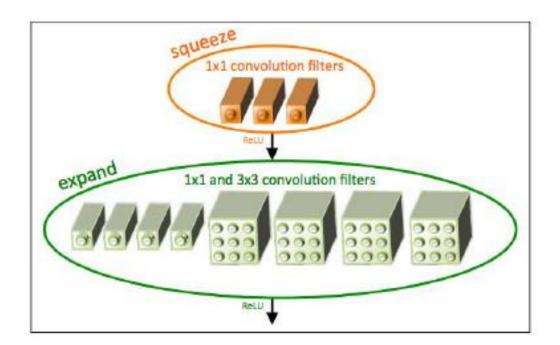


Densely Connected Convolutional Networks

Efficient networks...

SqueezeNet: AlexNet-level Accuracy With 50x Fewer Parameters and <0.5Mb Model Size

[landola et al. 2017]



------ 감 사 합 니 다 ! ------

발표 <mark>들어주셔</mark>서 감사합니다