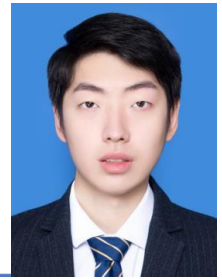


# Yunhao Zhou



Tel: +86 13585206891

E-mail: [zyheesjtu@sjtu.edu.cn](mailto:zyheesjtu@sjtu.edu.cn)

Web: [yyh-sjtu.github.io](http://yyh-sjtu.github.io)

## Education

**2022.9-present** Master of EE

**Shanghai Jiao Tong University (SJTU) Shanghai**

GPA: 3.86/4.0 Rank: 10/50

**2018.9-2022.6** Bachelor of EE **Huazhong University of Science and Technology (HUST) Wuhan**

GPA: 3.93/4.0 Rank: 1/28

**Major course:** Machine Learning, Computer Architecture, Fundamental of Software Technology, Digital IC

**Skills:** C/C++, Python, Verilog, TCL, Gem5, ABC, Cadence virtuoso, Quartus, Proteus

**IELTS:** 7.0

## Awards

2022&2023 Shanghai Jiao Tong University first-class scholarship 2022 Outstanding graduate of HUST

2021 American college students mathematical Contest in modeling: Meritorious Winner

2020 National Mathematical Contest in Modeling for College Students: National Second Prize

2020 Outstanding Undergraduate of HUST

## Experience

**2024 National Center of Technology Innovation for EDA**

python

- Verilog representation learning based on GNN and CDFG
- LLM based Verilog generation including finetune, inference, and auto-test

**2024 Nvidia Internship Project(released to global physical design department of Nvidia)**

python

- Develop inhouse VLSI Physical Design CAD software

**2023 Shanghai Innovation Center for Processor Technologies**

python C++ verilog

- Developed AGI Multimodal Large Models for Chip Auto-Design
- Data driven logic synthesis, research BSD based CPU generation and NN based approximate logic synthesis
- Co-author paper accepted by **DAC(2024)**, **ICCAD(2024)**

**2023 X-EPIC Technology Internship Project(released)**

C C++ TCL

- Conducted algorithm for X-valued combinational equivalence checking based on ABC system
- Participated in development of LEC APP in formal verification software

**2023 High Fanout Register Identification Algorithm in ASIC Chip Physical Design**

python verilog

- Implemented a verilog netlist parser for hierarchically structured Verilog netlists
- Developed a search algorithm with a time complexity of  $O(N)$  for extraction of register fanout counts

**2020 National Mathematical Contest in Modeling for College Students**

- Financial loan optimization problem based on decision tree and RAROC model

**2019 National Mathematical Contest in Modeling for College Students**

- The Numerical simulation of the control process of high pressure tube based on differential equation