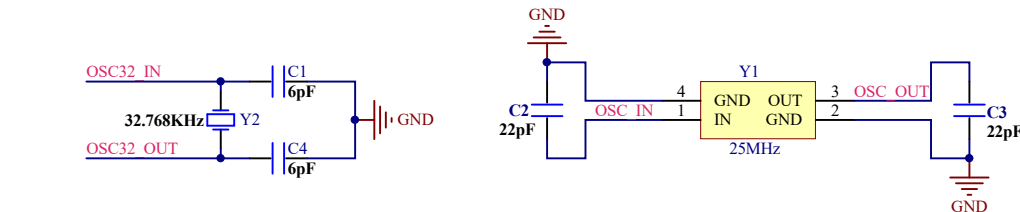


# MCU

U1A									
SDMMC2_CMD	PA0_C	T1	PA0_C	PD0	D13	PD0	FMC D2		
	PA0	N5	PA0	PD1	E12	PD1	FMC D3		
	PA1_C	T2	PA1_C	PD2	D12	PD2	SDMMC1_CMD		
	PA1	N4	PA1	PD3	B12	PD3			
WKUP	PA2	N3	PA2	PD4	A12	PD4			
LED0	PA3	U2	PA3	PD5	A11	PD5			
DCMI_HSYNC	PA4	U3	PA4	PD6	B11	PD6			
	PA5	T3	PA5	PD7	C11	PD7			
DCMI_PIXCLK	PA6	R3	PA6	PD8	U16	PD8	FMC D13		
	PA7	R5	PA7	PD9	T17	PD9	FMC D14		
KEY0	PA8	E15	PA8	PD10	T16	PD10	FMC D15		
USART1_TX	PA9	D15	PA9	PD11	R15	PD11	DCMI_SCL		
USART1_RX	PA10	D14	PA10	PD12	R16	PD12	I2C4_SCL		
USB_FS_DM	PA11	E17	PA11	PD13	R17	PD13	I2C4_SDA		
USB_FS_DP	PA12	E16	PA12	PD14	P16	PD14	FMC D0		
SWDIO	PA13	C15	PA13	PD15	P15	PD15	FMC D1		
SWCLK	PA14	B14	PA14						
	PA15	A14	PA15						
BL_CTR	PB0	U5	PB0	PE0	C4	PE0	FMC NBL0		
LED1	PB1	T5	PB1	PE1	B4	PE1	FMC NBL1		
	PB2	R6	PB2	PE2	C3	PE2	SPI4_SCK		
SDMMC2_D2	PB3	C6	PB3	PE3	D3	PE3			
SDMMC2_D3	PB4	B7	PB4	PE4	D2	PE4	SPI4_CS		
CTP-INT RTP-PEN	PB5	A5	PB5	PE5	D1	PE5	SPI4_MISO		
DCMI_D5	PB6	B5	PB6	PE6	E5	PE6	SPI4_MOSI		
DCMI_VSYNC	PB7	C5	PB7	PE7	U9	PE7	FMC D4		
DCMI_D6	PB8	D5	PB8	PE8	T9	PE8	FMC D5		
DCMI_D7	PB9	D4	PB9	PE9	P9	PE9	FMC D6		
CTP-SCL RTP-SCK	PB10	P11	PB10	PE10	N9	PE10	FMC D7		
CTP-SDA RTP-MOSI	PB11	P12	PB11	PE11	P10	PE11	FMC D8		
CTP-RST RTP-CS	PB12	T14	PB12	PE12	R10	PE12	FMC D9		
RTP_MISO	PB13	U14	PB13	PE13	T10	PE13	FMC D10		
SDMMC2_D0	PB14	U15	PB14	PE14	U10	PE14	FMC D11		
SDMMC2_D1	PB15	T15	PB15	PE15	R11	PE15	FMC D12		
FMC_SDNWE	PC0	L2	PC0	PF0	G4	PF0	FMC A0		
SDMMC2_CK	PC1	M2	PC1	PF1	G3	PF1	FMC A1		
	PC2	M3	PC2	PF2	G1	PF2	FMC A2		
	PC2_C	R1	PC2_C	PF3	H4	PF3	FMC A3		
LCD_DC	PC3	M4	PC3	PF4	J5	PF4	FMC A4		
	PC3_C	R2	PC3_C	PF5	J4	PF5	FMC A5		
	PC4	T4	PC4	PF6	K2	PF6	QSPI BK1 IO3		
DCMI_D0	PC5	U4	PC5	PF7	K3	PF7	QSPI BK1 IO2		
DCMI_D1	PC6	F14	PC6	PF8	K4	PF8	QSPI BK1 IO0		
DCMI_D2	PC7	F13	PC7	PF9	L4	PF9	QSPI BK1 IO1		
DCMI_D3	PC8	E14	PC8	PF10	L3	PF10	QSPI_CLK		
SDMMC1_D0	PC9	E13	PC9	PF11	T7	PF11	FMC_SDNRAS		
SDMMC1_D1	PC10	A13	PC10	PF12	R7	PF12	FMC A6		
SDMMC1_D2	PC11	B13	PC11	PF13	P7	PF13	FMC A7		
SDMMC1_D3	PC12	C12	PC12	PF14	P8	PF14	FMC A8		
SDMMC1_CK	PC13	E3	PC13	PF15	R9	PF15	FMC A9		
OSC32_IN	PC14	C2	PC14_OSC32_IN						
OSC32_OUT	PC15	C1	PC15_OSC32_OUT						

STM32H743XIH6

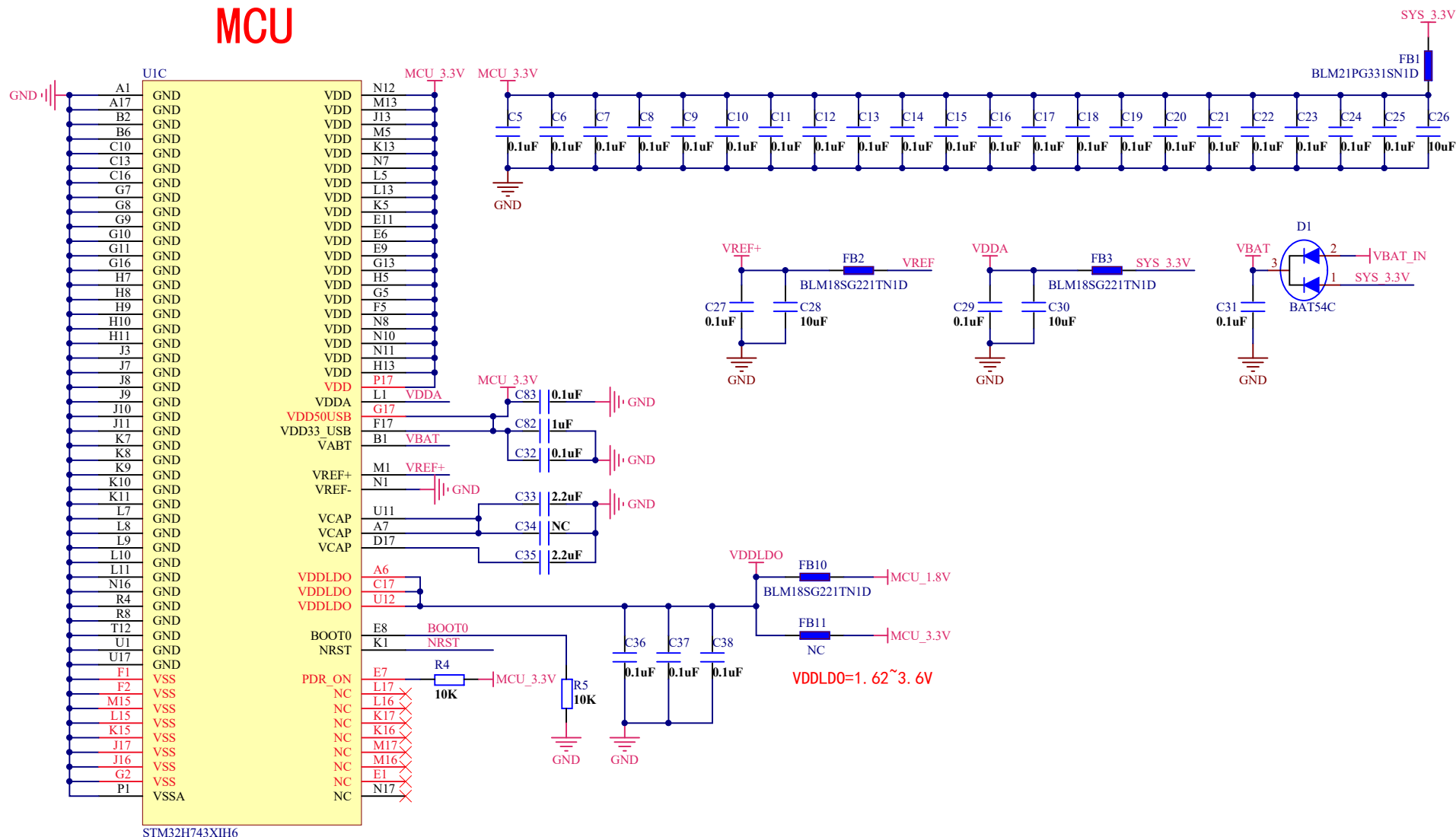


U1B									
FMC_A10	PG0	T8	PG0	P10	A16	P10	FMC D24		
FMC_A11	PG1	U8	PG1	P11	A15	P11	FMC D25		
LED_EN	PG2	H16	PG2	P12	B15	P12	FMC D26		
DCMI_SDA	PG3	H15	PG3	P13	C14	P13	FMC D27		
FMC_BA0	PG4	H14	PG4	P14	A4	P14	FMC NBL2		
FMC_BA1	PG5	G14	PG5	P15	A3	P15	FMC NBL3		
QSPI_CS	PG6	G15	PG6	P16	A2	P16	FMC D28		
DCMI_PDWN	PG7	F16	PG7	P17	B3	P17	FMC D29		
FMC_SDCLK	PG8	F15	PG8	P18	E4	P18	DCMI_RST		
QSPI_BK2_IO2	PG9	A10	PG9	P19	E2	P19	FMC D30		
	PG10	A9	PG10	P110	F3	P110	FMC D31		
	PG11	B9	PG11	P111	F4	P111			
	PG12	C9	PG12	P112	H1	P112	LCD_HSYNC		
	PG13	D9	PG13	P113	H2	P113	LCD_VSYNC		
QSPI_BK2_IO3	PG14	D8	PG14	P114	H3	P114	LCD_PCLK		
FMC_SDNCAS	PG15	D6	PG15	P115	P5	P115	LCD_R0		
OSC_IN	PH0	J2	PH0-OSC_IN	P10	N6	P10	LCD_R1		
OSC_OUT	PH1	J1	PH1-OSC_OUT	P11	P6	P11	LCD_R2		
QSPI_BK2_IO0	PH2	N2	PH2	P12	T6	P12	LCD_R3		
QSPI_BK2_IO1	PH3	P2	PH3	P13	U6	P13	LCD_R4		
	PH4	P3	PH4	P14	U7	P14	LCD_R5		
LCD_RST	PH5	P4	PH5	P15	R12	P15	LCD_R6		
FMC_SDNE1	PH6	T11	PH6	P16	N15	P16	LCD_R7		
FMC_SDCKE1	PH7	U13	PH7	P17	N14	P17	LCD_G0		
FMC_D16	PH8	T13	PH8	P18	N13	P18	LCD_G1		
FMC_D17	PH9	R13	PH9	P19	M14	P19	LCD_G2		
FMC_D18	PH10	P13	PH10	P110	L14	P110	LCD_G3		
FMC_D19	PH11	P14	PH11	P111	K14	P111	LCD_G4		
FMC_D20	PH12	R14	PH12	P112	D11	P112	LCD_B0		
FMC_D21	PH13	D16	PH13	P113	E10	P113	LCD_B1		
FMC_D22	PH14	B17	PH14	P114	D10	P114	LCD_B2		
FMC_D23	PH15	B16	PH15	P115	B10	P115	LCD_B3		
				PK0	J14	PK0	LCD_G5		
				PK1	J15	PK1	LCD_G6		
				PK2	H17	PK2	LCD_G7		
				PK3	C8	PK3	LCD_B4		
				PK4	B8	PK4	LCD_B5		
				PK5	A8	PK5	LCD_B6		
				PK6	C7	PK6	LCD_B7		
				PK7	D7	PK7	LCD_DEN		

STM32H743XIH6

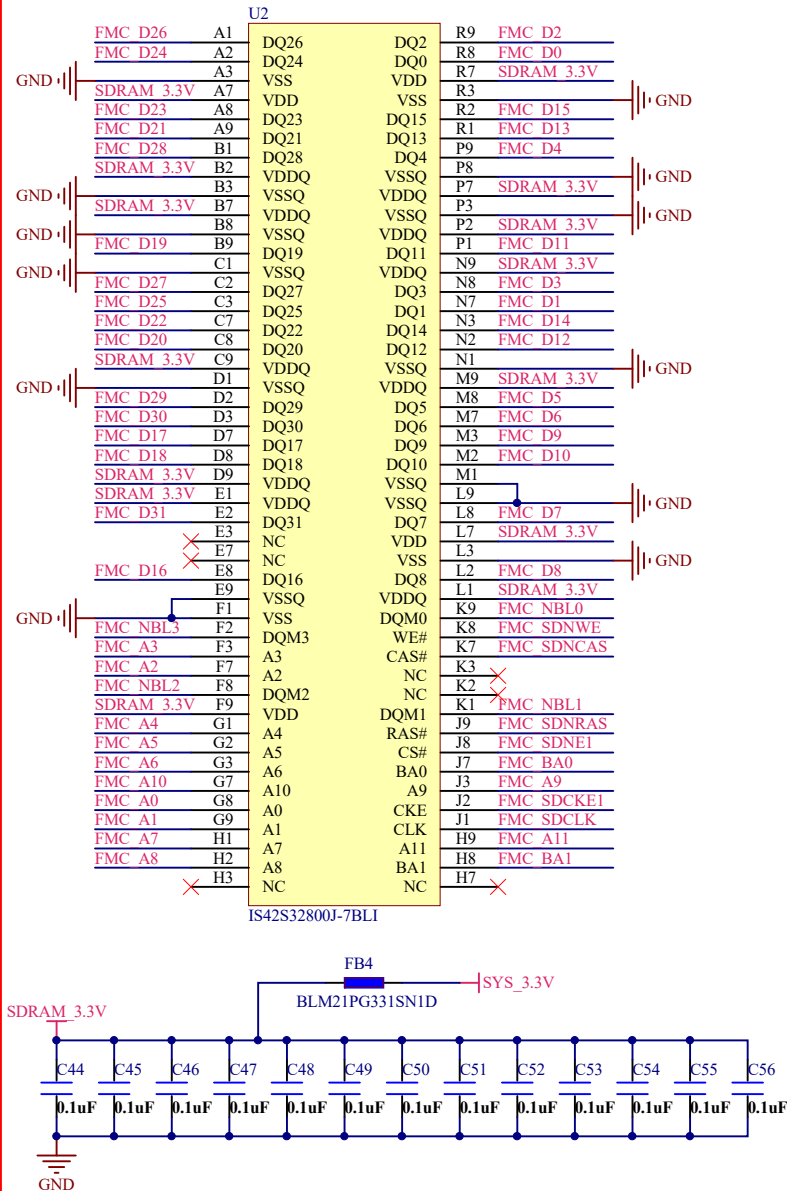
项目名称	STM32H743XIH6 Core Board V1.1	
设计时间	2025-9-23	Page 1 of 6
公司名称	东莞市慧勤智远科技有限公司	

# MCU

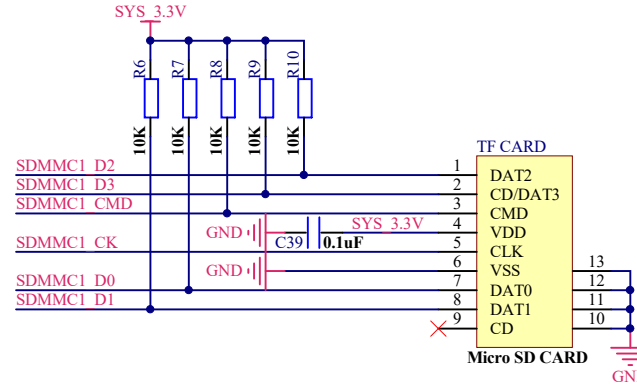


项目名称	STM32H743XIH6 Core Board V1.1	
设计时间	2025-9-23	Page 2 of 6
公司名称	东莞市慧勤智远科技有限公司	

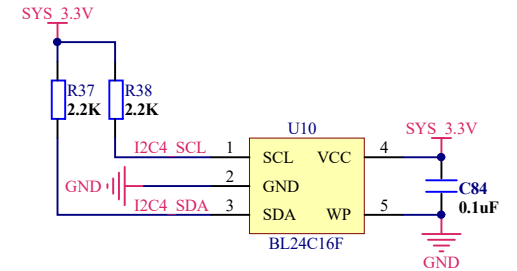
# SDRAM



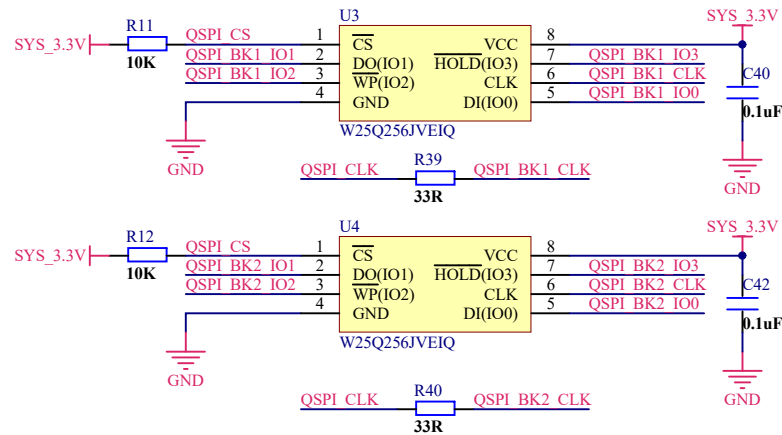
# TF CARD



# EEPROM



# QSPI FLASH

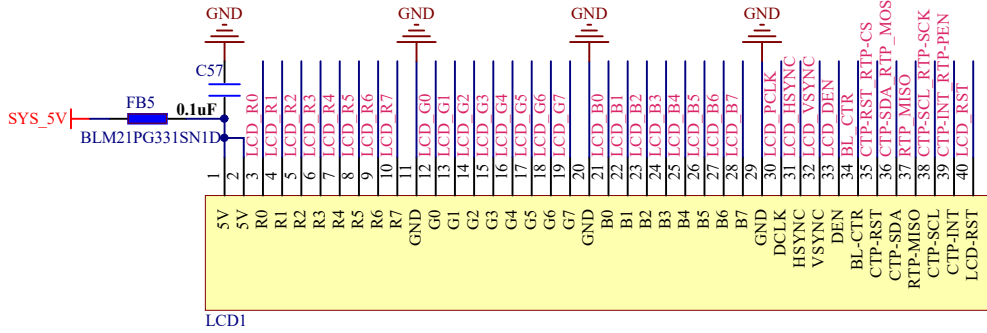


项目名称	STM32H743XIH6 Core Board V1.1	
设计时间	2025-9-23	Page 3 of 6
公司名称	东莞市慧勤智远科技有限公司	

# RGB

## TP pin define

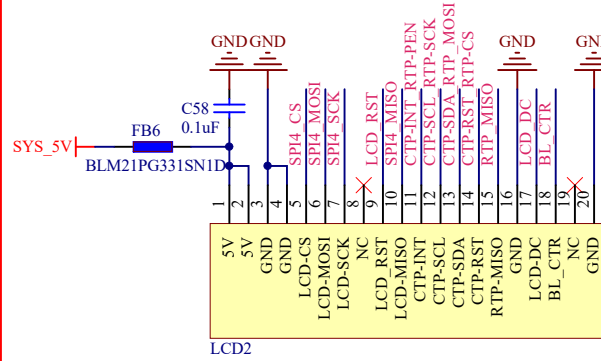
PIN NO.	CTP	RTP
PIN35	RST	CS
PIN36	SDA	MOSI
PIN37	NC	MISO
PIN38	SCL	SCK
PIN39	INT	PEN



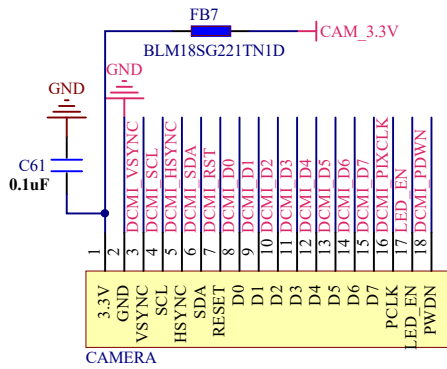
# SP I

## TP pin define

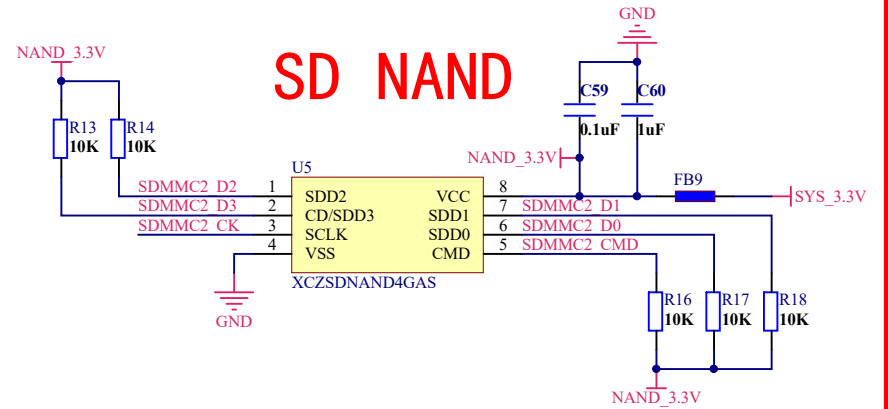
PIN NO.	CTP	RTP
PIN11	INT	PEN
PIN12	SCL	SCK
PIN13	SDA	MOSI
PIN14	RST	CS
PIN15	NC	MISO



# DCM I



# SD NAND



项目名称

STM32H743XIH6 Core Board V1.1

## 设计时间

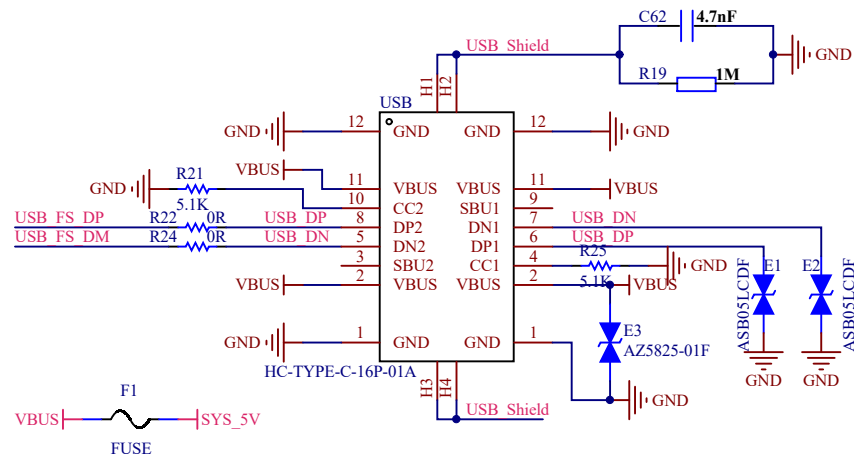
2025-9-23

Page 4 of 6

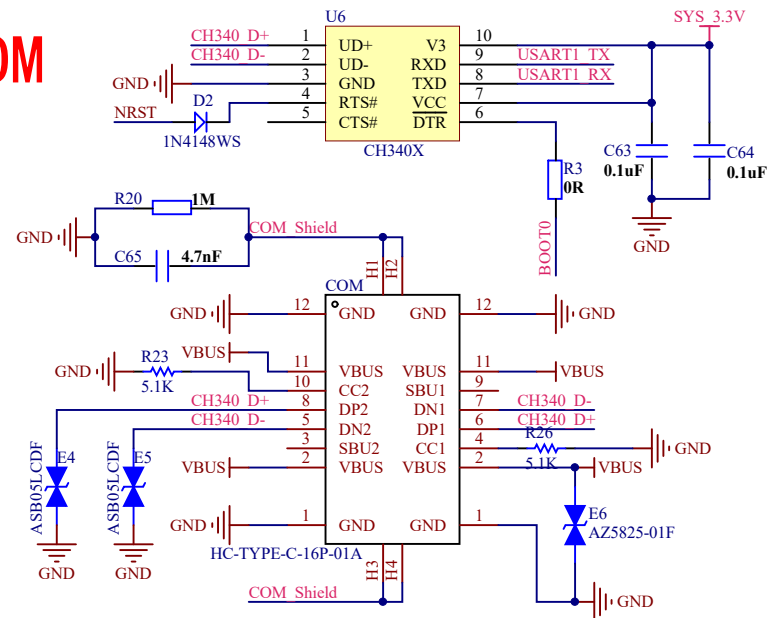
公司名称

东莞市慧勤智远科技有限公司

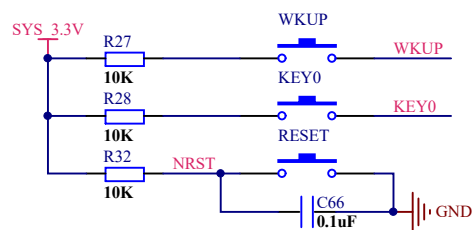
## USB OTG



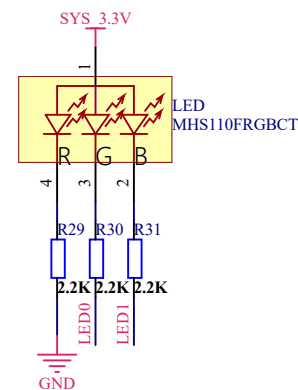
## COM



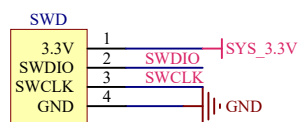
## KEY



## LED

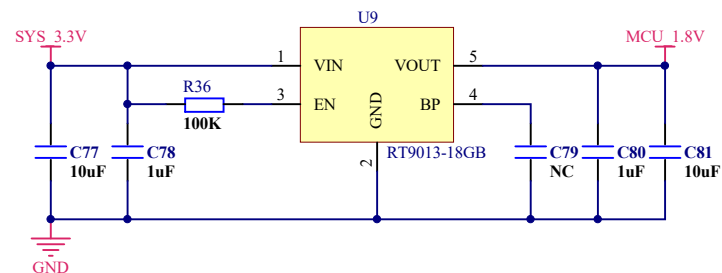
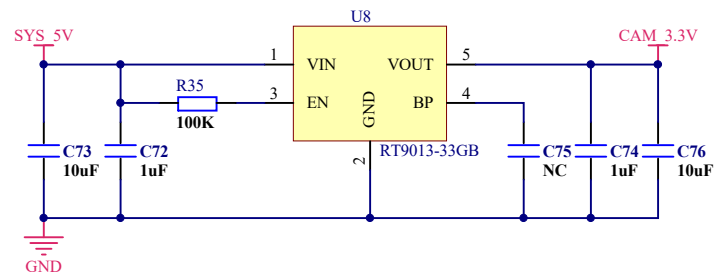
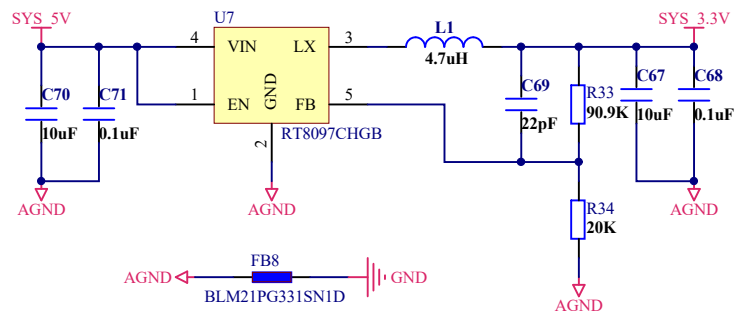


## SWD

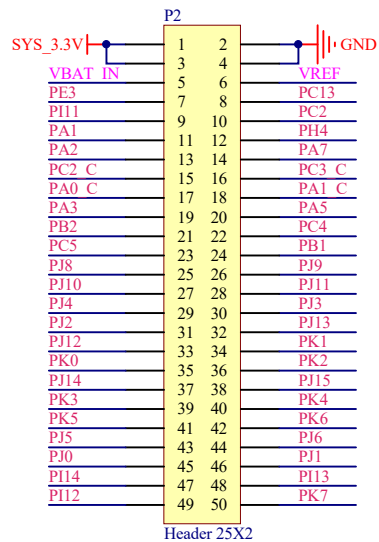
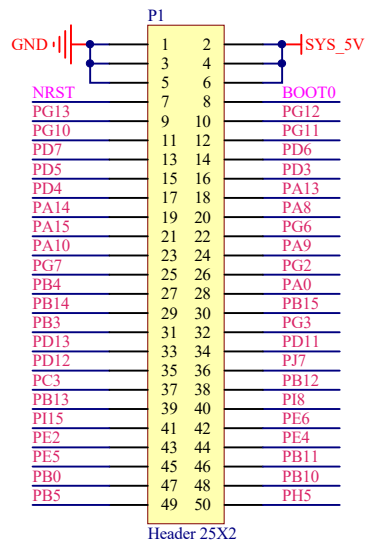


项目名称	STM32H743XIH6 Core Board V1.1	
设计时间	2025-9-23	Page 5 of 6
公司名称	东莞市慧勤智远科技有限公司	

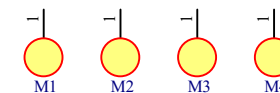
## POWER



## GPIO



## M3 HOLE



项目名称	STM32H743XIH6 Core Board V1.1	
设计时间	2025-9-23	Page 6 of 6
公司名称	东莞市慧勤智远科技有限公司	