

STM32H7xxx-Disco

MB1381

Revision B-02: correct pin name on CN6 on Arduino connector sheet.

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Top
TOP.SchDoc



Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

▲ Notes to generate the
board layout.

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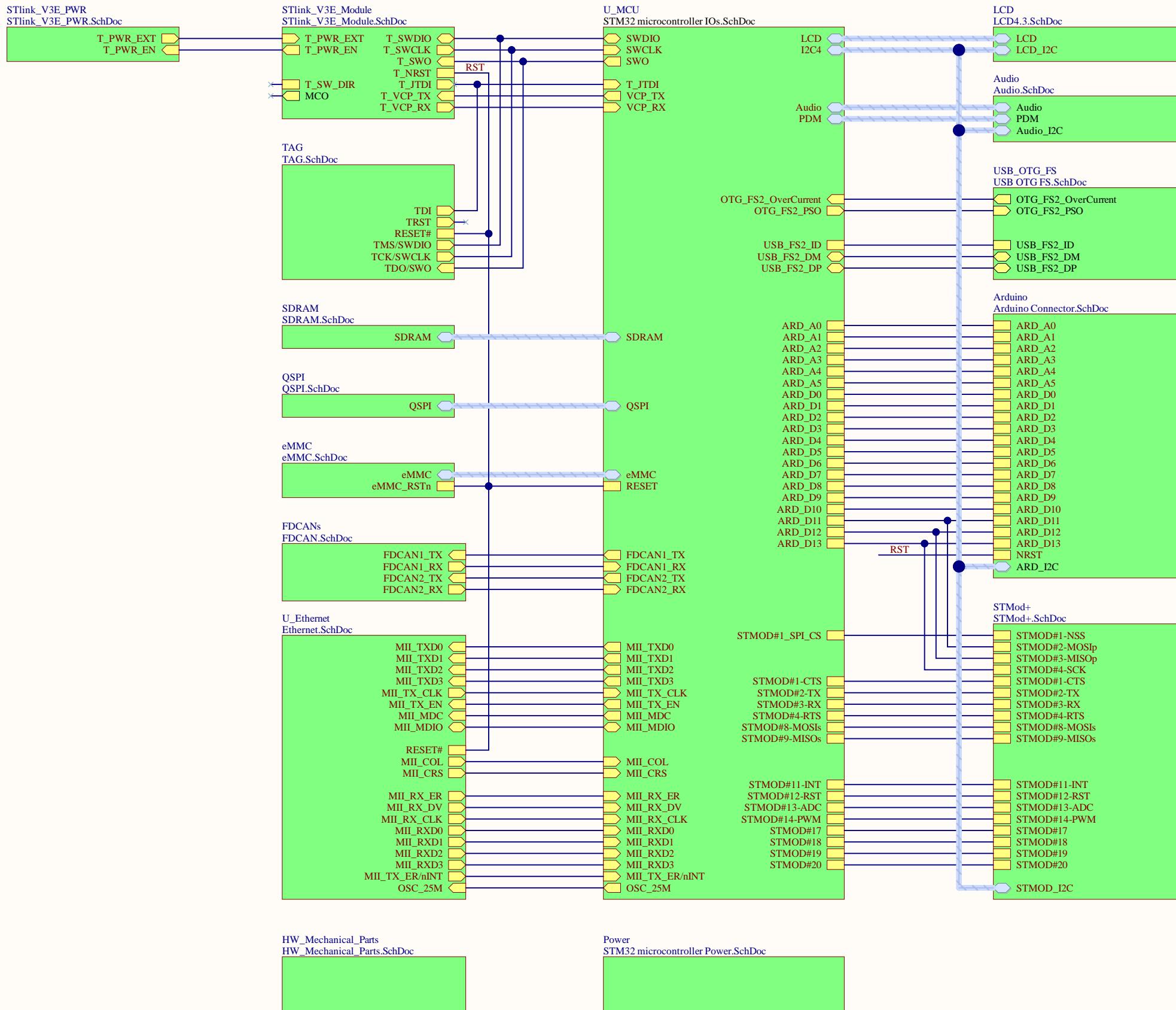
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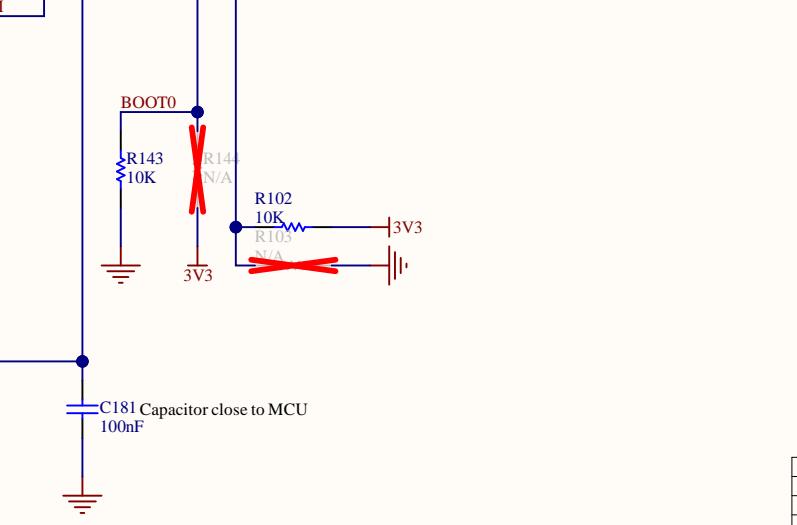
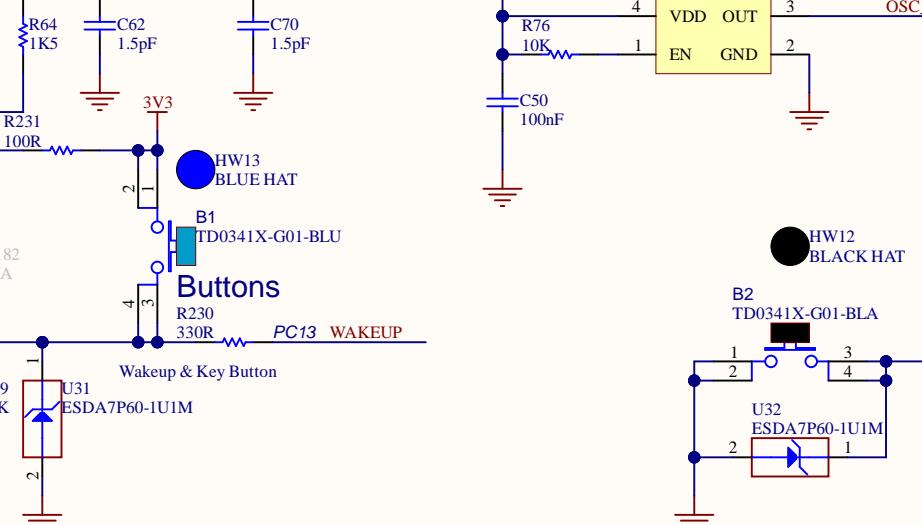
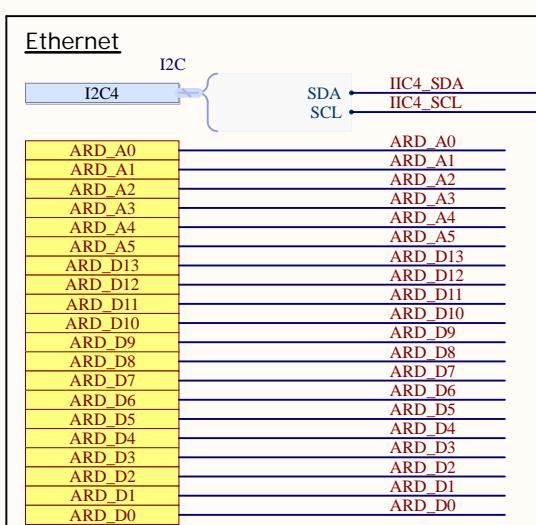
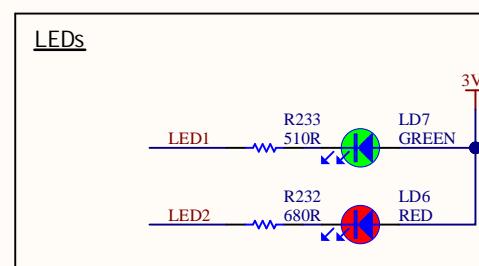
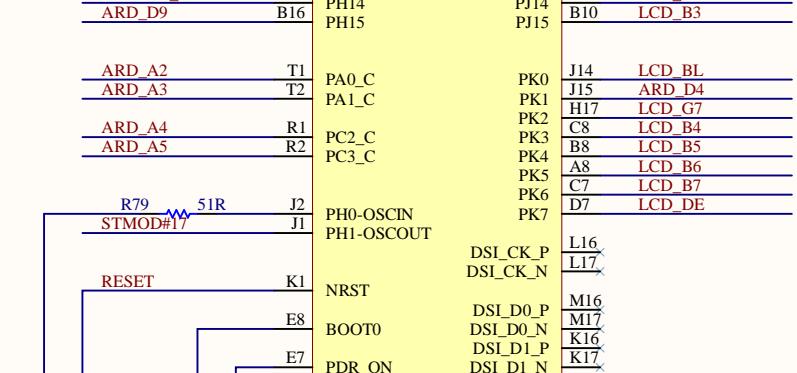
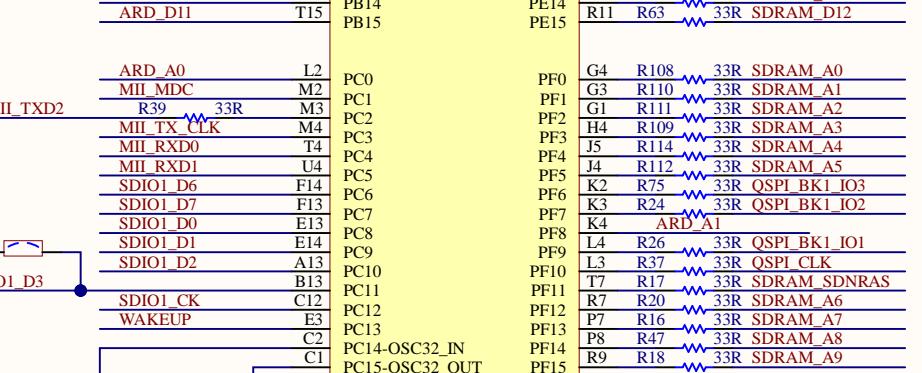
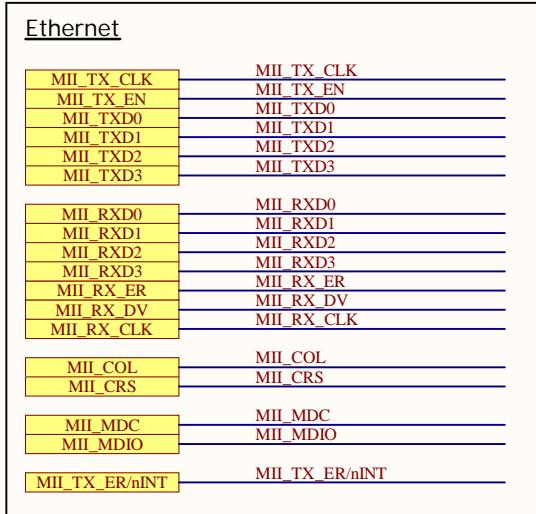
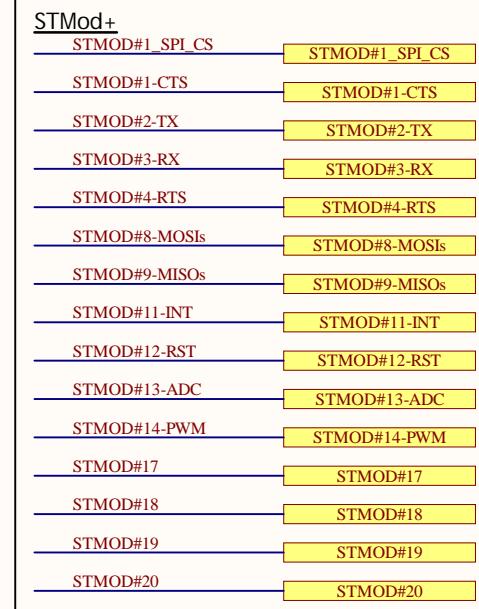
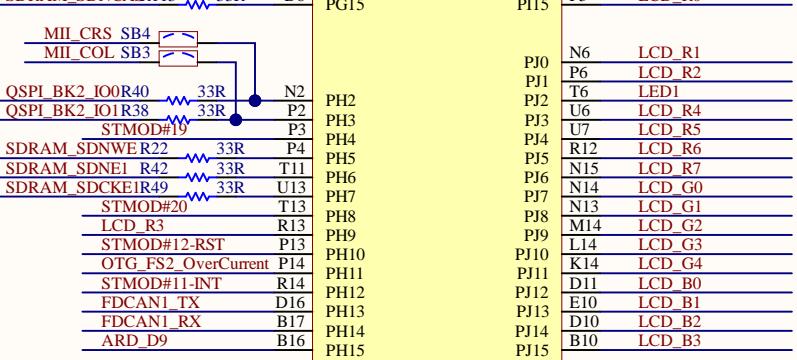
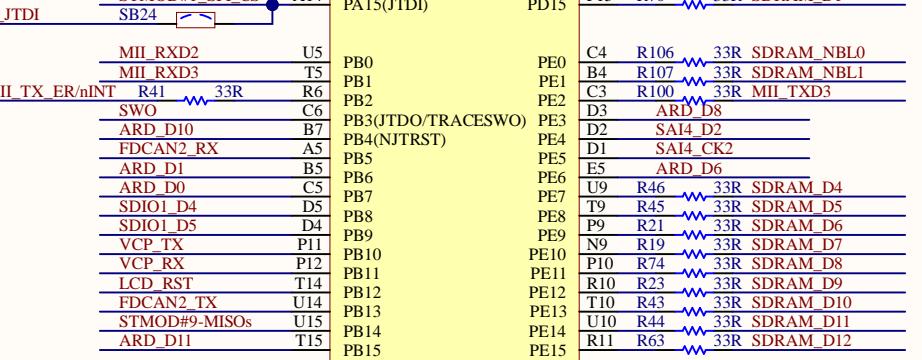
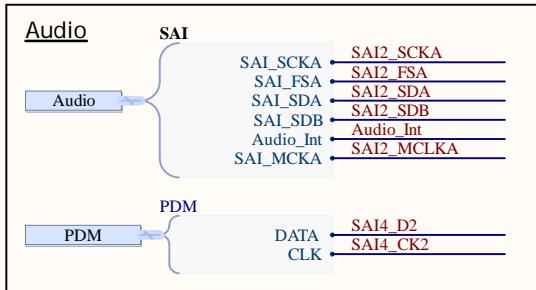
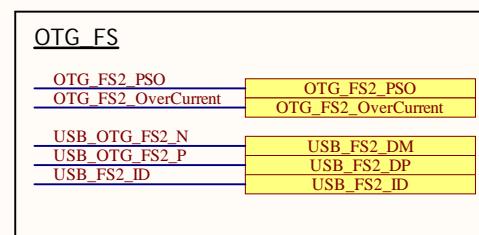
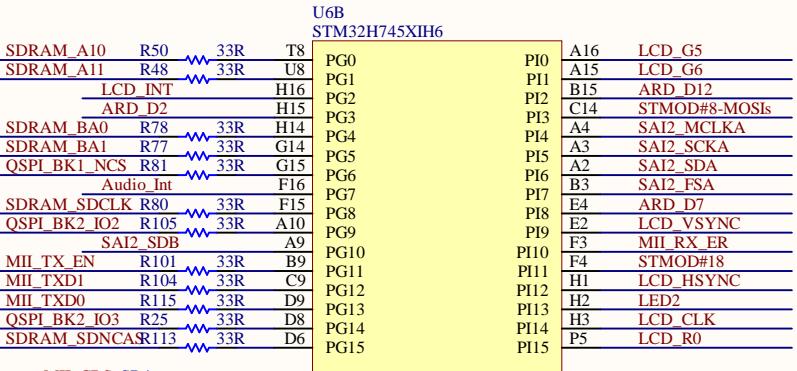
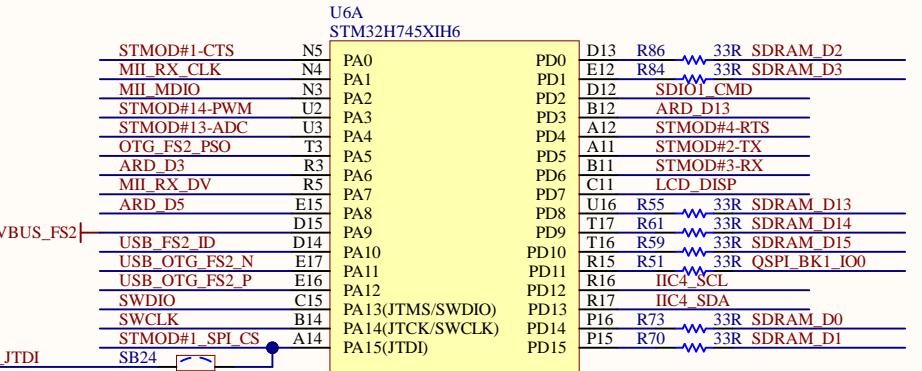
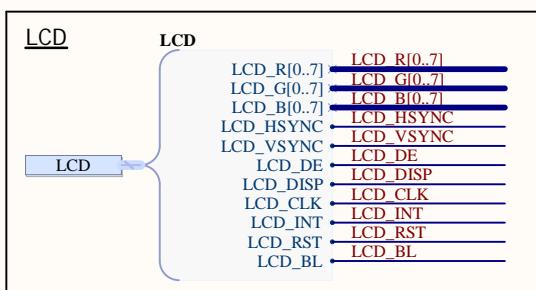
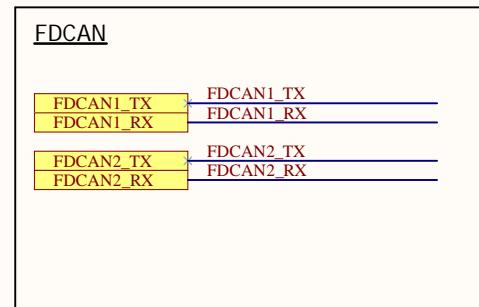
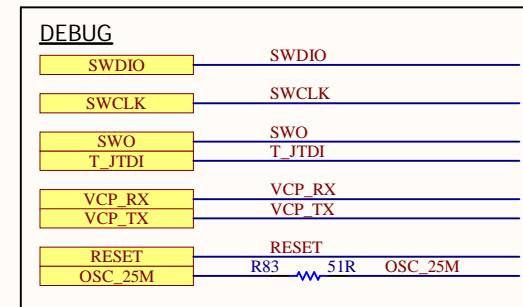
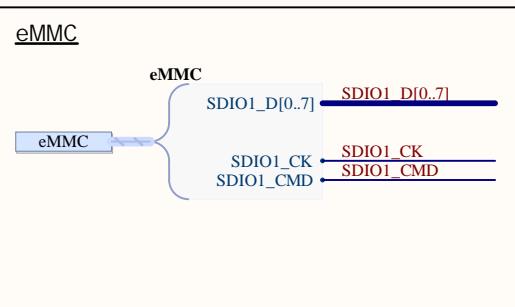
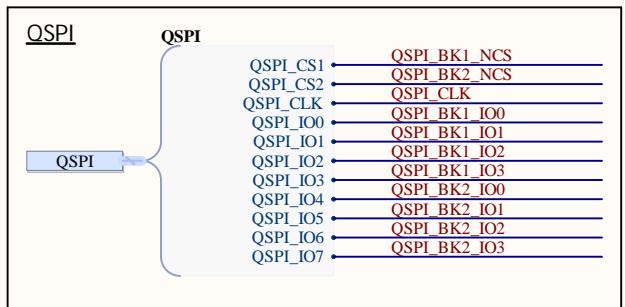
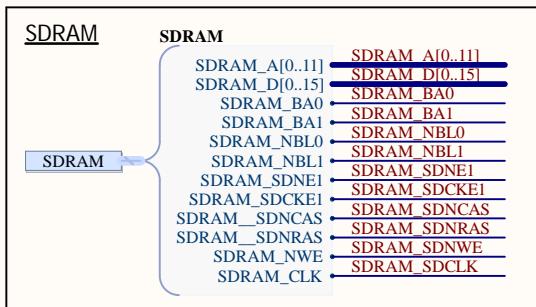
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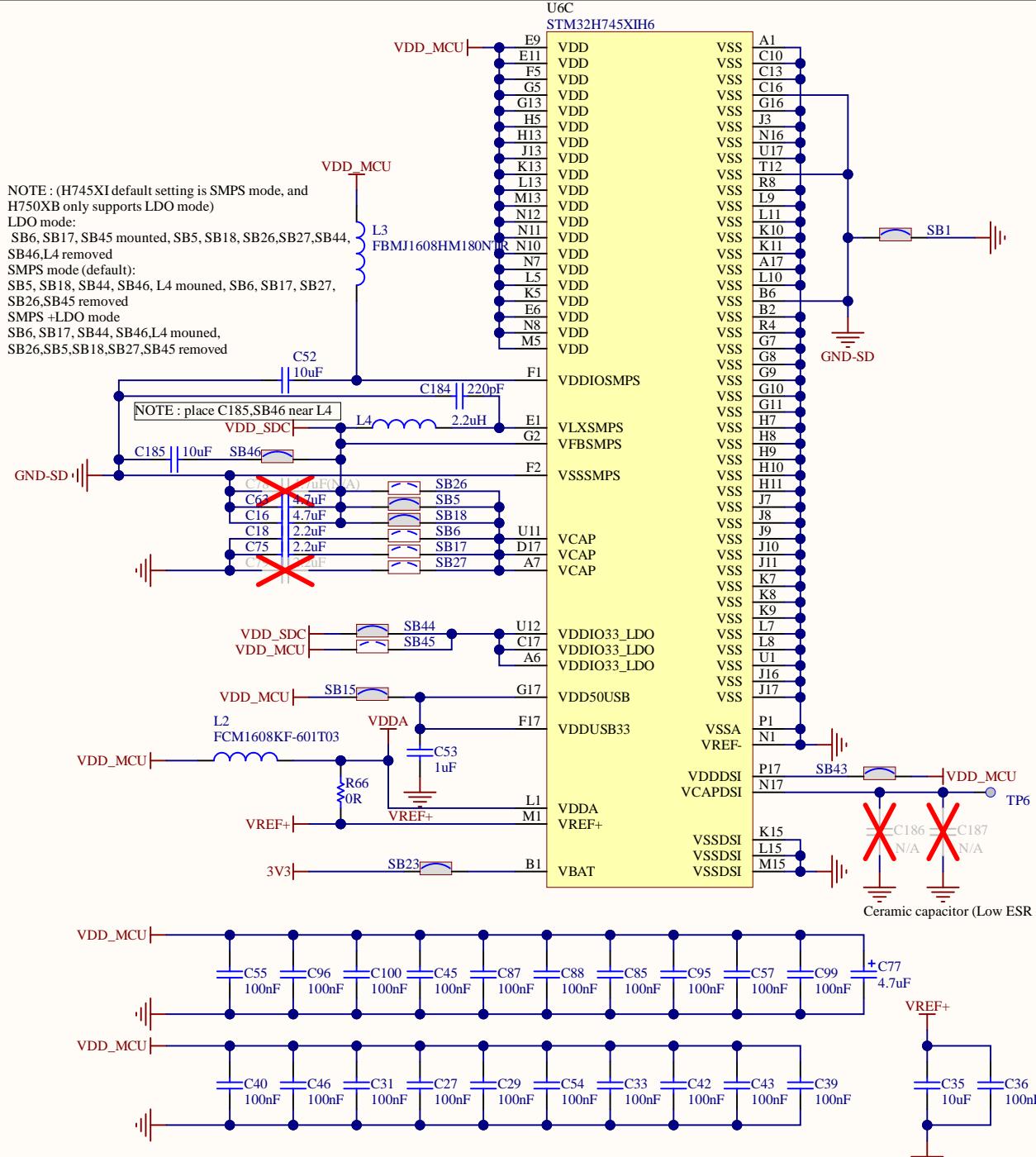
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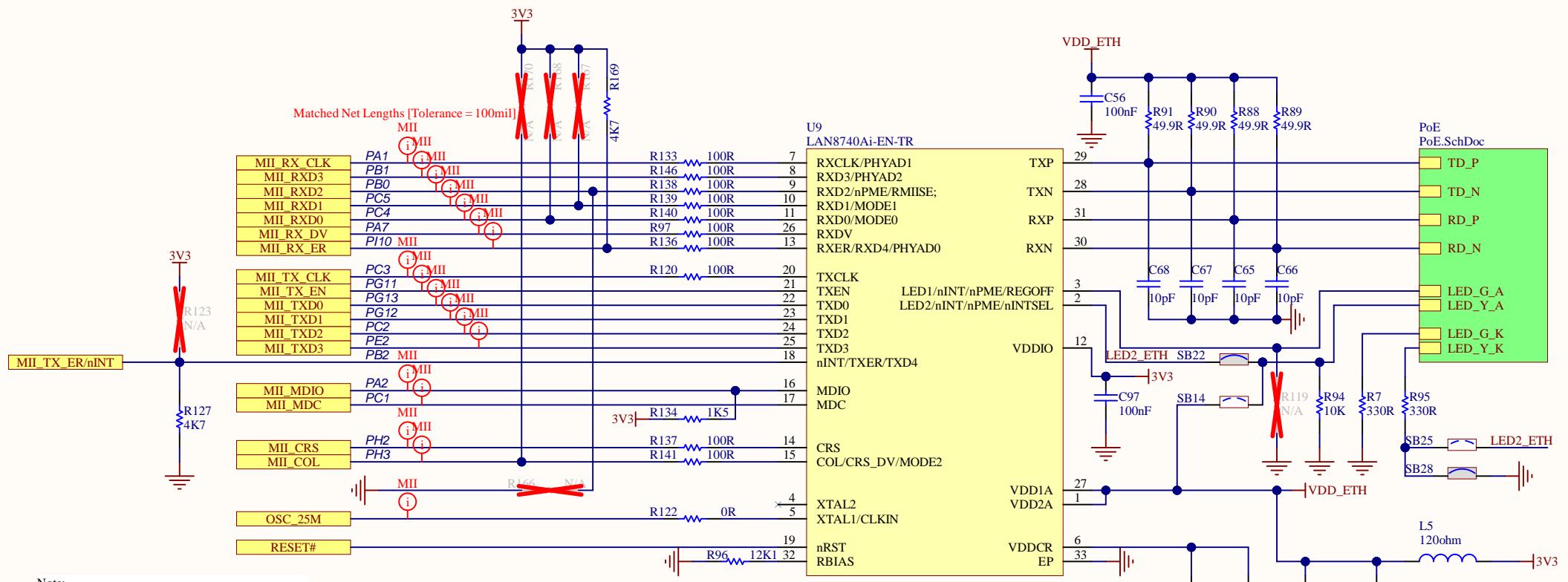
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Variant: H745XI			
Revision: B-03		Reference: MB1381	
Size: A4	Date: 02-JUN-22	Sheet: 1 of 19	







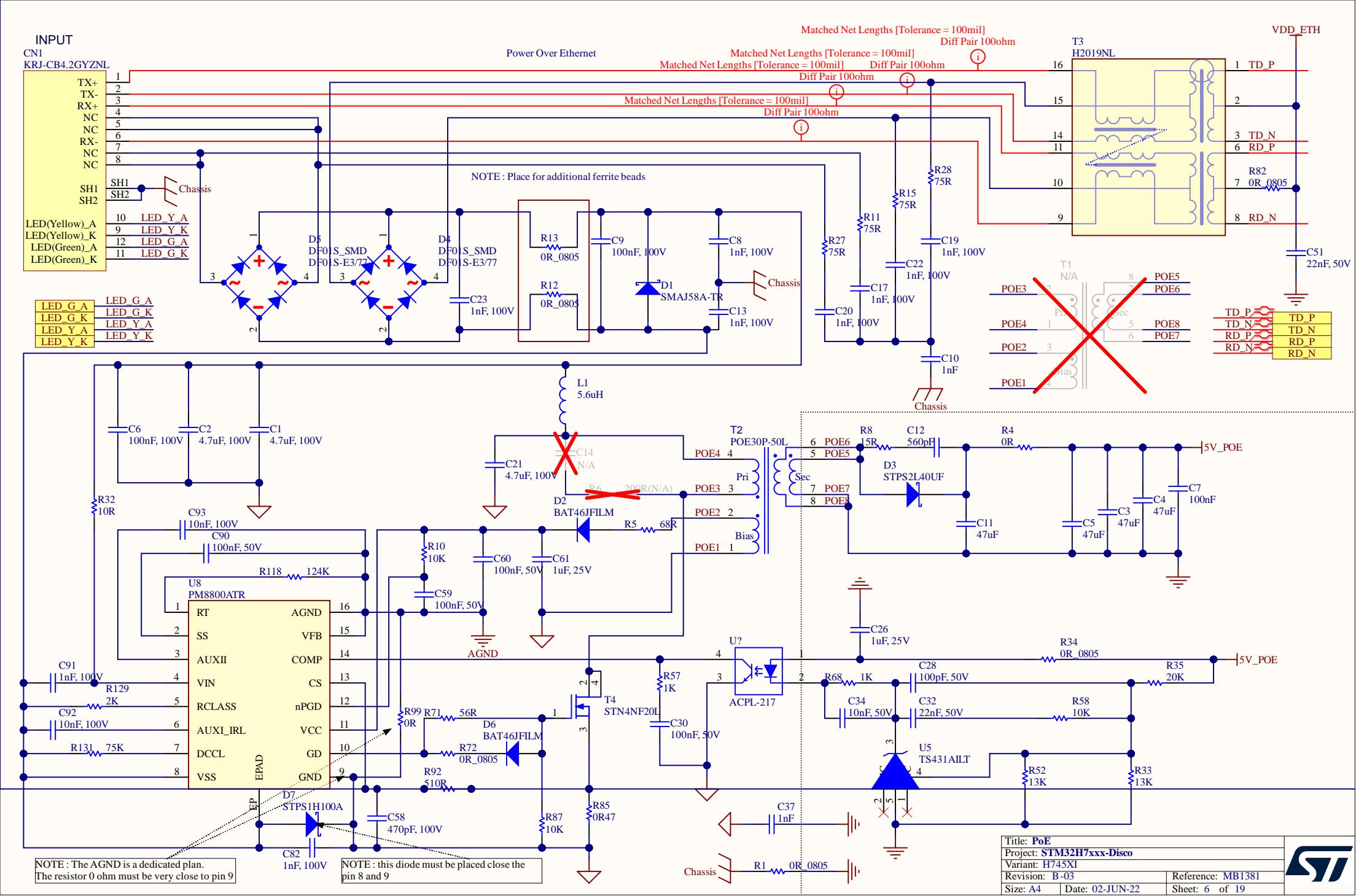


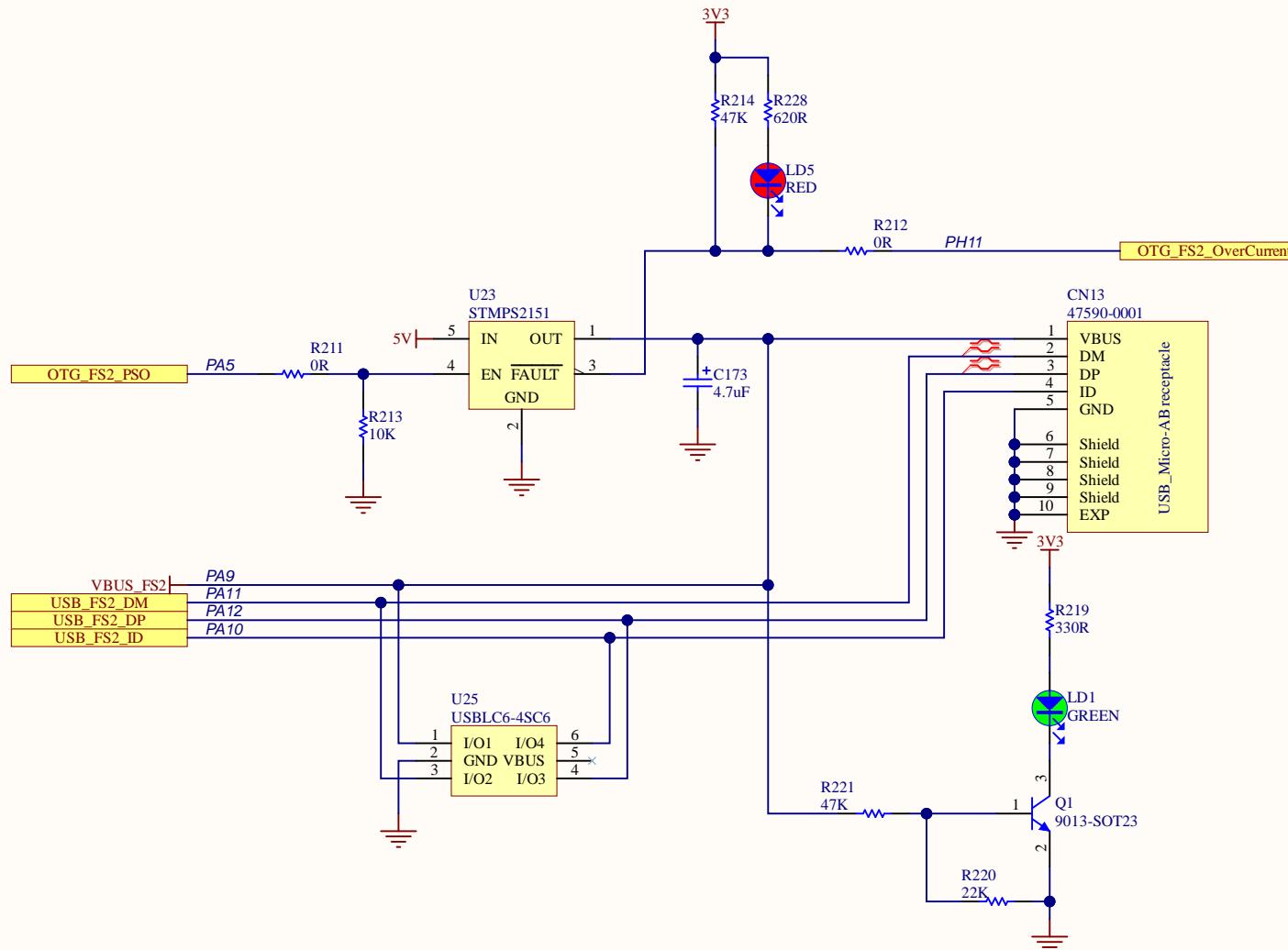


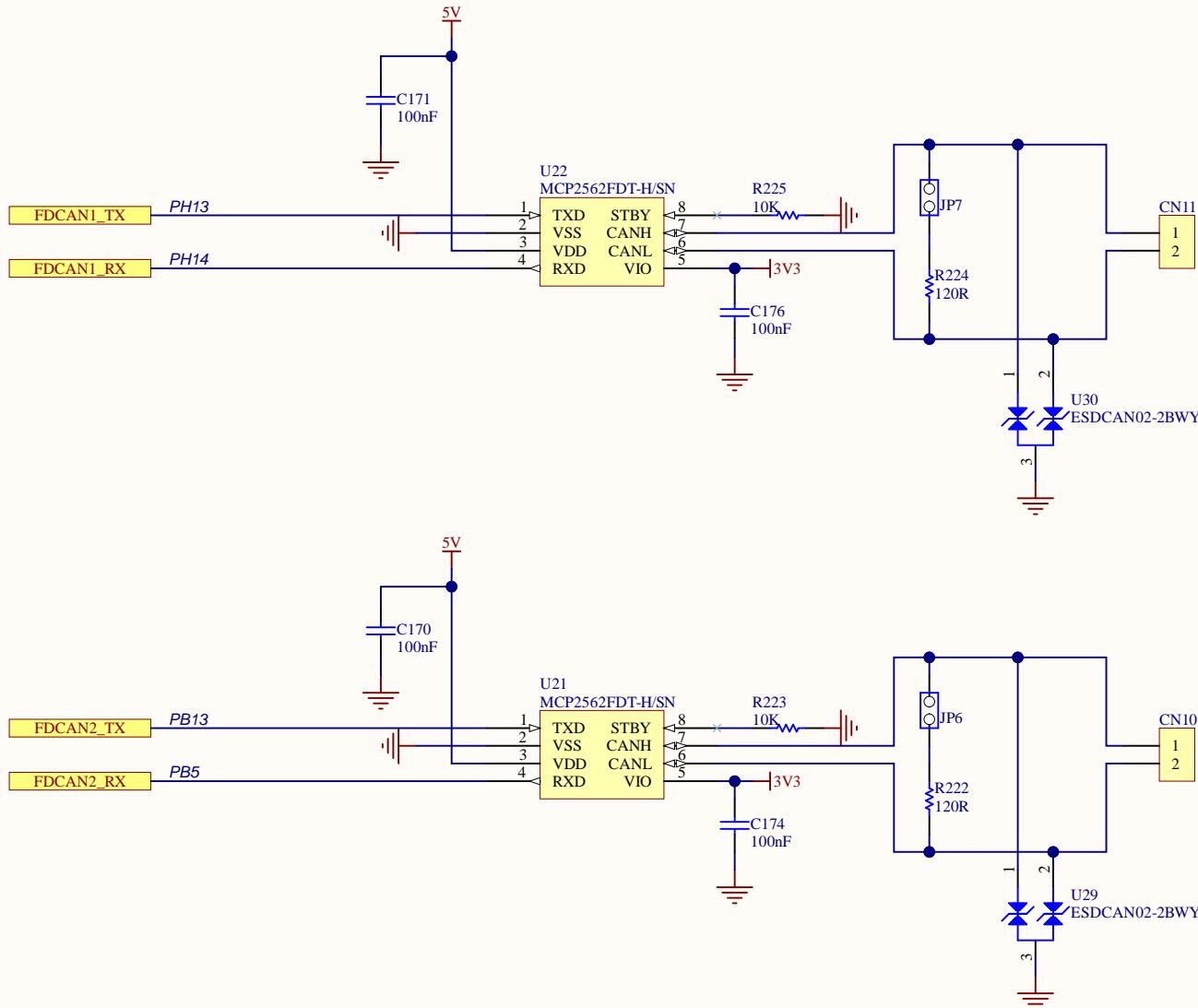
Note:
Remove R127 and keep R123 when nINT function is enabled. TXER is disabled
Remove R123 and keep R127 when nINT function is disabled. TXER is enabled

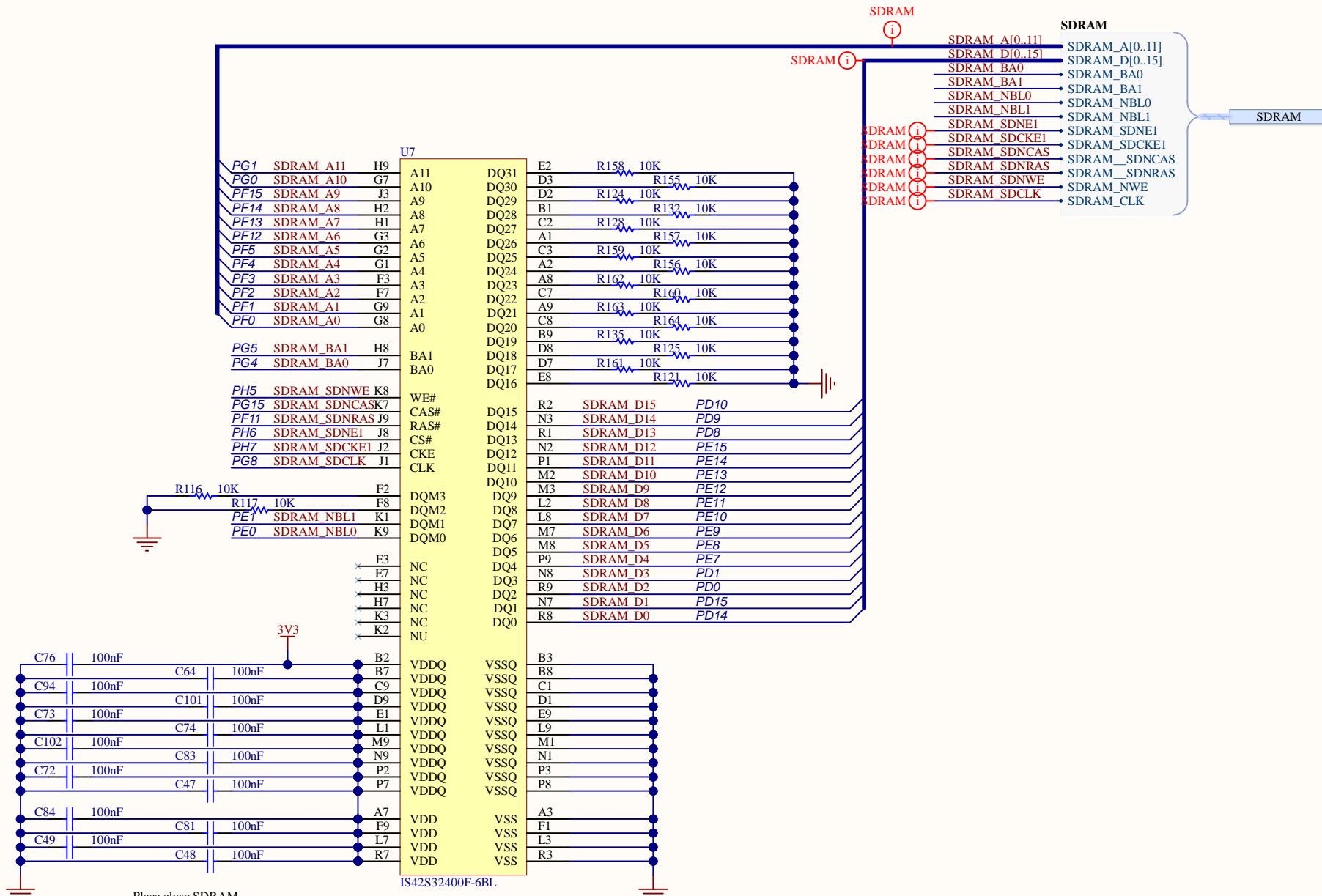
Note:
keep R94 to enable the TXER function, and close
SB22,SB28,open SB14,SB25 for LED2
Remove R94 and SB14 to enable nINT function, and
close SB14,SB25, open SB22,SB28 for LED2

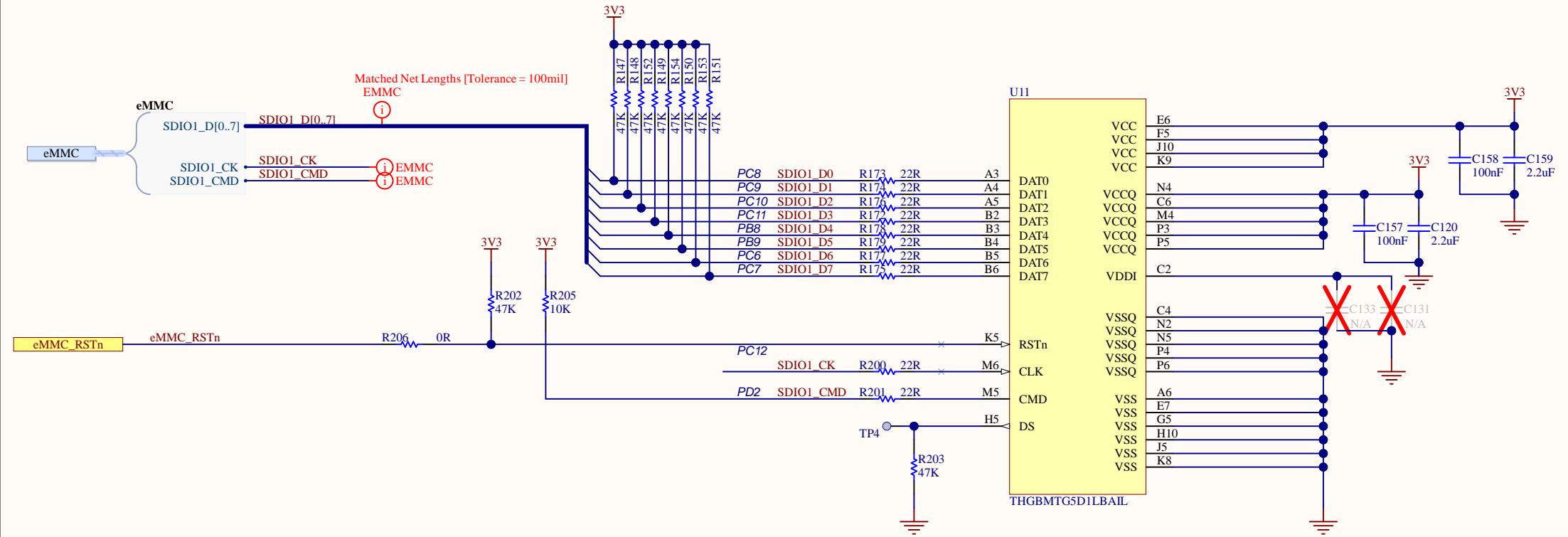
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Variant: H745X1		
Revision: B-03	Reference: MB1381	
Size: A4	Date: 02-JUN-22	Sheet: 5 of 19







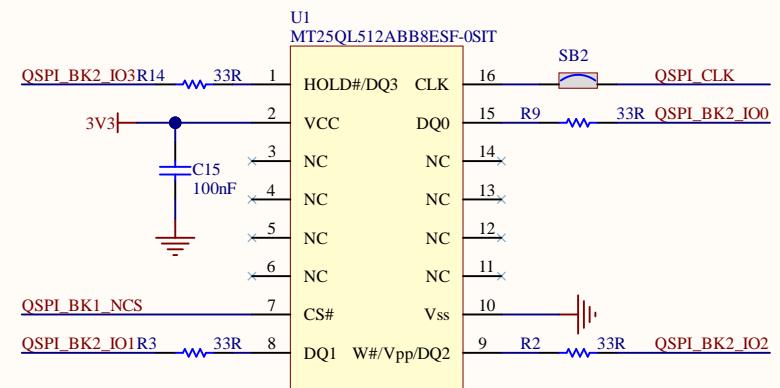
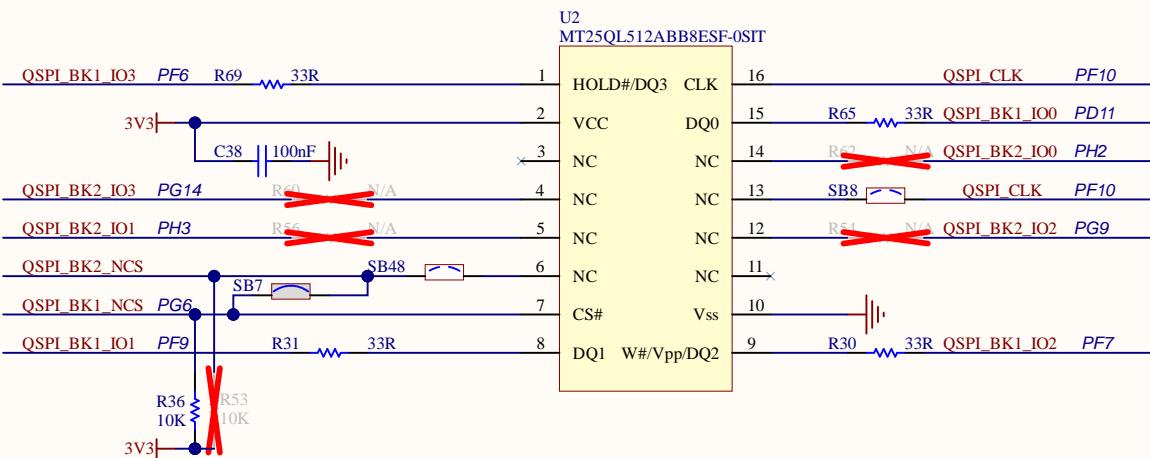
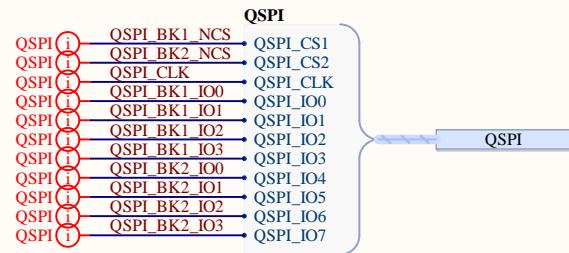




When change to THGBMDG5D1LBAIL, mount C131/C133

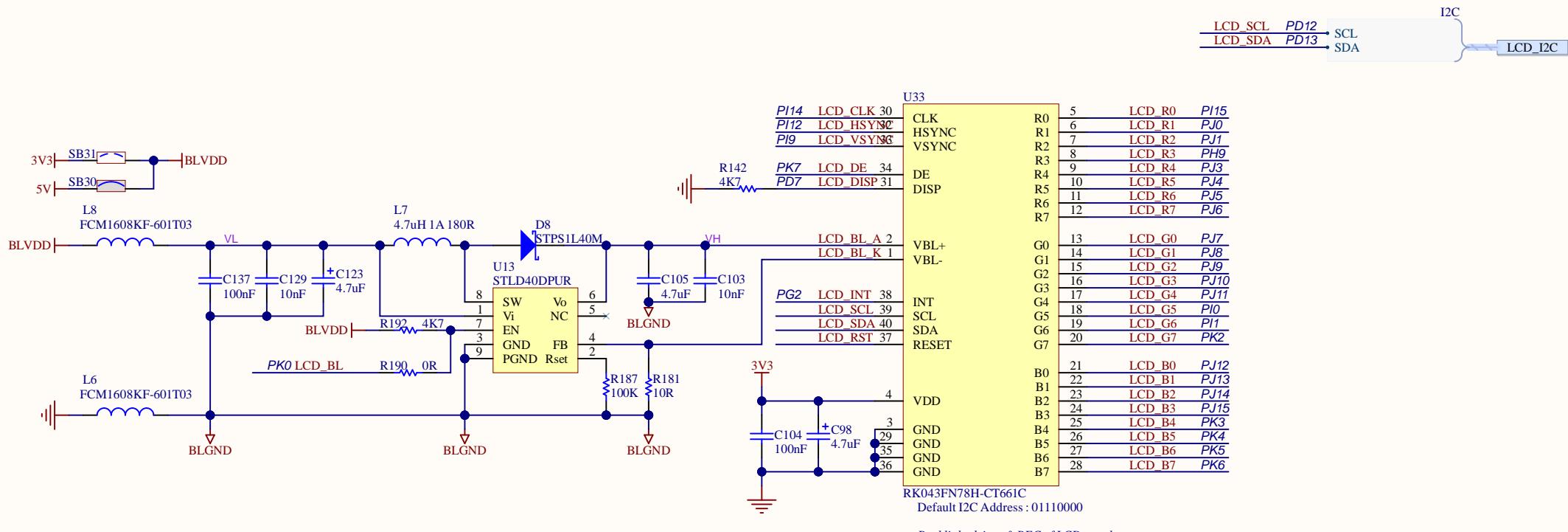
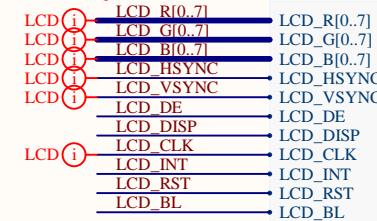
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Variant: H745XI		
Revision: B-03		
Size: A4	Date: 02-JUN-22	Reference: MB138I
Sheet: 10 of 19		

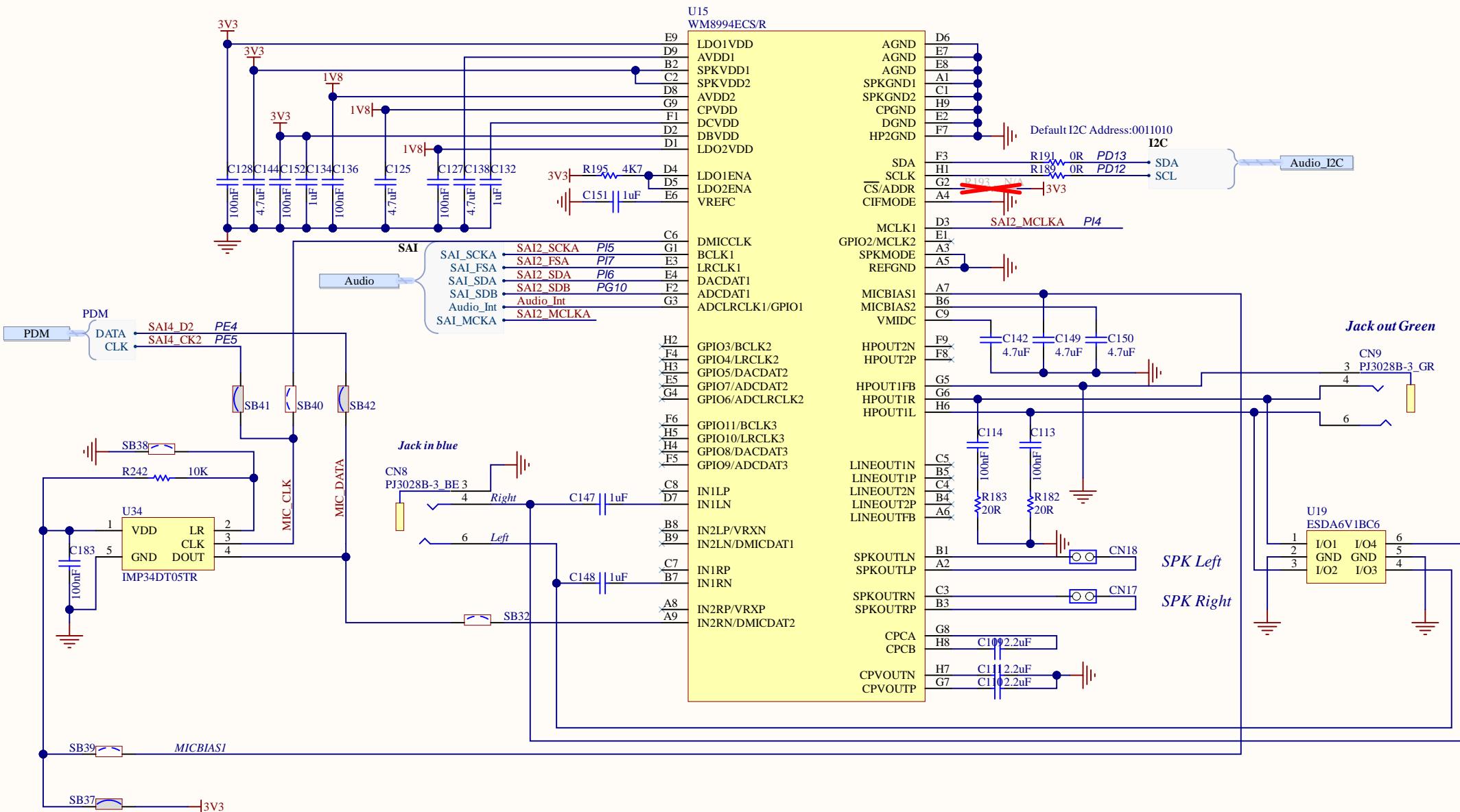
Matched Net Lengths [Tolerance = 100mil]

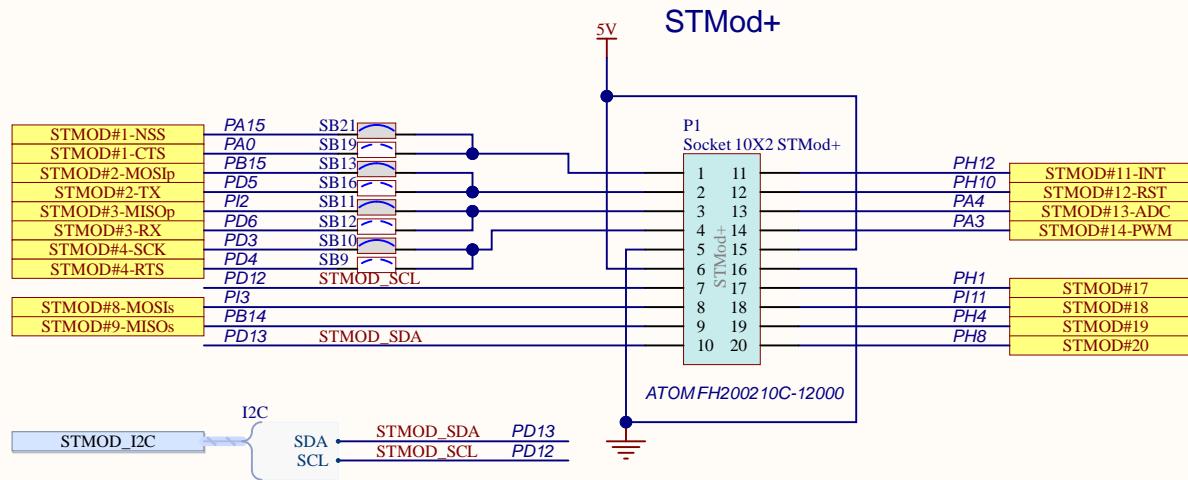


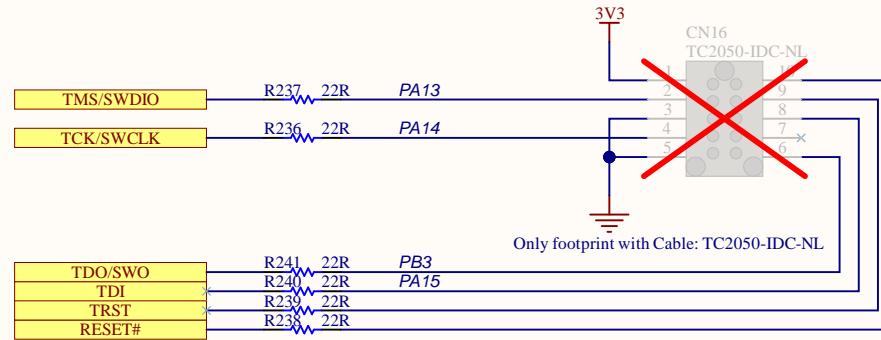
Twin Quad SPI Flash solutions:

One Twin Quad SPI Flash	U2:MT25TL01GHBB8ESF-0SIT	U1:NA	R53/R62/R54/R56/R60/SB8/SB48 ON	R14/R3/R2/R9./SB2./C15 OFF
Two Quad SPI Flash	U1:MT25QL512ABB8ESF-0SIT	U2:MT25QL512ABB8ESF-0SIT	R14/R3/R2/R9./SB2./C15 ON	R53/R62/R54/R56/R60/SB8/SB48 OFF

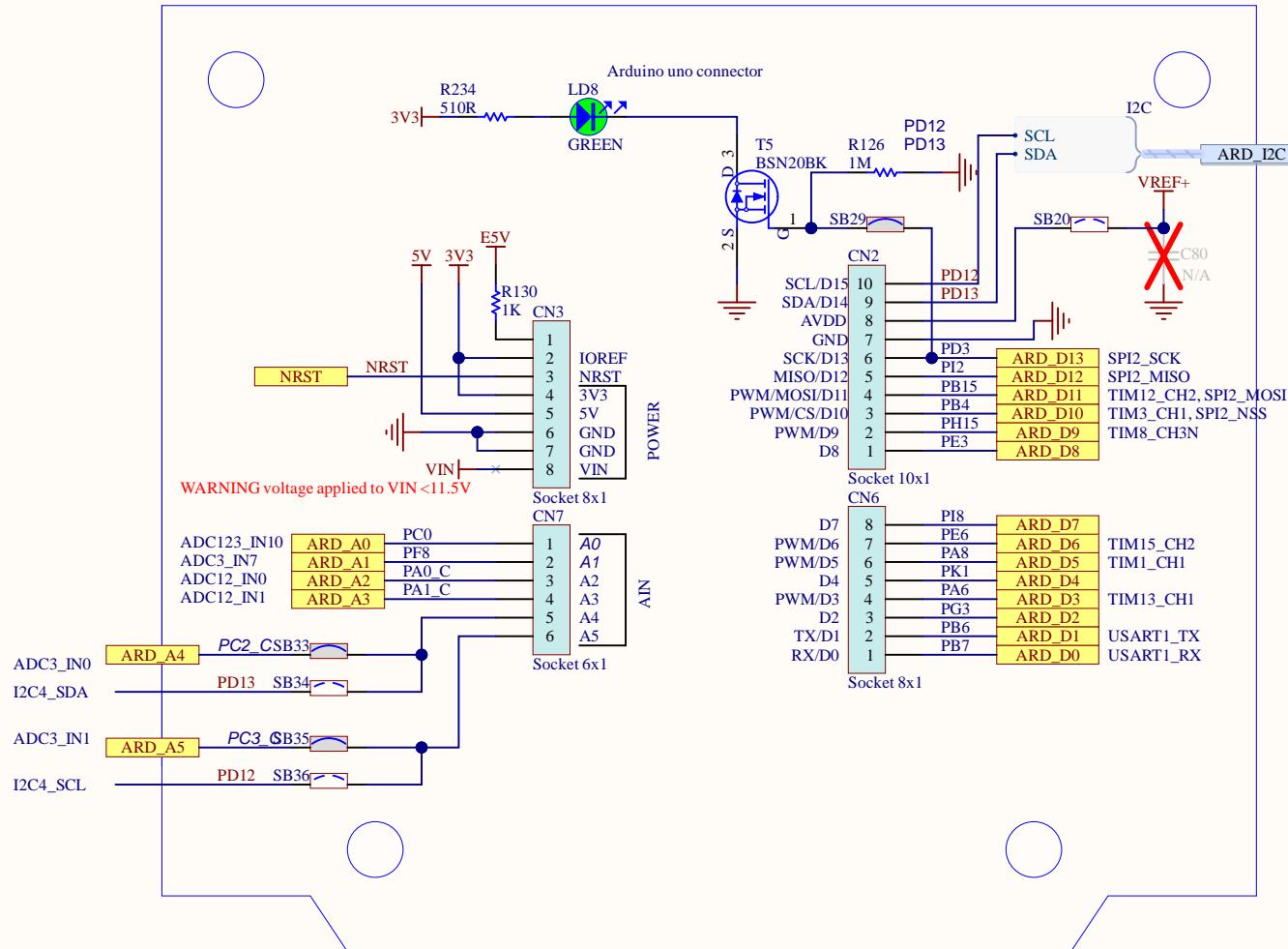


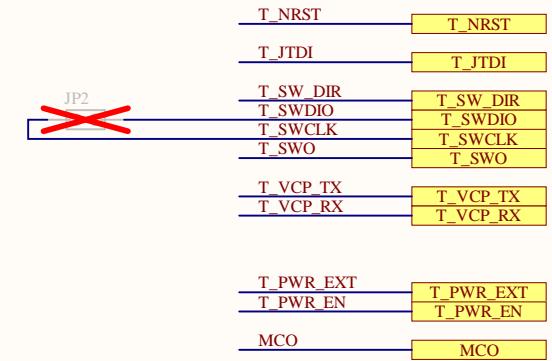
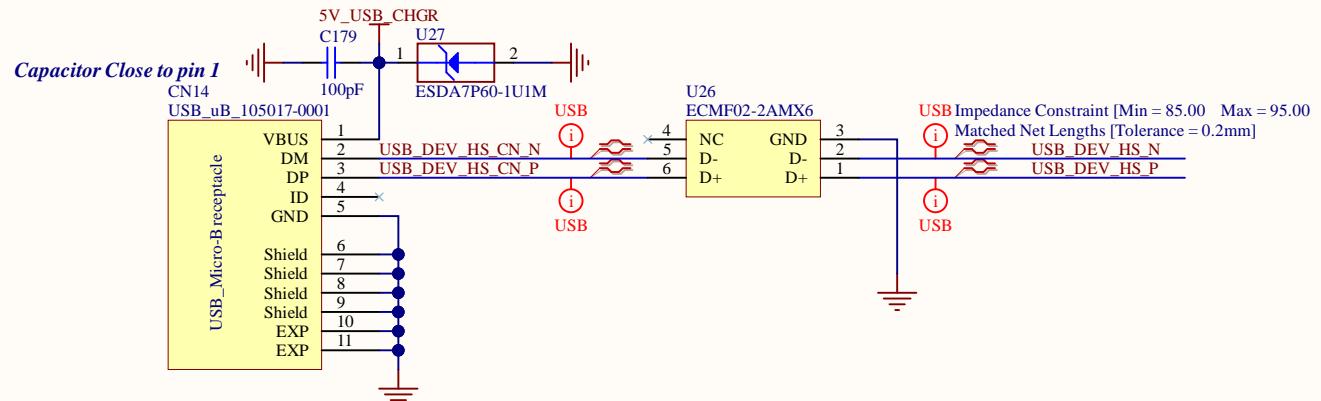






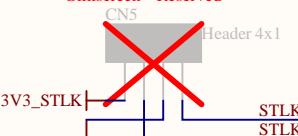
Place this connector close to STDC, they will share the same ESD components



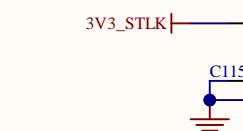
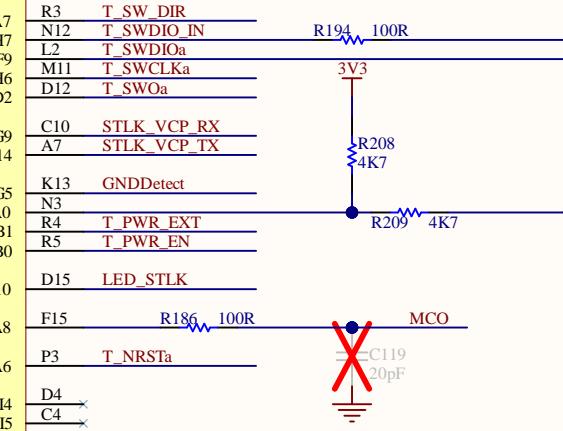
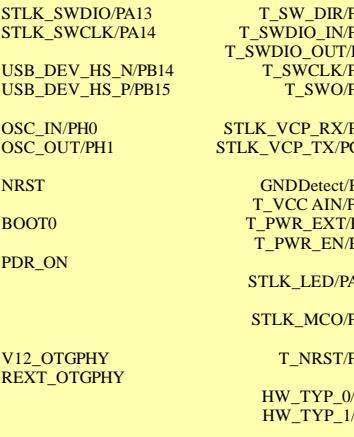


Must be on a border or the PCB.

Silkscreen = Reserved

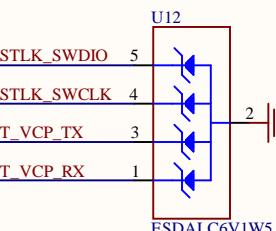
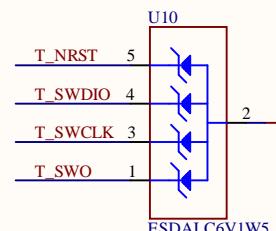
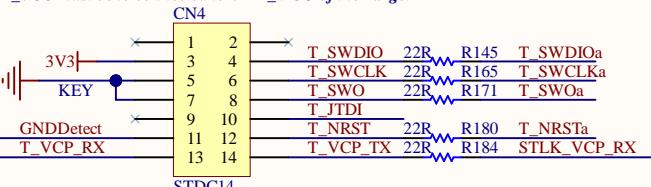


U16A
ST-LINKV3E_SWD



STDC14 Receiver

T_VCC must be connected to VDD_MCU of the Target



Title: ST-LINK/V3E-SWD Module

Project: STM32H7xxx-Disco

Variant: H745XI

Revision: B-03

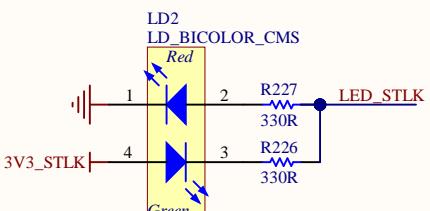
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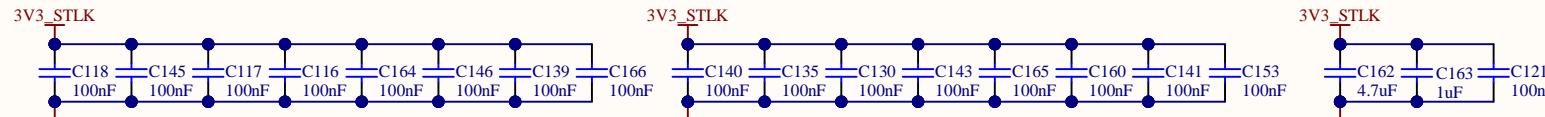
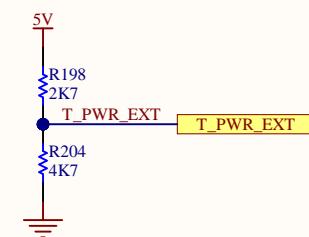
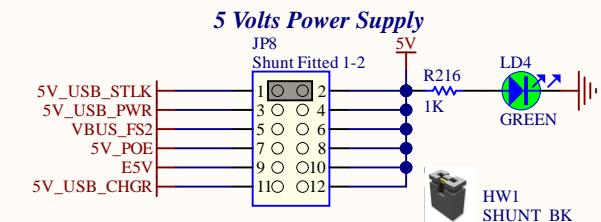
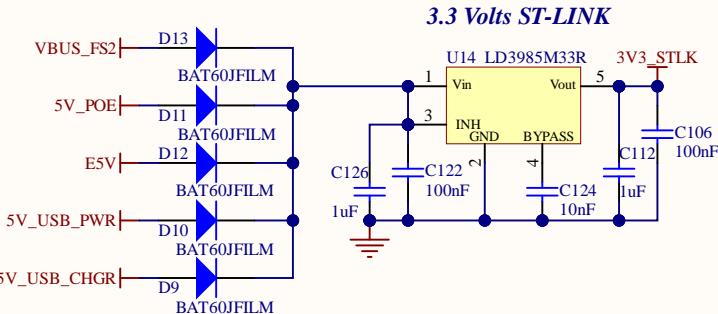
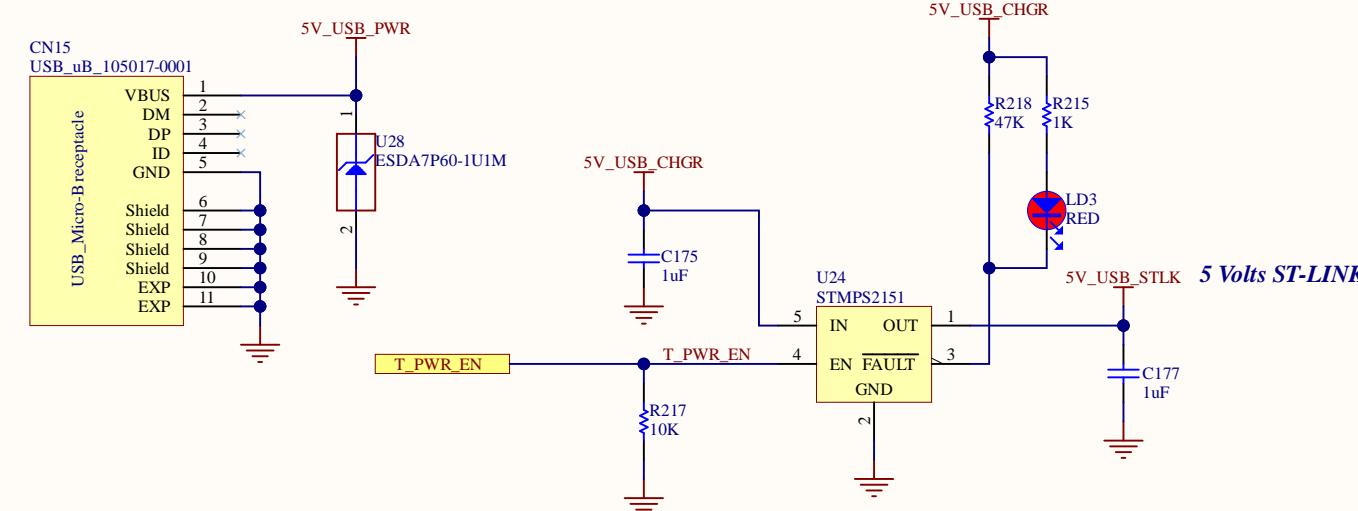
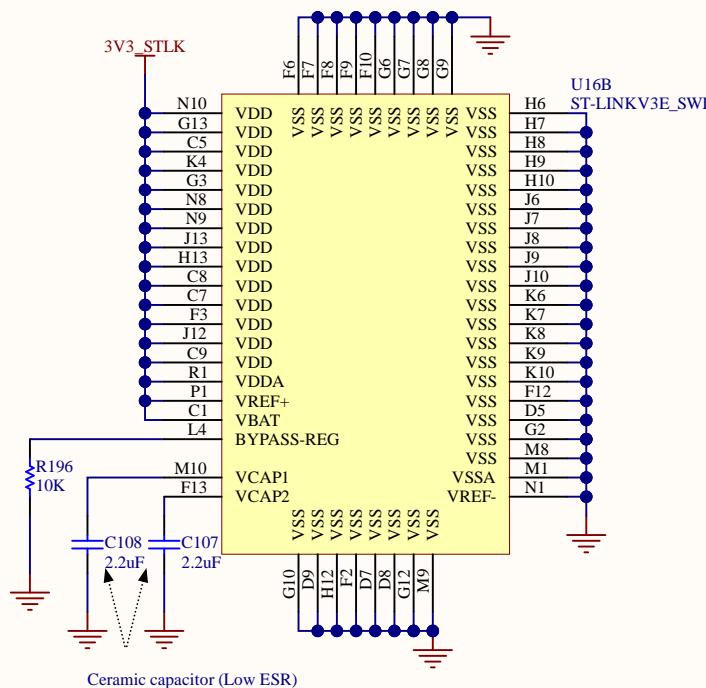
Size: A4

Date: 02-JUN-22



Sheet: 17 of 19





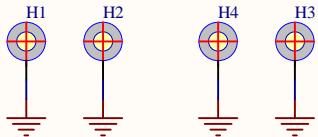
HW Mechanical parts

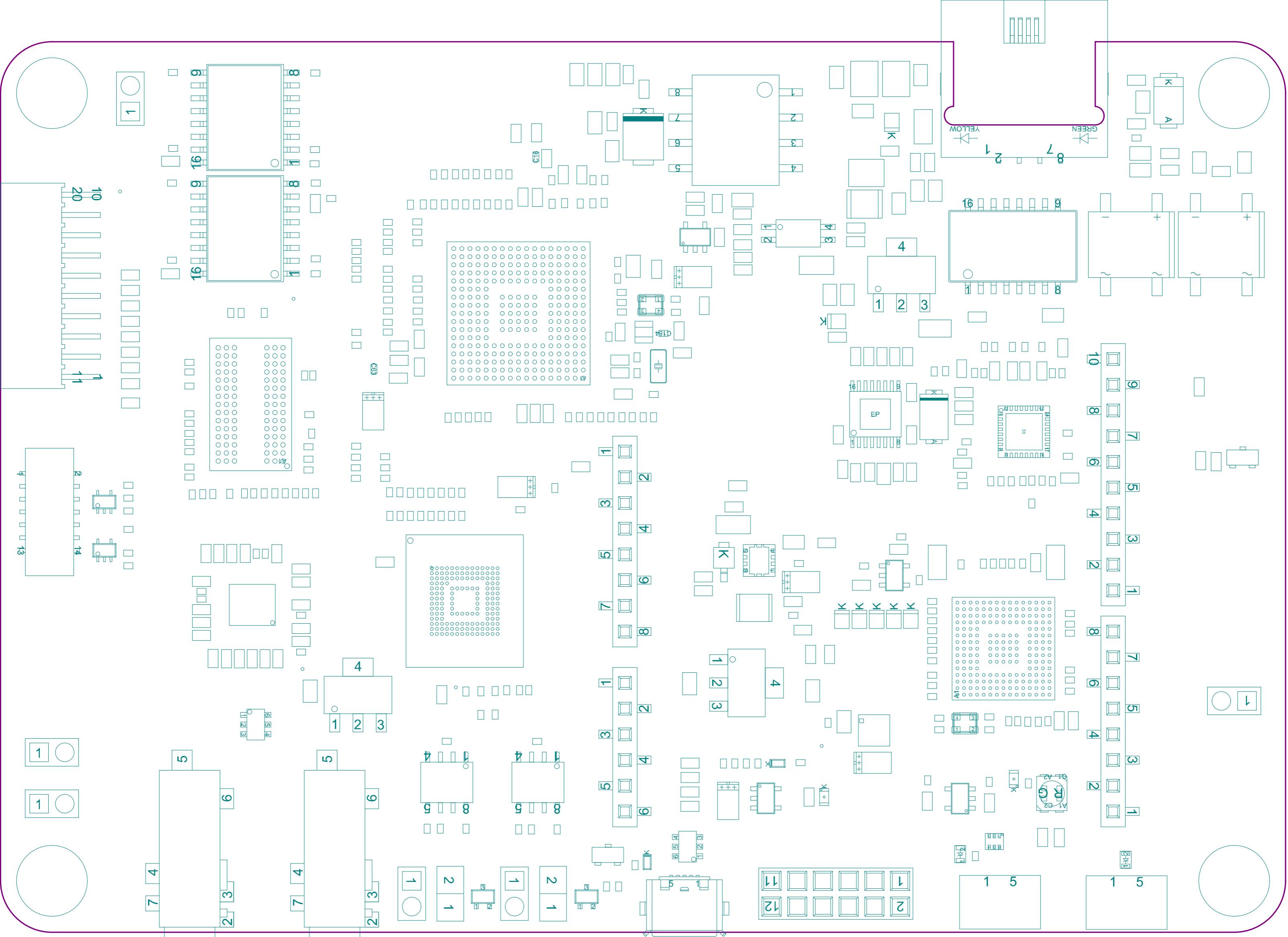
HW6
MB1381A
PCB

HW7
MB1381A-01 XXXX
BOARD REF

HW8
STM32H745I-Disco
BOARD CPN

HW4
LOGO ST
HW5
LOGO CE
HW3
LOGO ESD
HW9
LOGO ROHS
HW10
LOGO STM32



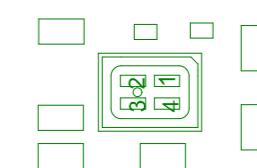


2

LAYER 2 (GND PLANE)

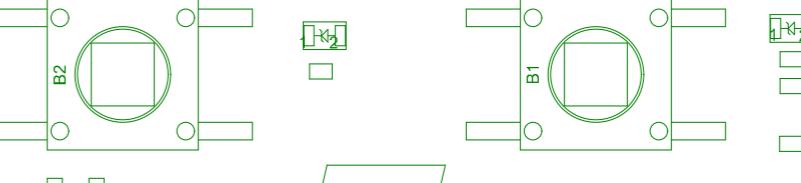
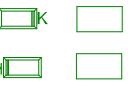
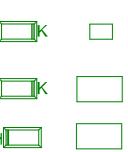
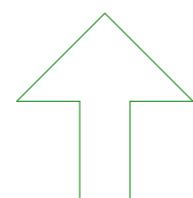
FAB DRAWING & DRILL LEGEND

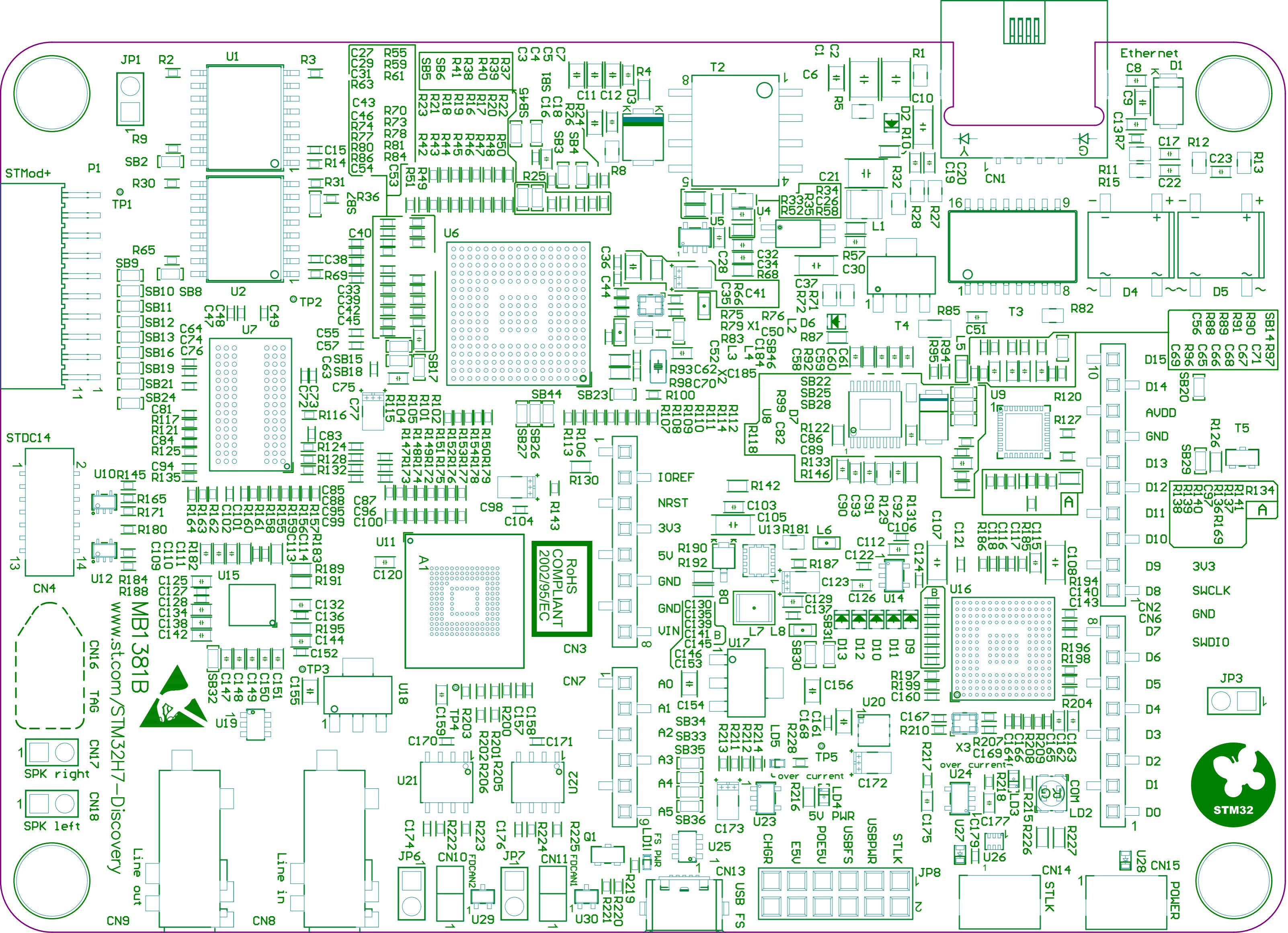
4,3 INCH WVGA
480x272 DOTS
Viewing direction
12:00 O'clock



SB47

40





2

LAYER 2 (GND PLANE)

SILKSCREEN (PRIMARY) LEGEND

