

3D Scanning by Means of Dual-Projector Structured Light Illumination

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ABSTRACT

This paper introduces a dual-projector phase measuring profilometry that adds a second projector in a traditional structured light illumination system to improve the overall quality of the 3D scanning. With this method, two projectors are synchronized to a single camera, but each one projects structured light patterns of its unique frequency. The system performance benefits from a wider projection angle and a doubled light intensity. In particular, a detailed system implementation in hardware level is described. Moreover, the major difference between the phase unwrapping of dual-projector system and the single-projector system is discussed, and a LUT-based phase unwrapping scheme for this dual-projector phase measuring profilometry system is proposed.

Keywords: Structured light illumination, phase measuring profilometry, dual-projector, phase unwrapping

1. INTRODUCTION

As one of the non-contact 3D shape measurement techniques, the structured light illumination (SLI) has been known for its high resolution and high speed.¹ Conventional SLI systems consist of one projector, one camera and one processing unit which is usually a computer. The projector presents patterns that are encoded with some information related to the pixel locations on the object to be measured. If the projection is on a flat surface, the patterns seen should be identical to its original design. However, with the presence of the non-planar object under the projection, what is actually seen from the camera is the distorted patterns on the surface of the object. By comparing and analyzing the distortion in the images taken by the camera, the 3D surface of the object can eventually be reconstructed in a processing unit.

Within the scope of this fundamental idea and system structure, a lot of research has been done over the past several decades.² Different practical implementations have been proposed, from pattern design to system calibration, then to the algorithms that are used to decode the captured patterns. One widely studied area is the one-shot SLI strategy in which only one static pattern is projected onto the object. It employs color pattern,³ binary grid pattern,⁴ gray-scale pattern⁵ or even composite pattern.⁶ Since there is only one image need to be processed for a scan, the 3D reconstruction can be completed fast. The one-shot strategy is ideal for high speed applications such as real time scanning. But in terms of accuracy, it is not so promising compared to some multi-shot SLI strategy in which the 3D reconstruction is derived by projecting a sequence of patterns and processing multiple images.⁷

According to the method of encoding the information containing the pixel location into the pattern, phase measuring profilometry (PMP)⁸ is a subset of SLI techniques that acquires the depth value by translating the phase data from the projected patterns pixelwise. The PMP has some advantageous features including its insensitivity to ambient light and its high accuracy.^{9,10} Higher frequency patterns have been introduced to PMP systems to reduce the effects of the noise and achieve higher accuracy.¹¹ Nonetheless, higher frequency patterns also produces the phase ambiguity which requires the system to execute some extra computation called phase unwrapping.¹² Furthermore, adding high frequency patterns increases the projection time as well as the number of images to be processed, which accordingly decreases the overall speed of the system. Liu *et al.* proposed an ingenious dual-frequency pattern strategy which combines a high-frequency pattern and a unit-frequency pattern into one composite pattern,¹³ it improved the accuracy without increasing the scanning time.

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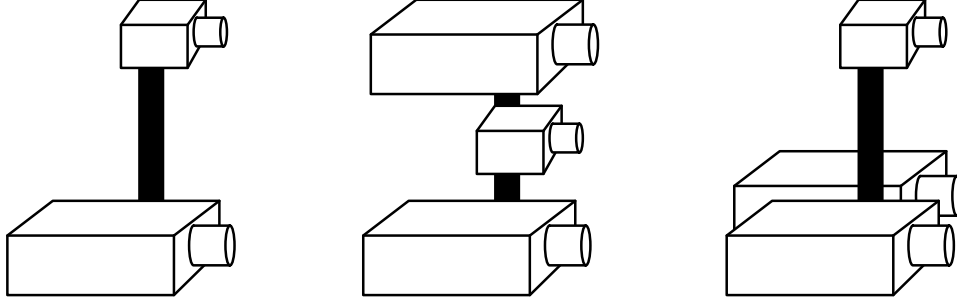


Figure 1: Structured light scanners composed of (left) a single projector, (center) opposed dual-projectors for wrap around scanning, and (right) complementary dual-projectors for increased luminance.

In this paper, we propose a dual-projector, dual-frequency SLI scheme and its practical implementation. Unlike the scanner presented by Jiang *et al.*,¹⁴ the proposed scanner projects patterns from both projectors simultaneously, which keeps the scan time the same versus a single projector scanner. We also allow the projectors to be placed in any arbitrary arrangement with 3D point clouds generated from a single fused phase image instead of fusing to independently generated point clouds. Depending on the arrangements of projectors as illustrated in Fig. 1, a dual-projector SLI scanner can be used to minimize occlusions¹⁵ since a scan surface needs to be visible to at least one projector and the camera. So placing projectors in opposition means a face, for example, can be illuminated from both sides. Having two unique paths of light from projector to camera also creates new opportunities and challenges for overcoming the issue of multi-path.¹⁶ Placing projectors side-by-side can be used to achieve twice the luminance of a single projector. Higher light intensity gives rise to the higher signal-to-noise ratios (SNR),¹⁷ which makes the system less susceptible to sensor noise, consequently the system becomes more robust and accurate.

Now in order for dual-projector SLI scanners to become practical, there are two fundamental problems that need to be addressed. The first is the practical implementation of control logic to perfectly synchronize the projectors to a camera. For this, we describe an FPGA-based controller using a commodity FPGA board to simultaneously drive two HDMI ports. The advantage of this system is that it allows us to avoid the practical issues associated with modern GPUs as well as staying low cost. The second problem that needs to be addressed is how to perform phase unwrapping when the phase images derive from separate projectors arbitrarily arranged. Using the dual-frequency pattern scheme, we demonstrate using a unit-frequency phase image from one projector to unwrap the high-frequency phase image in the second, opposed projector, using a look-up table-based operation ideally suited for GPUs. This real-time approach is highly flexible and can be used to replace phase unwrapping schemes such as the Chinese remainder technique of Xia *et al.*¹⁸

2. DUAL-FREQUENCY PHASE MEASURING PROFILOMETRY

The dual-frequency pattern scheme was initially proposed by Liu *et al.*,¹³ it is essentially an improved derivation of the phase measuring profilometry.¹⁰ In the traditional PMP, a series of phase-shifting sinusoidal fringe patterns are projected onto the object to be measured. The fringe patterns can be either horizontal or vertical such that the presence of the object distorts the patterns where by analyzing the phase values in the deformed pattern images, the 3D depth values can be obtained.

Suppose the phase-shifting patterns are vertical, which indicates that all the pixels in a same row have the identical intensity, and from top to bottom the value of intensities in each row form a sinusoidal function. Therefore, this kind of PMP patterns can be generalized as:

$$I_n^p(x^p, y^p) = A^p + B^p \cos \left(2\pi f y^p - \frac{2\pi n}{N} \right), \quad (1)$$

where I_n^p is the intensity of the pixel at the coordinate (x^p, y^p) from the projector's point of view; A^p is background intensity which is considered as a constant; B^p is another constant which represents the fringe contrast compared

to the background; f is the frequency of the fringe pattern set which is equal to the number of sinusoidal periods from top to the bottom of the projected patterns; N is the total number of the phase-shifting patterns of the same frequency in a set, n is the current number of the pattern within the range of $[0, N - 1]$.

On the camera's side, the image of the object under the projection of each unique PMP pattern is captured as part of the data to reconstruct the 3D profile of the object. So the projected scene of the object can be described as Eq. (2) from the camera's point of view,

$$I_n^c(x^c, y^c) = A^c(x^c, y^c) + B^c(x^c, y^c) \cos\left(\phi(x^c, y^c) - \frac{2\pi n}{N}\right), \quad (2)$$

where I_n^c is the intensity of the given pixel (x^c, y^c) in the camera's coordinates system; $A^c(x^c, y^c)$ is the average intensity of the given pixel over the N patterns; B^c can be considered as the average intensity of the PMP pattern seen by the camera. So for any given pixel (x^c, y^c) in a pattern set $[0, N - 1]$, A^c and B^c are both constant; furthermore, $B^c(x^c, y^c)$ can be derived from Eq. (2) as:

$$B^c(x^c, y^c) = \frac{2}{N} \sqrt{\left[\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \sin\left(\frac{2\pi n}{N}\right) \right]^2 + \left[\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \cos\left(\frac{2\pi n}{N}\right) \right]^2}. \quad (3)$$

In Eq. 2, there is another important constant ϕ for any given pixel (x^c, y^c) in a given pattern set $[0, N - 1]$, it is the phase value $\phi(x^c, y^c)$ of the distorted fringe patterns that is eventually used to calculate the corresponding depth value of the object. The expression of $\phi(x^c, y^c)$ can be inferred from Eq. (3) as:

$$\phi(x^c, y^c) = \frac{1}{2\pi} \left(\arctan \frac{\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \sin(\frac{2\pi n}{N})}{\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \cos(\frac{2\pi n}{N})} + \pi \right), \quad (4)$$

where the $\arctan(\cdot)$ range of $[-\pi, \pi]$ is transformed to the phase value, $\phi(x^c, y^c)$, in the range of $[0, 1]$.

The dual-frequency pattern scheme adds a second sinusoidal component to the phase-shifting patterns. As a result, the total number of patterns is still N , but each pattern has two sinusoidal components. One is of unit frequency, $f_u = 1$, while the other is of a high frequency, f_h , that is used to reduce the impact of noises in the system. According to Liu *et al.*, from the projector's point of view, the new dual-frequency pattern is expressed as:

$$I_n^p(x^p, y^p) = A^p + B_1^p \cos\left(2\pi f_h y^p - \frac{2\pi n}{N}\right) + B_2^p \cos\left(2\pi f_u y^p - \frac{4\pi n}{N}\right), \quad (5)$$

where the I_n^p is the intensity of the pixel (x^p, y^p) in the n^{th} pattern. Similarly, the intensity equation of the camera coordinates used for reconstruction becomes:

$$I_n^c(x^c, y^c) = A^c(x^c, y^c) + B_1^c(x^c, y^c) \cos\left(\phi_h(x^c, y^c) - \frac{2\pi n}{N}\right) + B_2^c(x^c, y^c) \cos\left(\phi_u(x^c, y^c) - \frac{4\pi n}{N}\right). \quad (6)$$

Likewise, the B_1^c , B_2^c and ϕ_h , ϕ_u can be derived from Eq. (3) and Eq. (4) respectively,

$$B_m^c(x^c, y^c) = \frac{2}{N} \sqrt{\left[\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \sin\left(m \frac{2\pi n}{N}\right) \right]^2 + \left[\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \cos\left(m \frac{2\pi n}{N}\right) \right]^2}, \quad (7)$$

where $m = 1$ or 2 in this case,

$$\phi_h(x^c, y^c) = \frac{1}{2\pi} \left(\arctan \frac{\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \sin(\frac{2\pi n}{N})}{\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \cos(\frac{2\pi n}{N})} + \pi \right) \text{ and} \quad (8)$$

$$\phi_u(x^c, y^c) = \frac{1}{2\pi} \left(\arctan \frac{\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \sin(\frac{4\pi n}{N})}{\sum_{n=0}^{N-1} I_n^c(x^c, y^c) \cos(\frac{4\pi n}{N})} + \pi \right). \quad (9)$$

where, again, ϕ_h and ϕ_u are scaled to the range $[0, 1]$. However, the actual range of ϕ_h is $[0, f_h]$ – so ϕ_h is called the wrapped phase. Unwrapping the ϕ_h to $\tilde{\phi}_h \in [0, f_h]$ can lead to a more accurate conversion from phase to depth. In order to obtain $\tilde{\phi}_h$, the value of ϕ_u is used as the following equation shows,

$$\tilde{\phi}_h(x^c, y^c) = (\phi_h(x^c, y^c) + \lfloor f_h \phi_u(x^c, y^c) - \phi_h(x^c, y^c) + 0.5 \rfloor) / f_h, \quad (10)$$

where the $\lfloor \cdot \rfloor$ is the symbol of floor function which outputs the greatest integer less than or equal to the value enclosed.

Now that we have unambiguous, unwrapped phase term, $\tilde{\phi}_h$, we can derive the 3D point from our scan according to Liu *et al.*:¹³

$$Z^w(x^c, y^c) = M_z(x^c, y^c) + \frac{N_z(x^c, y^c)}{C(x^c, y^c)\tilde{\phi}_h(x^c, y^c) + 1}, \quad (11)$$

$$X^w(x^c, y^c) = E_x(x^c, y^c)Z^w(x^c, y^c) + F_x(x^c, y^c), \quad (12)$$

$$Y^w(x^c, y^c) = E_y(x^c, y^c)Z^w(x^c, y^c) + F_y(x^c, y^c). \quad (13)$$

where the coefficients $M_z(x^c, y^c)$, $N_z(x^c, y^c)$, $E_x(x^c, y^c)$, $E_y(x^c, y^c)$, $F_x(x^c, y^c)$, $F_y(x^c, y^c)$ and $C(x^c, y^c)$ are determined, for each individual camera pixel, during calibration of the projector and cameras lenses, assuming pin-hole lens models.

3. DUAL-PROJECTOR PHASE MEASURING PROFILOMETRY

In this paper, we apply the dual-frequency PMP to a dual-projector system in which the SLI patterns in Eq. (5) are jointly generated by two separate projectors H and U. The projector H produces the high frequency (f_h) component while the projector U only produces the unit frequency (f_u) component. Thus, their intensity equations can be expressed as:

$$I_n^{p_h}(x^{p_h}, y^{p_h}) = A^{p_h} + B_1^{p_h} \cos\left(2\pi f_h y^{p_h} - \frac{2\pi n}{N}\right), \text{ and} \quad (14)$$

$$I_n^{p_u}(x^{p_u}, y^{p_u}) = A^{p_u} + B_2^{p_u} \cos\left(2\pi f_u y^{p_u} - \frac{4\pi n}{N}\right), \quad (15)$$

respectively. During the reconstruction process, Eqn. (6) and (9) remain the same; however, Eqn. (10) is not applicable for the dual-projector system since the two phases do not have a deterministic relationship. Instead, we use a lookup table (LUT) to acquire the unwrapped phase according to:

$$\tilde{\phi}_h(x^c, y^c) = (\phi_h(x^c, y^c) + \lfloor LUT[x^c, y^c, \phi_u(x^c, y^c)] - \phi_h(x^c, y^c) + 0.5 \rfloor) / f_h, \quad (16)$$

where the look-up table, $LUT[x^c, y^c, \phi_u(x^c, y^c)]$, represents a 3D matrix that is populated from the data collected during calibration.

The procedure for calibrating our scanner is illustrated in Fig. 2 and involves sweeping a rigid, flat checkerboard pattern in correspondence with the XY -plane and moving in fixed, small-steps through the scan volume from the nearest plane where $Z = Z_{min}$ to the farthest plane at $Z = Z_{max}$. The physical moving of the checkerboard pattern is performed using a motorized rail like the Velmex Bi-slide and VXM controller. These rails can be configured to move with a positional accuracy of 0.076 mm and repeatability of 0.005 mm. The specific checkerboard pattern that we use the CalTag calibration target introduced by Atcheson *et al.*⁷ where, inside each square, is a unique 16-bit codeword identifying and absolute world (X_w, Y_w) coordinates of each squares four corners.

As stated above, the calibration procedure involves iteratively moving the CalTag target along the Z -axis from Z_{min} to Z_{max} such that, at each step, we perform a structured light scan with each camera pixel defined according to:

$$I_c[x^c, y^c, k] = [X_w[x^c, y^c, k], Y_w[x^c, y^c, k], Z_w[x^c, y^c, k], P_h[x^c, y^c, k], P_u[x^c, y^c, k]] \quad (17)$$

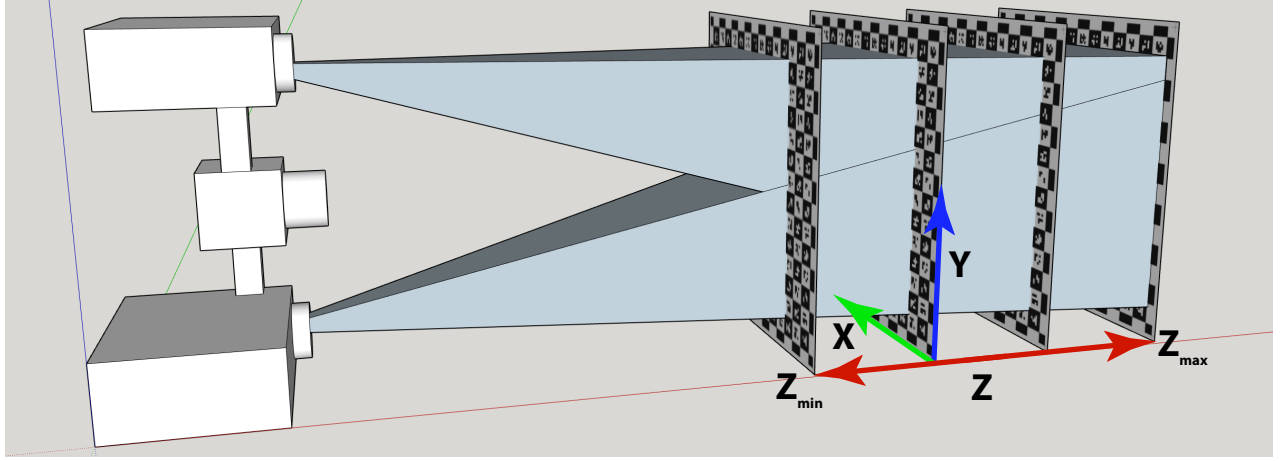


Figure 2: Illustration showing dual-projector scanner calibration.

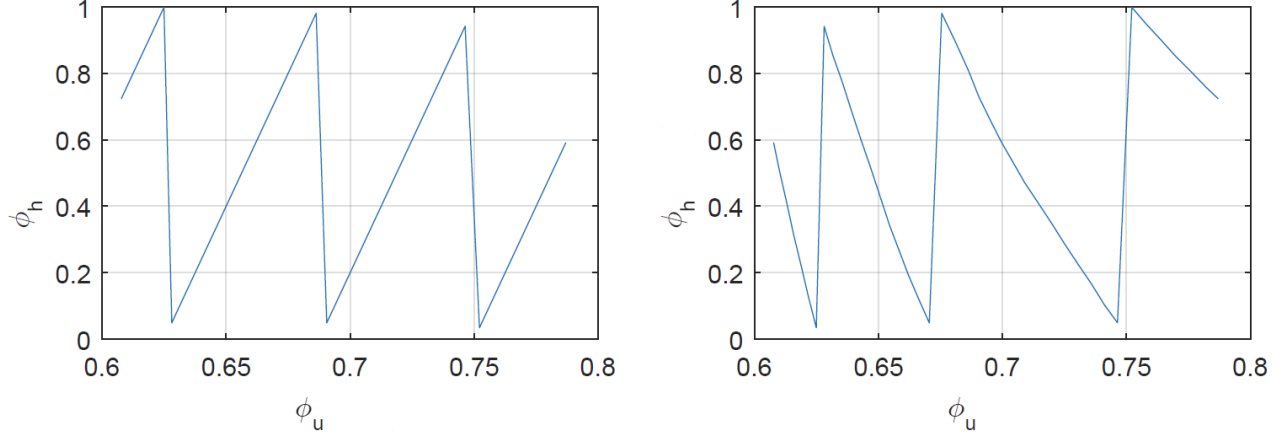
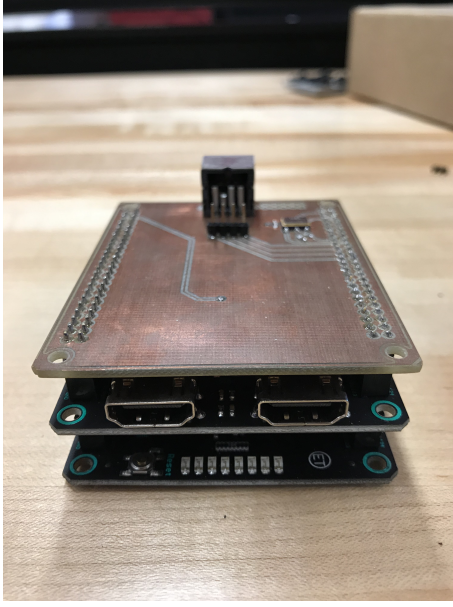


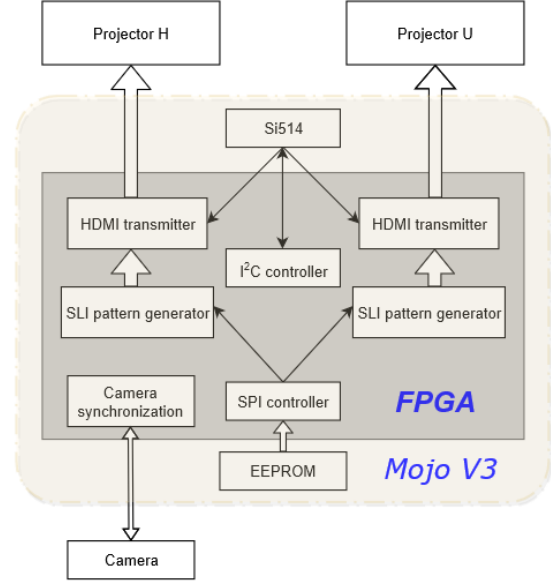
Figure 3: ϕ_f vs. ϕ_u in single-projector system (left) and dual-projector system (right)

where x^c and y^c are the pixel's column and row coordinates; k is the step index for the current scan; $X_w[x^c, y^c, k]$ and $Y_w[x^c, y^c, k]$ are the CalTag grid coordinate at the intersection of the CalTag target's surface and the camera pixel's line-of-sight; $Z_w[x^c, y^c, k]$ is the Z-position of the rail; and $P_u[x^c, y^c, k]$ and $P_h[x^c, y^c, k]$ are the dual phase values of the projected PMP sequences in the line-of-sight of the camera pixel. In the case of $P_h[x^c, y^c, k]$, we apply phase unwrapping to the captured images so that $P_h[x^c, y^c, k]$ is the unwrapped phase value in the range 0 to f_h . In this way, the floor of $P_h[x^c, y^c, k]$ corresponds to the unwrapping index while the fractional part corresponds to the wrapped phase value. Once complete, we then have a 3D texture composed of a stack of 5-color digital images that are the height and width of our camera and K layers deep with the unwrapped $P_h[x^c, y^c, k]$ ranging from $P_{min}[x^c, y^c]$ to $P_{max}[x^c, y^c]$ as $Z_w[x^c, y^c, k]$ ranges from $Z_{min}[x^c, y^c]$ to $Z_{max}[x^c, y^c]$.

Now in order to complete calibration, we...From these vectors, we can plot the relationship between any two parameters. For instance, Fig. 3 plots the relationship between $P_u[x^c, y^c, k]$ and the unwrapped or fractional component of $P_h[x^c, y^c, k]$. It is this relationship that we store in $LUT[x^c, y^c, \phi_u(x^c, y^c)]$ of eqn. (16). Specifically, Fig. 3 (left) illustrates the phase relationships when the two phase terms derive from a single projector while Fig. 3 (right) shows the arbitrary relationship found in our dual-projector system. It should be noted that our look-up table approach can be applied to both. In fact, applying our approach to a traditional structured light scanner proved a reliable way to debug the source code.



(a) A photo of the system



(b) System diagram

Figure 4: Our dual-projector system

4. SYSTEM IMPLEMENTATION

Our FPGA-based dual-projector Structured Light Illumination (SLI) system generates two synchronized SLI patterns which are then fed to two projectors via HDMI. Meanwhile, the projectors and the camera need to be synchronized to ensure that the camera images are taken at the right timing. A photo of our system and the system diagram are shown in Fig. 4a and Fig. 4b. The two SLI pattern generators output two synchronized phase-shifting fringe patterns which are later encoded into TMDS data streams by the HDMI transmitters and eventually move to projectors.

HDMI is the abbreviation of High-Definition Multimedia Interface, it is one of the most popular display interfaces. The newest release, HDMI Version 2.1 supports up to 10K video at 120Hz. A standard HDMI connector has 19 pins,¹⁹ Data channel 2, 1, 0 are mainly used to transfer red, green and blue components of the video respectively. The HDMI does not only transfer video data, but also some auxiliary data, for example audio data, packet header. The auxiliary data, video data as well as some control signals are encoded in data channel 2, 1, 0 and then digitally transmitted in serial. In between any two adjacent video periods, one or more data island period and control period are inserted.

In HDMI, there are six important control signals, HSYNC indicates the beginning and end of a row in a frame of the video, VSYNC indicates the beginning and end of a frame, CTL0 CTL3 indicate the data type of the following data period. The three data channels are transmitted through a differential signaling technology called Transition-Minimized Differential Signaling (TMDS) to reduce the impact of electromagnetic interference and enable high clock skew tolerance.

The 5 volts power signal is provided by the HDMI source or an external source, which after the HDMI sink reads the 5 volts signal, it immediately asserts the pin, Hot Plug Detect. Once the HDMI source detects the presence of a sink by the assertion of the pin Hot Plug Detect, it sends an I^2C -based command of a read request to the sink. The pins SCL and SDA compose the display data channel (DDC) via which the Extended Display Identification Data (EDID) is read by the HDMI source from the sink as the response to the read request. The EDID is usually 128 or 256 bytes long, it contains various information related to the features of display system, including but not limited to, manufacturer ID, serial number, week and year of manufacture, screen size, supported timing, etc.

The pin CEC is used to add some advanced functionalities for the HDMI systems. Usually it is a remote control that issues different high-level commands to the devices connected by HDMI cables. CEC stands for Consumer Electronics Control, it is also a one-wire bus protocol, the implementation of CEC is optional, because not all the HDMI devices support this feature. Since HDMI 1.4, the previously reserved pin has become the HDMI Ethernet and Audio Return Channel (HEAC). While it is in audio return channel mode, only the HEAC+ line is used to transmit audio data; in HDMI Ethernet channel mode, the HEAC+ line pairs up with the HEAC- line as a differential signal to establish a high speed Ethernet communication.

To obtain the uncommon 73.25 MHz pixel clock for 800×600 pixel video at 120 Hz which cannot be generated by the Spartan 6 of the Mojo board, we utilize a programmable oscillator, the Si514. By configuring the internal registers through I^2C bus, Si514 can generate any frequency from 100 kHz to 250 MHz with a tuning resolution of 0.026 ppb. Therefore, an I^2C master controller was incorporated into the system.

Now in order to linearize the projector to avoid the deleterious effects of gamma distortion²⁰ on our projected PMP patterns, we use the EEPROM on the Mojo board to upload calibrated tone correction curves for our target projector. This LUT can be hard-coded into the configuration file of the FPGA, but the drawback is that once the lookup table changes, the FPGA configuration file has to be changed. It is quite inconvenient especially when the system needs to be often applied to a different projector, because generating a new FPGA configuration file requires special development tool and takes more time. We devise an approach that the user stores the LUT in a separate EEPROM chip which can be erased and written by any computer via some general serial or USB tools, and the SPI master module in the FPGA reads the EEPROM every time the system is powered on. With this approach, users can load new LUTs much faster and easier.

The camera synchronization module not only controls the timing of the camera trigger, but also monitors the status of the camera. There are three wires that link the camera and the FPGA. They are *ENABLE*, *TRIGGER*, and the *TRIGGER_READY* signal. The *ENABLE* and *TRIGGER_READY* are output signals from the camera that are programmable from the host PC. The *TRIGGER* signal is an input signal to the camera that is controlled by the FPGA. Initially, the camera pulls the *ENABLE* high to tell the FPGA that a new scan is about to start. Upon receiving *ENABLE*, the FPGA sends the first SLI pattern to the projector starting on the next *Vertical_Sync*. At the end of this first sent video frame, the rising edge of the *Vertical_Sync* from the HDMI module is copied to the *TRIGGER* to trigger the camera's first exposure. The FPGA also increments the frame counter and starts sending the second frame in the SLI sequence to the projector. Upon receiving the trigger, the camera delays its exposure to the point where the first frame begins displaying on the projector's spatial light modulator. Because this first frame was only sent once, its maximum exposure must be less than the time between vertical syncs in the HDMI signal.

Now while exposing the sensor, the camera pulls its *TRIGGER_READY* low, telling the FPGA that no more triggers should be issued for the camera because the camera is busy processing the current exposure. During this period, the projector keeps displaying the same SLI pattern. Once an exposure is done, the *TRIGGER_READY* immediately goes high, which tells the FPGA that the camera is ready for another exposure. So the rising edge of the following *Vertical_Sync* can be sent to *TRIGGER* with the frame counter incremented inside the FPGA so that the next SLI pattern can be sent to the projector. From the PC side, the *TRIGGER_READY* is programmed by setting different values to the camera's parameters such as trigger delay and exposure time. Figure 5 briefly illustrates the synchronization between the camera and the projector for the cases of a fast or slow camera.

In the case of a fast camera with frame rate exceeding the HDMI frame rate, the camera is able to trigger on each and every vertical sync. Hence, the resulting scanner runs at the maximum rate of the projectors. For the slow camera that runs at say 90 fps, the trigger ready tells the projector to hold on to its triggers such that the camera is triggered on every second vertical sync. That means that the scanner runs at a rate of 60 fps since this is 120/2 fps. Of course, it should be noted that using a camera with a 90 fps maximum frame rate, there are 85 Hz refresh rates in the HDMI specification.

5. CONCLUSIONS

In this paper, we have introduced a generalized frame work for building multi-projector structured light scanners. Specifically, we have developing an FPGA controller to drive two synchronized projectors with camera hand-

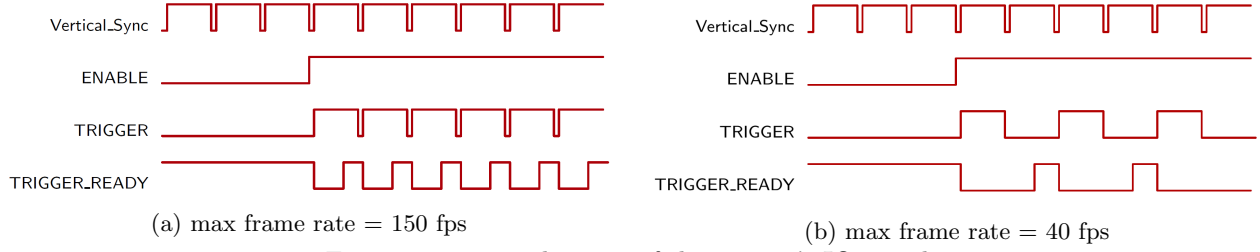


Figure 5: Timing diagram of the camera's IO signals

shaking such that the projected pattern sequences run at an adjustable frame rate regulated by the camera's acquisition speed. In addition to this hardware, we have also introduced a pattern scheme that allows multiple patterns to be superimposed on each other and separated by the processing of the captured images inside the CPU. This disambiguation allows the projectors to be assembled in any arbitrary arrangement. And using our described calibration technique, we can fuse the phase images from multiple projectors to acquire a cohesively fused phase image from which to derive depth. Future research will focus on integrating the FPGA engine inside the FPGA resources of an existing machine vision camera to create a camera that converts any HDMI projector into an instant structured light scanner.

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