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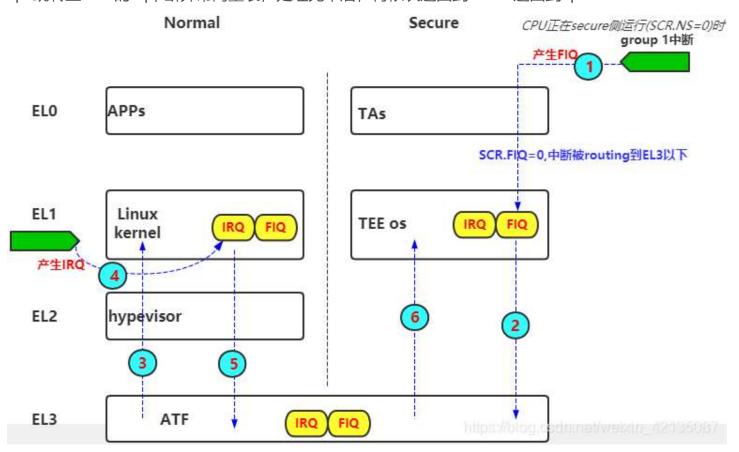
环境:

linux kernel 4.4, (SCR.IRQ=0、SCR.FIQ=1) optee 3.6 (SCR.IRQ=0、SCR.FIQ=0) ARMV8 GICV3

当cpu处于secure侧时,来了一个非安全中断,根据SCR.NS=0/中断在non-secure group1组,cpu interface将会给cpu一个FIQ, (由于SCR.FIQ=0, FIQ将被routing到EL1),跳转至optee的fiq中断异常向量表,

再optee的fiq处理函数中,调用了smc跳转到ATF,ATF再切换至normal EL1(linux),此时SCR.NS的状态发生变化,根据SCR.NS=1/中断在non-secure group1组,cpu interface会再给cpu发送一个IRQ异常,

cpu跳转至linux的irg中断异常向量表,处理完毕后,再依次返回到ATF—返回到optee



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我们从那代码中, 依次拆解以上步骤:

在cpu进入TEE之前,CPU是通过optee_open_session()、optee_close_session()、optee_invoke_func()等函数进入TEE,而这些函数都是调用了optee_do_call_with_arg(),在该函数中再调用smc。

optee_do_call_with_arg()函数原型如下: (注意中文注释)

```
u32 optee do call with arg(struct tee context *ctx, phys addr t parg)
 1
 2
    {
       struct optee *optee = tee get drvdata(ctx->teedev);
 3
       struct optee call waiter w;
 4
       struct optee rpc param param = { };
 5
       struct optee_call_ctx call_ctx = { };
 6
       u32 ret;
 7
 8
       param.a0 = OPTEE_SMC_CALL_WITH_ARG;
 9
       reg_pair_from_64(&param.a1, &param.a2, parg);
10
       /* Initialize waiter */
11
       optee_cq_wait_init(&optee->call_queue, &w);
12
       while (true) {
13
           struct arm_smccc_res res;
14
15
           // 注意,这里调用smc,-->ATF-->TEE
16
           optee->invoke_fn(param.a0, param.a1, param.a2, param.a3,
17
                    param.a4, param.a5, param.a6, param.a7,
18
                    &res);
19
           // 从TEE回来之后,执行这里(无论是TEE正常返回,还是RPC返回,还是中断切过来的)
20
           // 如果是REE中断导致TEE切过来的,那么这里已经触发irg了,带irg执行完毕后,程序继续》
21
           // 注:REE中断导致的cpu从TEE切过来,也属于RPC返回
22
23
           if (res.a0 == OPTEE_SMC_RETURN_ETHREAD_LIMIT) {
24
25
                * Out of threads in secure world, wait for a thread
26
                * become available.
27
28
               optee_cq_wait_for_completion(&optee->call_queue, &w);
29
           } else if (OPTEE_SMC_RETURN_IS_RPC(res.a0)) {
30
               // 如果是RPC、中断返回走这里, 然后看下optee_handle_rpc()函数原型
31
               param.a0 = res.a0;
32
               param.a1 = res.a1;
33
               param.a2 = res.a2;
34
               param.a3 = res.a3;
35
               optee_handle_rpc(ctx, &param, &call_ctx);
36
               // 如果是中断切过来的,optee_handle_rpc()函数相当于啥都没干,程序继续执行上面。
37
               // 会调用optee->invoke fn, cpu又切回了TEE
38
           } else {
39
               // 如果是正常返回,走这里,退出optee do call with arg()函数
40
```

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```
41
                 ret = res.a0;
                 break;
42
            }
43
        }
44
45
        optee_rpc_finalize_call(&call_ctx);
46
47
         * We're done with our thread in secure world, if there's any
48
          * thread waiters wake up one.
49
50
        optee_cq_wait_final(&optee->call_queue, &w);
51
52
        return ret;
53
    }
```

然后我们再看步骤1和步骤2: 在optee中产生FIQ, 跳转到FIQ中断向量表, 然后调用smc切换到ATF

```
LOCAL FUNC elx irq , :
 1
    #if defined(CFG ARM GICV3)
 2
        native_intr_handler irq
 3
    #else
 4
        foreign_intr_handler
                                irq
 5
    #endif
 6
    END FUNC elx irq
 7
 8
    LOCAL FUNC elx fiq , :
 9
    #if defined(CFG ARM GICV3)
10
                                fiq // 在optee运行时,来了REE中断,触发FIQ,程序会调用到这些
        foreign_intr_handler
11
    #else
        native_intr_handler fiq
12
    #endif
13
    END_FUNC elx_fiq
14
```

然后看下foreign_intr_handler 的具体实现:其实就是将当前进程的一些寄存器和栈寄存器保存,恢复中断模式的寄存器和tmp_stack栈,然后调用smc切换到ATF,其中smdid = TEESMC_OPTEED_RETURN_CALL_DONE

```
/* The handler of foreign interrupt. */
 1
    .macro foreign_intr_handler mode:req
 2
        /*
 3
         * Update core local flags
 4
 5
        ldr w1, [sp, #THREAD_CORE_LOCAL_FLAGS]
 6
        lsl w1, w1, #THREAD_CLF_SAVED_SHIFT
 7
        orr w1, w1, #THREAD_CLF_TMP
 8
                 \mode\(),fiq
         .ifc
 9
        orr w1, w1, #THREAD_CLF_FIQ
10
        .else
11
```

```
orr w1, w1, #THREAD_CLF_IRQ
12
        .endif
13
14
        str w1, [sp, #THREAD_CORE_LOCAL_FLAGS]
15
        /* get pointer to current thread context in x0 */
16
17
        get thread ctx sp, 0, 1, 2
18
        /* Keep original SP_EL0 */
19
        mrs x2, sp el0
20
21
        /* Store original sp el0 */
22
        str x2, [x0, #THREAD_CTX_REGS_SP]
23
        /* store x4..x30 */
24
        store_xregs x0, THREAD_CTX_REGS_X4, 4, 30
25
        /* Load original x0..x3 into x10..x13 */
        load_xregs sp, THREAD_CORE_LOCAL_X0, 10, 13
26
        /* Save original x0..x3 */
27
        store_xregs x0, THREAD_CTX_REGS_X0, 10, 13
28
29
30
        /* Load tmp_stack_va_end */
        ldr x1, [sp, #THREAD_CORE_LOCAL_TMP_STACK_VA_END]
31
32
        /* Switch to SP EL0 */
33
        msr spsel, #0
34
        mov sp, x1
35
        /*
36
         * Mark current thread as suspended
37
38
        mov w0, #THREAD_FLAGS_EXIT_ON_FOREIGN_INTR
39
        mrs x1, spsr_el1
40
        mrs x2, elr_el1
41
        bl thread_state_suspend
42
                         /* Supply thread index */
        mov w4, w0
43
44
        /* Update core local flags */
45
        /* Switch to SP_EL1 */
46
        msr spsel, #1
47
        ldr w0, [sp, #THREAD_CORE_LOCAL_FLAGS]
48
        lsr w0, w0, #THREAD_CLF_SAVED_SHIFT
49
        str w0, [sp, #THREAD_CORE_LOCAL_FLAGS]
50
        msr spsel, #0
51
52
        /*
53
         * Note that we're exiting with SP ELO selected since the entry
54
         * functions expects to have SP_ELO selected with the tmp stack
         * set.
55
         */
56
57
        ldr w0, =TEESMC_OPTEED_RETURN_CALL_DONE
58
        ldr w1, =OPTEE SMC RETURN RPC FOREIGN INTR
50
```

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60
61
62

mov w2, #0
mov w3, #0
/* w4 is already filled in above */
```

63

64

65

.endm

然后到了步骤3:看ATF的opteed_smc_handler()函数,我们直接来看case TEESMC_OPTEED_RETURN_CALL_DONE处。

/* SMC should not return */

在optee时,触发了FIQ,foreign_intr_handler调用smc,进入ATF后,走这里,这里将恢复linux系统的寄存器,ELR EL3填充linux侧的PC指针值,SMC RET4后cpu将切回linux

```
1
        case TEESMC_OPTEED_RETURN_CALL_DONE:
 2
              * This is the result from the secure client of an
 3
              * earlier request. The results are in x0-x3. Copy it
 4
              * into the non-secure context, save the secure state
 5
              * and return to the non-secure state.
 6
            assert(handle == cm get context(SECURE));
 7
            cm_el1_sysregs_context_save(SECURE);
 8
 9
            /* Get a reference to the non-secure context */
10
            ns_cpu_context = cm_get_context(NON_SECURE);
11
             assert(ns_cpu_context);
12
13
            /* Restore non-secure state */
14
            cm_el1_sysregs_context_restore(NON_SECURE);
15
             cm_set_next_eret_context(NON_SECURE);
16
17
             SMC_RET4(ns_cpu_context, x1, x2, x3, x4);
18
```

接着又到了步骤4和步骤5:该部分对应的代码就是本篇一开始贴出的optee_do_call_with_arg(),程序回到次函数后,由于SCR.NS的状态发生了变化,cpu interface会再次给ARM Core发送一个IRQ,此时立即进入了linux kernel的IRQ中断向量表,待中断处理函数执行完毕后。PC再次指向此处,接着也就是下面这段逻辑了

```
1
  else if (OPTEE_SMC_RETURN_IS_RPC(res.a0)) {
2
             // 如果是RPC、中断返回走这里, 然后看下optee handle rpc()函数原型
             param.a0 = res.a0;
3
4
             param.a1 = res.a1;
5
             param.a2 = res.a2;
             param.a3 = res.a3;
6
             optee_handle_rpc(ctx, &param, &call_ctx);
7
8
             // 如果是中断切过来的,optee handle rpc()函数相当于啥都没干,程序继续执行上面
9
```

在optee_handle_rpc()中的OPTEE_SMC_RPC_FUNC_FOREIGN_INTR业务逻辑中,其实啥逻辑都没干,直接返回. 子函数返回后, optee_do_call_with_arg()中的while循环继续执行, optee->invoke_fn() 再次将CPU切到ATF。

```
1
    void optee_handle_rpc(struct tee_context *ctx, struct optee_rpc_param *param,
 2
                   struct optee call ctx *call ctx)
    {
 3
        struct tee device *teedev = ctx->teedev;
 4
        struct optee *optee = tee_get_drvdata(teedev);
 5
        struct tee shm *shm;
 6
        phys_addr_t pa;
 7
 8
        switch (OPTEE SMC RETURN GET RPC FUNC(param->a0)) {
 9
        case OPTEE SMC RPC FUNC ALLOC:
10
             shm = tee_shm_alloc(ctx, param->a1, TEE_SHM_MAPPED);
11
             if (!IS_ERR(shm) && !tee_shm_get_pa(shm, 0, &pa)) {
12
                 reg_pair_from_64(&param->a1, &param->a2, pa);
13
                 reg_pair_from_64(&param->a4, &param->a5,
14
                          (unsigned long)shm);
15
             } else {
16
                 param -> a1 = 0;
17
                 param -> a2 = 0;
18
                 param->a4 = 0;
19
                 param -> a5 = 0;
20
             }
21
            break;
22
        case OPTEE_SMC_RPC_FUNC_FREE:
23
             shm = reg_pair_to_ptr(param->a1, param->a2);
24
            tee_shm_free(shm);
25
            break;
26
        case OPTEE_SMC_RPC_FUNC_FOREIGN_INTR: //---看下面的英文注释吧,如果是中断切过来的,
27
28
              * A foreign interrupt was raised while secure world was
29
              * executing, since they are handled in Linux a dummy RPC is
30
              * performed to let Linux take the interrupt through the normal
              * vector.
31
              */
32
            break;
33
        case OPTEE_SMC_RPC_FUNC_CMD:
34
             shm = reg_pair_to_ptr(param->a1, param->a2);
35
             handle rpc func cmd(ctx, optee, shm, call ctx);
36
            break;
37
        default:
38
             pr_warn("Unknown RPC func 0x%x\n",
39
                 (u32)OPTEE SMC RETURN GET RPC FUNC(param->a0));
10
```

```
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             break;
41
         }
42
43
         param->a0 = OPTEE SMC CALL RETURN FROM RPC;
44
    }
45
46
```

接下来步骤6: 再次回到了ATF, 进入ATF的opteed_smc_handler()函数中,然后将optee_vectors->fast smc entry赋值给ELR EL3, 然后ERET退出ATF, 跳转到optee中线程向量表的fast smc entry 中

```
if (is_caller_non_secure(flags)) {
 1
 2
             * This is a fresh request from the non-secure client.
 3
             * The parameters are in x1 and x2. Figure out which
 4
             * registers need to be preserved, save the non-secure
 5
             * state and send the request to the secure payload.
 6
            assert(handle == cm get context(NON SECURE));
 7
 8
            cm_el1_sysregs_context_save(NON_SECURE);
 9
10
11
             * We are done stashing the non-secure context. Ask the
12
             * OPTEE to do the work now.
13
14
15
16
             * Verify if there is a valid context to use, copy the
             * operation type and parameters to the secure context
17
             * and jump to the fast smc entry point in the secure
18
             * payload. Entry into S-EL1 will take place upon exit
19
             * from this function.
20
            assert(&optee_ctx->cpu_ctx == cm_get_context(SECURE));
21
22
            /* Set appropriate entry for SMC.
23
             * We expect OPTEE to manage the PSTATE.I and PSTATE.F
24
             * flags as appropriate.
25
             */
26
            if (GET_SMC_TYPE(smc_fid) == SMC_TYPE_FAST) {
27
                cm_set_elr_el3(SECURE, (uint64_t)
28
                        &optee_vectors->fast_smc_entry);
29
            // Linux处理完中断再回TEE时,走这里,将fast smc entry地址赋给了ELR EL3, fast sm
30
            // fast smc entry函数的地址,是optee开机初始化时,传过来的,然后ATF保存到全局变量
31
            }
32
```

最后,在optee的线程向量表的fast smc entry向量中,将恢复optee之前进程的寄存器和PC值,至此 整个中断处理 流程完成。

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