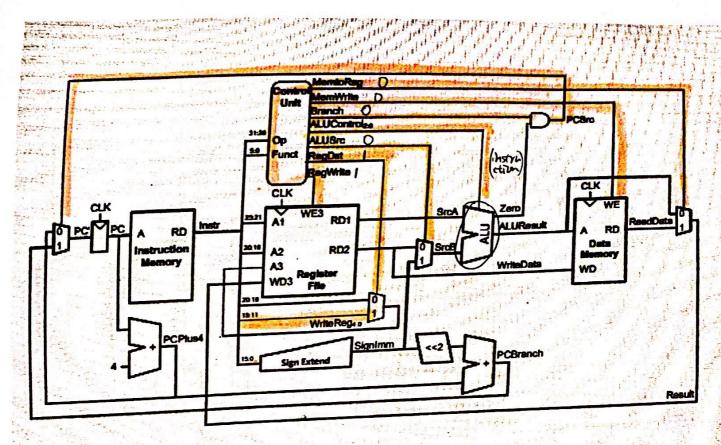
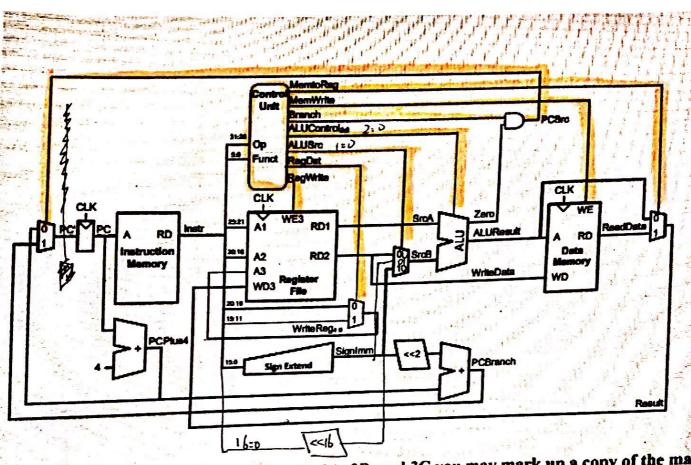


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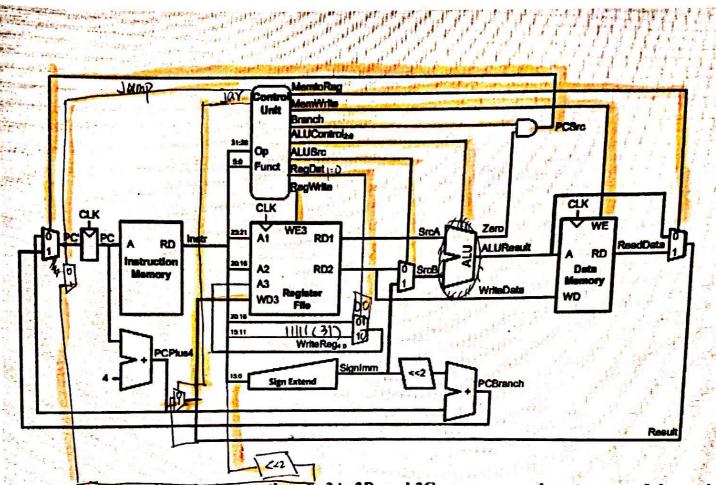
Similarly, for each of the instructions in 3A, 3B, and 3C you may mark up a copy of the main decoder truth table given below to show the changes to it.

		3-1	D Det	ALUSTC	Branch	MemWrite	MemtoReg	ALUOp
Instruction	Opcode	RegWrite	RegDst	July 1g.		<b>b</b> ' - 115	0	10
R-type	000000	1 v	1 1	- 0	A Section	The second secon		00
lw .	100011	The state of the s	. , 0	1, 1,	. 0,	0 1414	· · · · · · · · · · · · · · · · · · ·	00
Sw	101011	* * O ( )	, X, T.	J. 1	" O. CT	F. 4.	The Contract of	01
beq	000100	0.	A W A Y	0. 0	1, 1	17,0	X	4 74 7 10 10
العادية	000110			* * * * * * * * * * * * * * * * * * *	· D	ATT TO THE	0	. (1
	1000110	6 5 2 9 7 1 4		a Harry	har that the	1 14 1	White has I st	<b>美国福祉</b> 在



Similarly, for each of the instructions in 3A, 3B, and 3C you may mark up a copy of the main decoder truth table given below to show the changes to it.

							24 -21 24	
		D - Weite	RegDst	ALUSTC	Branch	MemWrite	MemtoReg	ALUOp
Instruction	Opcode	RegWrite	Total and	4.0	0 .	0'=+	O . 4 4	10
R-type	000000	1	1	199	0.	. 0 444	1	. 00
1e	100011	1	0	1, "	0.,		X	00
SW 4	101011	0.	X. "			44.0	XILA	. 01
bed	000100	0	**, X	1 0 1 h		The Think	10 11 11 11	
- Jui	000111		D'.	10	0	. 0	ON	



Similarly, for each of the instructions in 3A, 3B, and 3C you may mark up a copy of the main decoder truth table given below to show the changes to it.

Instruction C	)pcode	Reg Write	RegDst	ALUSrc	Branch	MemWrite	MemtoReg	ALUOp
R-type 0	00000	1		0	0 .	O Tries,	0	10
]   Nw   1	00011		0 ,	$t_{\mathbf{k}_{i_1}} \circ \mathbf{l}_{i_1}^{i_{i_1}}$	0, "	0 4.	1 1	00
sw 1	01011	0.	( X. ) W	1 .	0	1 1 1 1	X	00
beq 0	00100	, o .	<b>X</b> ",	" t " O " " t	· 4 1 · 4 .	14.0	X	. 01