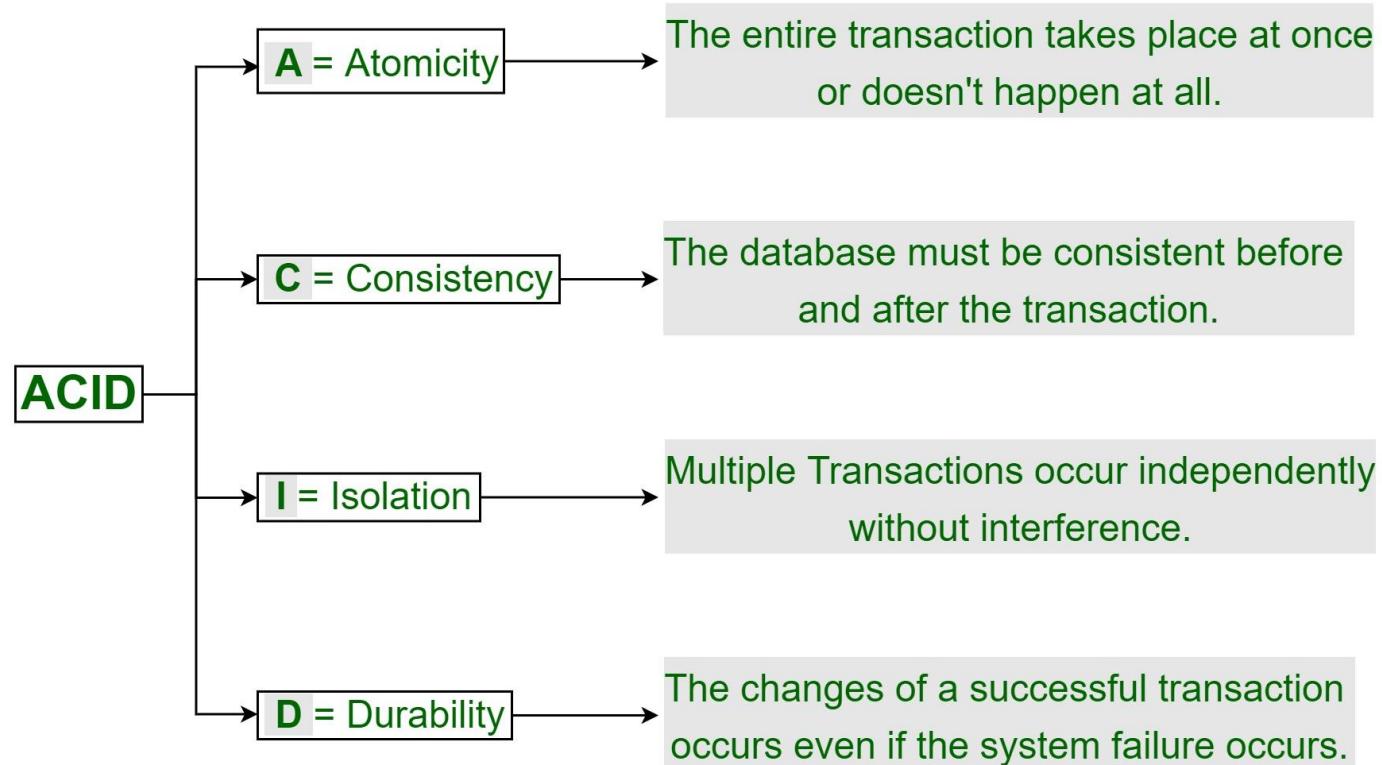


Serializability

CS 341 Database Systems

ACID Properties of Transactions



Recovery Mechanism

To recover the state of the database we can use:

- A **log file** recording every database operation.
- **Checkpoints** recording the state of all active transactions.
 - Then: develop an algorithm for transactions to **UNDO**,
 - and those that we need to **REDO**, to effect recovery.
 - at intervals, the system will:
 - Flush its buffers – buffers are forced to write changes to secondary storage.
 - Write out a checkpoint record to log indicating which transactions are in progress.

Three classic problems

Problem: Two or more transactions read / write on the same part of the database.

Although transactions execute correctly, results may **interleave** in different ways => 3 classic problems.

- **Lost Update**
- **Uncommitted Dependency**
- **Inconsistent Analysis**

Read Write Conflicts

Table 10.11 Read/Write Conflict Scenarios: Conflicting Database Operations Matrix

		Transactions		
		T1	T2	Result
Operations	Read	Read	No conflict	
	Read	Write	Conflict	
	Write	Read	Conflict	
	Write	Write	Conflict	

Time	T ₁	T ₂	bal _x
t ₁			100
t ₂	begin_transaction		100
t ₃	read(bal _x)		100
t ₄	bal _x = bal _x - 10		200
t ₅	write(bal _x)		90
t ₆	commit	begin_transaction	90
		read(bal _x)	100
		bal _x = bal _x + 100	200
		write(bal _x)	200
		commit	200

Lost Update WW

Uncommitted Dependency WR

Time	T ₅	T ₆	bal _x	bal _y	bal _z	sum
t ₁		begin_transaction	100	50	25	
t ₂	begin_transaction	sum = 0	100	50	25	0
t ₃	read(bal _x)	read(bal _x)	100	50	25	0
t ₄	bal _x = bal _x - 10	sum = sum + bal _x	100	50	25	100
t ₅	write(bal _x)	read(bal _y)	90	50	25	100
t ₆	read(bal _z)	sum = sum + bal _y	90	50	25	150
t ₇	bal _z = bal _z + 10		90	50	35	150
t ₈	write(bal _z)		90	50	35	150
t ₉	commit	read(bal _z)	90	50	35	150
t ₁₀		sum = sum + bal _z	90	50	35	185
t ₁₁		commit	90	50	35	185

Inconsistent Analysis RW

ANSI/ISO Transaction Isolation Levels

- Transaction isolation levels refer to the degree to which transaction data is "**protected or isolated**" from other **concurrent** transactions.
- The isolation levels are described based on what data other transactions can see (read) during execution. More precisely, the *transaction isolation levels are described by the type of "reads" that a transaction allows or does not allow.*

Types of Read Operations



Preventable Read Phenomena by Isolation Level

Table 10.15 Transaction Isolation Levels

	Isolation Level	Allowed			Comment
		Dirty Read	Nonrepeatable Read	Phantom Read	
Less restrictive  	Read Uncommitted	Y	Y	Y	Reads uncommitted data, and allows nonrepeatable reads and phantom reads.
	Read Committed	N	Y	Y	Does not allow uncommitted data reads but allows nonrepeatable reads and phantom reads.
	Repeatable Read	N	N	Y	Only allows phantom reads.
	Serializable	N	N	N	Does not allow dirty reads, nonrepeatable reads, or phantom reads.
Oracle/SQL Server Only	Read Only/Snapshot	N	N	N	Supported by Oracle and SQL Server. The transaction can only see the changes that were committed at the time the transaction started.

Exclusive VS Shared Locks

Exclusive Lock

- An **exclusive lock** exists when access is reserved specifically for the transaction that locked the object. The exclusive lock must be used when the potential for conflict exists.
- An exclusive lock is issued when a transaction wants **to update (write)** a data item and no locks are currently held on that data item by any other transaction.

Shared Lock

- A **shared lock** exists when concurrent transactions are granted read access on the basis of a common lock. A shared lock produces no conflict as long as all the concurrent transactions are read-only.
- A shared lock is issued when a transaction wants to **read data** from the database and no exclusive lock is held on that data item.

Solved: Lost Update

Time	User 1 (Trans A)	User2 (Trans B)
1	Retrieve t (get S-lock on t)	
2		Retrieve t (get S-lock on t)
3	Update t (request X-lock on t)	
4	wait	Update t (request X-lock on t)
5	wait	wait
6	wait	wait
7		

- No update lost but → Deadlock

Solved: Uncommitted Dependency

Time	User 1 (Trans A)	User 2 (Trans B)
1		Update t (get X-lock on t)
2	Retrieve t (request S-lock on t)	-
3	wait	-
4	wait	-
5	wait	Commit / Rollback (releases X-lock on t)
6	Resume: Retrieve t (get S-lock on t)	
7	-	
8		

Solved: Inconsistent Analysis

Time	User 1 (Trans A)	User 2 (Trans B)
1	Retrieve Acc1 : (get S-lock) Sum = 40	
2	Retrieve Acc2 : (get S-lock) Sum = 90	
3		Retrieve Acc3: (get S-lock)
4		Update Acc3: (get X-lock) 30 → 20
5		Retrieve Acc1: (get S-lock)
6		Update Acc1: (request X-lock) wait
7	Retrieve Acc3: (request S-lock) wait	wait wait wait

- Inconsistent Analysis is prevented → Deadlock

Serializability

Serializability

- Objective of a concurrency control protocol is **to schedule transactions** in such a way as to avoid any interference.
- Possible solution is to **Run all transactions serially**. This is often too restrictive as *it limits degree of concurrency or parallelism in system*.

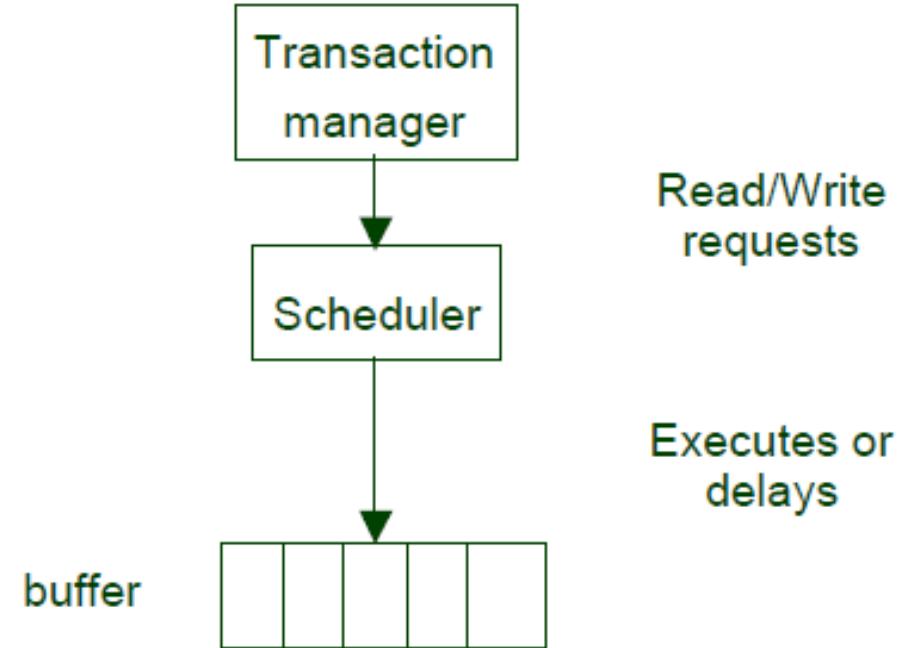
Serializability identifies those executions of transactions that are guaranteed to ensure database consistency.

Schedules

- **Schedule** - *a sequences of instructions that specify the chronological order in which instructions of concurrent transactions are executed*
 - A schedule for a set of transactions must consist of all instructions of those transactions
 - Must preserve the order in which the instructions appear in each individual transaction.
- A transaction that successfully completes its execution will have a commit instruction as the last statement
 - By default, transaction assumed to execute commit instruction as its last step
- A transaction that fails to successfully complete its execution will have an abort instruction as the last statement

The Scheduler

- The scheduler component of a DBMS must ensure that the individual steps of different transactions preserve consistency.



Serializability - some definitions

- **Schedule:** *time-ordered sequence of the important actions taken by one or more transitions.*
- **Serial Schedule:** *a schedule where the operations of each transaction are executed consecutively without any interleaved operations from other transactions.* No guarantee that results of all serial executions of a given set of transactions will be identical.
- **Non-serial Schedule:** Schedule where operations from a set of concurrent transactions are interleaved.

Serializability - some definitions

- Objective of **serializability** :
Find *non-serial* schedules that allow transactions to execute concurrently without interfering with one another.
- In other words, find *non-serial schedules that are equivalent to some serial schedule*. Such a schedule is called **serializable**.

Serializability - some definitions

In serializability, ordering of read/writes is important:

- a) If two transactions only **read** a data item, they do **not conflict** and order is not important.
- b) If two transactions either **read or write completely separate data items**, they do **not conflict** and order is not important.
- c) If one transaction **writes a data item and another reads or writes same data item**, they **conflict** and **order of execution is important**.

Conflict Serializability

Time	T ₇	T ₈	T ₇	T ₈	T ₇	T ₈
t ₁	begin_transaction					
t ₂	read(bal _x)					
t ₃	write(bal _x)					
t ₄		begin_transaction				
t ₅		read(bal _x)				
t ₆		write(bal _x)				
t ₇	read(bal _y)		read(bal _y)			
t ₈	write(bal _y)		write(bal _y)			
t ₉	commit		commit			
t ₁₀		read(bal _y)		read(bal _y)		
t ₁₁		write(bal _y)		write(bal _y)		
t ₁₂		commit		commit		

(a) Schedule S₁

(b) Schedule S₂

(c) Schedule S₃

Figure 22.7 Equivalent schedules: (a) nonserial schedule S₁; (b) nonserial schedule S₂ equivalent to S₁; (c) serial schedule S₃, equivalent to S₁ and S₂.

S2 – swap non-conflicting pairs (Schedule (b))

Time	T7	T8
T1	Begin transaction	
T2	Read (balx)	
T3	Write (balx)	
T4		Begin transaction
T5		Read (balx)
T6	Read (baly)	
T7		Write (balx)
T8	Write (baly)	
T9	Commit	
T10		Read (baly)
T11		Write (baly)
T12		Commit

S2 – swap non-conflicting pairs

Time	T7	T8
T1	Begin transaction	
T2	Read (balx)	
T3	Write (balx)	
T4		Begin transaction
T5		Read (balx)
T6	Read (baly)	
T7	Write (baly)	
T8	Commit	
T9		Write (balx)
T10		Read (baly)
T11		Write (baly)
T12		Commit

S2 – swap non-conflicting pairs

Time	T7	T8
T1	Begin transaction	
T2	Read (balx)	
T3	Write (balx)	
T4		Begin transaction
T5		Read (balx)
T6	Read (baly)	
T7	Write (baly)	
T8	Commit	
T9		Write (balx)
T10		Read (baly)
T11		Write (baly)
T12		Commit

S2 – swap non-conflicting pairs

Time	T7	T8
T1	Begin transaction	
T2	Read (balx)	
T3	Write (balx)	
T4		
T5	Read (baly)	Begin transaction
T6		Read (balx)
T7	Write (baly)	
T8	Commit	
T9		Write (balx)
T10		Read (baly)
T11		Write (baly)
T12		Commit

S2 – swap non-conflicting pairs

Time	T7	T8
T1	Begin transaction	
T2	Read (balx)	
T3	Write (balx)	
T4		
T5	Read (baly)	Begin transaction
T6		Read (balx)
T7	Write (baly)	
T8	Commit	
T9		Write (balx)
T10		Read (baly)
T11		Write (baly)
T12		Commit

S2 – swap non-conflicting pairs

Time	T7	T8
T1	Begin transaction	
T2	Read (balx)	
T3	Write (balx)	
T4		
T5	Read (baly)	
T6	Write (baly)	Begin transaction
T7	Commit	Read (balx)
T8		
T9		Write (balx)
T10		Read (baly)
T11		Write (baly)
T12		Commit

Serial Schedule

Time	T7	T8
T1	Begin transaction	
T2	Read (balx)	
T3	Write (balx)	
T4	Read (baly)	
T5	Write (baly)	
T6	Commit	
T7		Begin transaction
T8		Read (balx)
T9		Write (balx)
T10		Read (baly)
T11		Write (baly)
T12		Commit

Conflict Serializability

Time	T ₇	T ₈	T ₇	T ₈	T ₇	T ₈
t ₁	begin_transaction					
t ₂	read(bal _x)					
t ₃	write(bal _x)					
t ₄		begin_transaction				
t ₅		read(bal _x)				
t ₆		write(bal _x)				
t ₇	read(bal _y)					
t ₈	write(bal _y)					
t ₉	commit					
t ₁₀		read(bal _y)				
t ₁₁		write(bal _y)				
t ₁₂		commit				

(a) Schedule S₁

(b) Schedule S₂

(c) Schedule S₃

Figure 22.7 Equivalent schedules: (a) nonserial schedule S₁; (b) nonserial schedule S₂ equivalent to S₁; (c) serial schedule S₃, equivalent to S₁ and S₂.

Test for Conflict Serializability

Precedence Graph

- Create a node for each transaction. ($T_1, T_2, T_3 \dots$)
- Create a directed edge $T_i \rightarrow T_j$,
 - if T_j reads the value of an item written by T_i . **(WR)**
 - if T_j writes a value into an item after it has been read by T_i . **(RW)**
 - if T_j writes a value into an item after it has been written by T_i . **(WW)**
- If an edge $T_i \rightarrow T_j$ exists in the precedence graph for S , then in any serial schedule equivalent to S , T_i must appear before T_j .
- If the precedence graph contains a **cycle**, the schedule is **not conflict serializable**.

Two Concurrent update transactions that are not conflict serializable

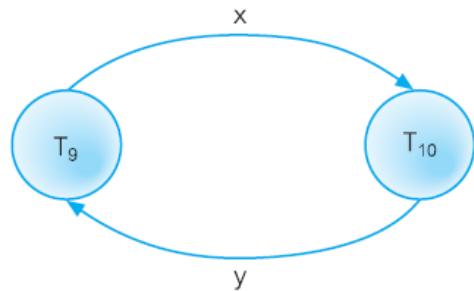
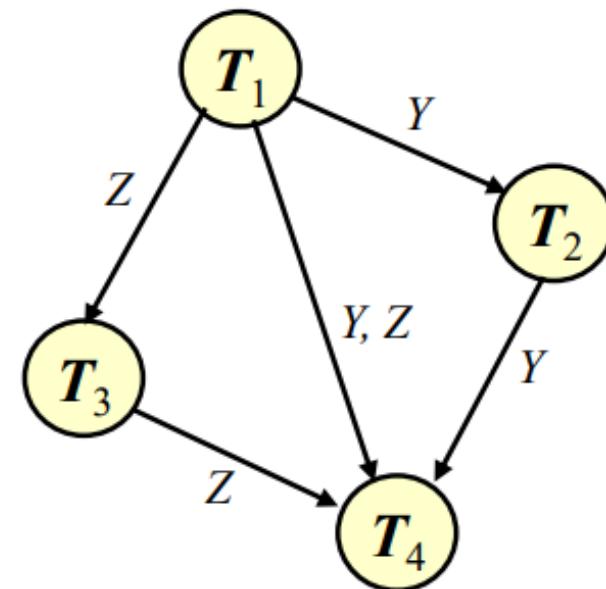


Figure 22.9 Precedence graph for Figure 22.8 showing a cycle, so schedule is not conflict serializable.

Time	T ₉	T ₁₀
t ₁	begin_transaction	
t ₂	read(bal _x)	
t ₃		bal _x = bal _x + 100
t ₄		write(bal _x)
t ₅		
t ₆		
t ₇		
t ₈		
t ₉		
t ₁₀		
t ₁₁		begin_transaction
t ₁₂	read(bal _y)	read(bal _x)
t ₁₃		bal _x = bal _x * 1.1
t ₁₄		write(bal _x)
		commit
	read(bal _y)	read(bal _y)
		bal _y = bal _y - 100
		write(bal _y)
		commit

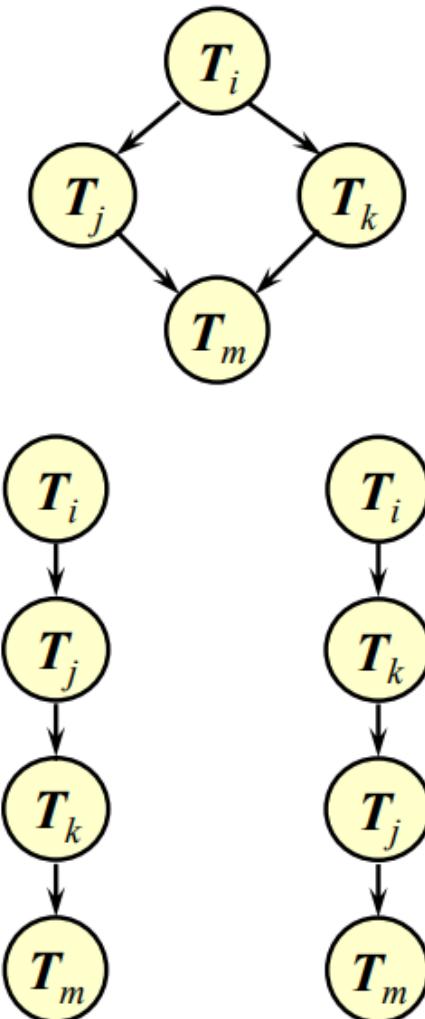
Precedence Graph

T_1	T_2	T_3	T_4	T_5
	$r(X)$			
$r(Y)$				
$r(Z)$				
			$r(V)$	
			$r(W)$	
$r(Y)$				
$w(Y)$				
	$w(Z)$			
$r(U)$				
		$r(Y)$		
		$w(Y)$		
		$r(Z)$		
		$w(Z)$		
$r(U)$				
$w(U)$				



Serializable

- A schedule is serializable if and only if its precedence graph is acyclic.
- If precedence graph is acyclic, the serializability order can be obtained by a topological sorting of the graph.
- A topological sort is a linear ordering of vertices in a directed acyclic graph (DAG). Given a DAG $G = (V, E)$, a topological sort algorithm returns a sequence of vertices in which the vertices never come before their predecessors on any paths.

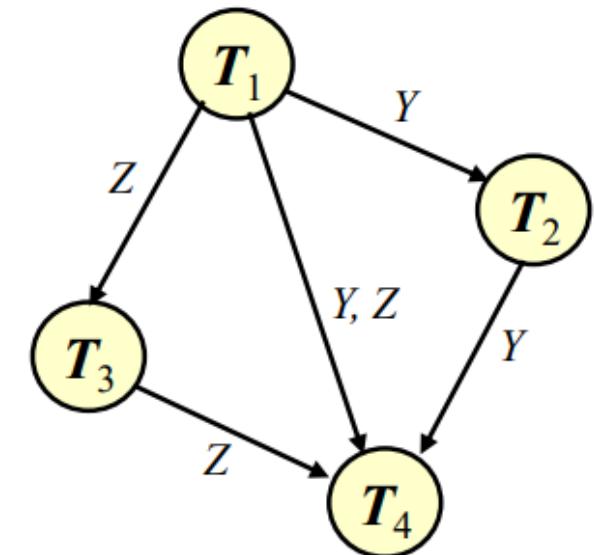


No cycles → Serializable

T1 → T2 → T3 → T4

Or?

	T_1	T_2	T_3	T_4	T_5
		$r(X)$			
	$r(Y)$				
	$r(Z)$				$r(V)$
					$r(W)$
		$r(Y)$			
		$w(Y)$			
			$w(Z)$		
	$r(U)$			$r(Y)$	
				$w(Y)$	
				$r(Z)$	
				$w(Z)$	
	$r(U)$				
	$w(U)$				

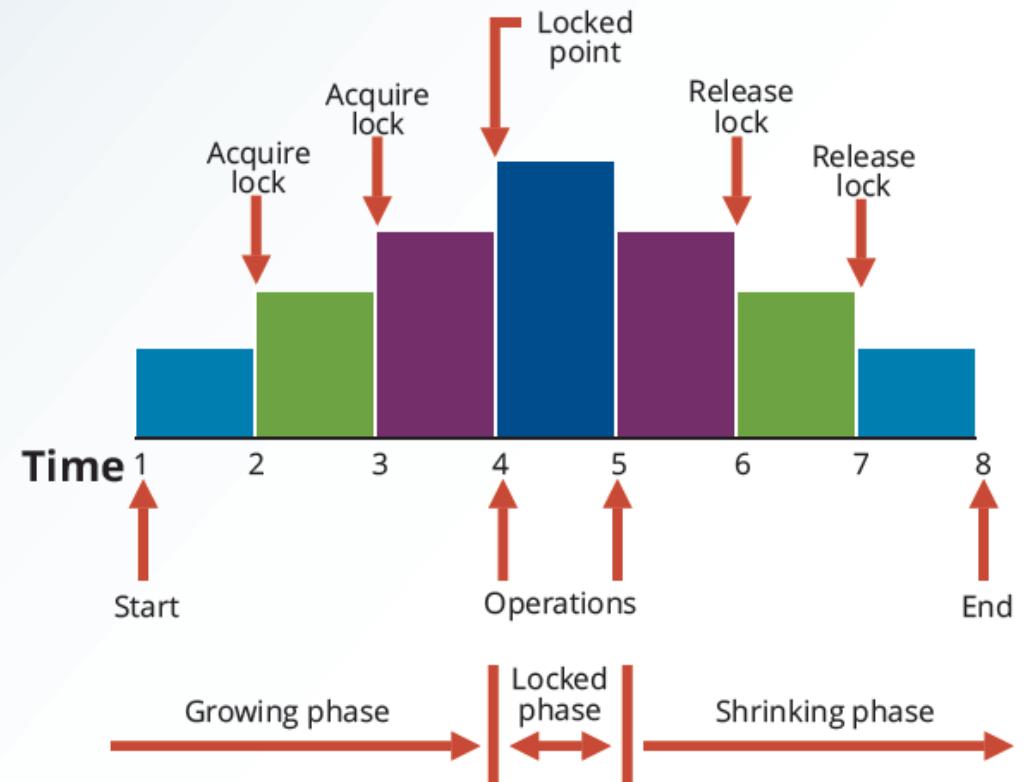


2-Phase Locking Protocol (2PL)

2PL defines how transactions acquire and relinquish locks. It guarantees *serializability*, but it does not prevent deadlocks.

1. A **growing phase**, in which a transaction acquires all required locks without unlocking any data. After all locks have been acquired, the transaction is in its *locked point*.
2. A **shrinking phase**, in which a transaction releases all locks and cannot obtain a new lock.

Figure 10.7 Two-Phase Locking Protocol

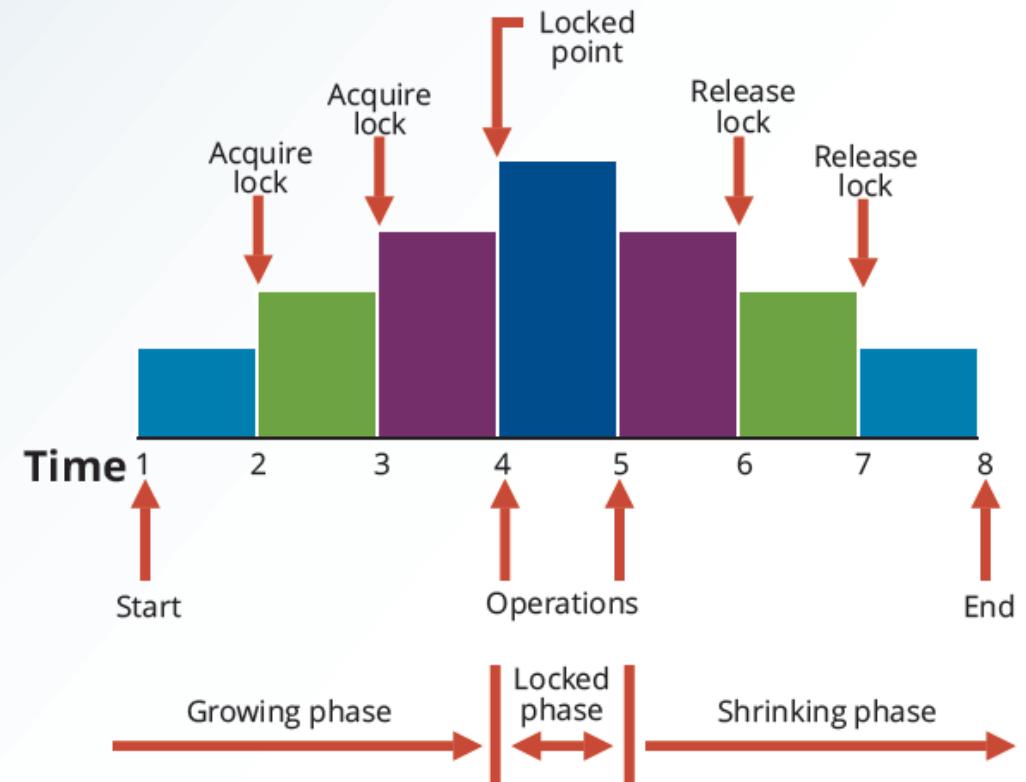


2-Phase Locking Protocol (2PL)

A transaction follows the two-phase locking protocol if all locking operations **precede the first unlock operation** in the transaction.

- There is no requirement that all locks be obtained simultaneously.
- Normally, the transaction acquires some locks, does some processing, and goes on to acquire additional locks as needed.
- However, it never releases any lock until it has reached a stage where no new locks are needed. *In Strict 2PL, all locks released on commit.*

Figure 10.7 Two-Phase Locking Protocol



Preventing the lost update problem

Time	T ₁	T ₂	bal _x
t ₁		begin_transaction	100
t ₂	begin_transaction	write_lock(bal _x)	100
t ₃	write_lock(bal _x)	read(bal _x)	100
t ₄	WAIT	bal _x = bal _x + 100	100
t ₅	WAIT	write(bal _x)	200
t ₆	WAIT	commit/unlock(bal _x)	200
t ₇	read(bal _x)		200
t ₈	bal _x = bal _x - 10		200
t ₉	write(bal _x)		190
t ₁₀	commit/unlock(bal _x)		190

Figure 22.15 Preventing the lost update problem.

Preventing the uncommitted dependency problem

Time	T ₃	T ₄	bal _x
t ₁		begin_transaction	100
t ₂		write_lock(bal _x)	100
t ₃		read(bal _x)	100
t ₄	begin_transaction	bal _x = bal _x + 100	100
t ₅	write_lock(bal _x)	write(bal _x)	200
t ₆	WAIT	rollback/unlock(bal _x)	100
t ₇	read(bal _x)		100
t ₈	bal _x = bal _x - 10		100
t ₉	write(bal _x)		90
t ₁₀	commit/unlock(bal _x)		90

Figure 22.16 Preventing the uncommitted dependency problem.

Preventing the inconsistent analysis problem

To prevent this problem from occurring, T5 must precede its reads by exclusive locks, and T6 must precede its reads with shared locks.

Time	T ₅	T ₆	bal _x	bal _y	bal _z	sum
t ₁		begin_transaction	100	50	25	
t ₂	begin_transaction	sum = 0	100	50	25	0
t ₃	write_lock(bal _x)		100	50	25	0
t ₄	read(bal _x)	read_lock(bal _x)	100	50	25	0
t ₅	bal _x = bal _x - 10	WAIT	100	50	25	0
t ₆	write(bal _x)	WAIT	90	50	25	0
t ₇	write_lock(bal _z)	WAIT	90	50	25	0
t ₈	read(bal _z)	WAIT	90	50	25	0
t ₉	bal _z = bal _z + 10	WAIT	90	50	25	0
t ₁₀	write(bal _z)	WAIT	90	50	35	0
t ₁₁	commit/unlock(bal _x , bal _z)	WAIT	90	50	35	0
t ₁₂		read(bal _x)	90	50	35	0
t ₁₃		sum = sum + bal _x	90	50	35	90
t ₁₄		read_lock(bal _y)	90	50	35	90
t ₁₅		read(bal _y)	90	50	35	90
t ₁₆		sum = sum + bal _y	90	50	35	140
t ₁₇		read_lock(bal _z)	90	50	35	140
t ₁₈		read(bal _z)	90	50	35	140
t ₁₉		sum = sum + bal _z	90	50	35	175
t ₂₀		commit/unlock(bal _x , bal _y , bal _z)	90	50	35	175

Figure 22.17 Preventing the inconsistent analysis problem.

Deadlock

- **Deadlock** occurs when 2 or more transaction are in a simultaneous **wait state**.
- A deadlock occurs when two transactions wait indefinitely for each other to unlock data. For example, a deadlock occurs when two transactions, T1 and T2, exist in the following mode:
 - T1 = access data items X and Y
 - T2 = access data items Y and X
- Note that deadlocks are possible only when one of the transactions wants to obtain an exclusive lock on a data item; no deadlock condition can exist among shared lock.

Deadlock

Time	T ₁₇	T ₁₈
t ₁	begin_transaction	
t ₂	write_lock(bal_x)	begin_transaction
t ₃	read(bal_x)	write_lock(bal_y)
t ₄	bal_x = bal_x - 10	read(bal_y)
t ₅	write(bal_x)	bal_y = bal_y + 100
t ₆	write_lock(bal_y)	write(bal_y)
t ₇	WAIT	write_lock(bal_x)
t ₈	WAIT	WAIT
t ₉	WAIT	WAIT
t ₁₀	:	WAIT
t ₁₁	:	:

Deadlock Resolution

The system must **detect** and **break deadlocks** by the following strategies:

- Choosing one transaction as a **victim** and **rolling it back**.
- **Timing out** the transaction and returning an error.
- **Automatically restarting** the transaction hoping not to get deadlock again.
- Return an **error code** back to the **victim** and leaving it up to program to handle situation.