Starks Tang

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WORK EXPERIENCE

Chongzhipi CO., LT	Founder	2018.12 - Now

Deep Sentinel Corp.
San Francisco, California

Senior Hardware and Software Engineer — 2017.08 - 2018.08

Designed the first generation portable SOC multimedia electronics

Developed the design of image processing using Verilog on a SOC platform

Designed and verified firmware of the SOC platform

AspectMicrosystems, Corp.

IC Design Assistant Manager — 2014.05 - 2017.07

Designed the first generation portable SOC multimedia electronics

Developed the design of image processing using Verilog on a SOC platform

Designed and verified firmware of the SOC platform

Clipick Inc.

Senior Web Engineer — 2013.09 - 2013.12

Designed an intelligent advertising system for clothes to reduce search time and meet users' needs more precisely

Designed a web application programming interface (API) for clothing manufacturers to advertise their products and expand services

Designed a system which automatically updates the information on advertised garments, including their features

Freelancer — 2008.10 - 2013.09

Invented a search system integrated with both tag and hierarchical features

Obtained U.S. Patent and Taiwan R.O.C. Patent

Prolific Technology Inc.

Senior Digital IC Design Engineer — 2002.10 - 2008.10

Participated in system-on-chip(SOC) multi-media IC project

Designed multimedia SoC FPGA development boards

EDUCATION

July 2000 - July 2002

National Cheng Kung University, Taiwan, R.O.C. M.S., Electrical Engineering

July 1996 - July 2000

National Cheng Kung University, Taiwan, R.O.C. B.S. , Engineering Science

SKILLS

Software

Programming language

C, Ruby, Python, PHP

Client-side programming languages

Javascript, JQuery

Server-side programming languages on frameworks

Ruby on Sinatra/Rails, PHP on Codeigniter, and Python on WSGI

Database

Structured Query Language(SQL), MySQL, SQLite, DataMapper(Ruby-based)

Amazon Web Service(AWS)

EC2, S3, RDS

Web servers

Apache, Nginx, Thin(Ruby)

Application Programming Interface(API)

Designing and implementing services of REST APIs

App development tool for Android and Apple devices

PhoneGap, Apache Cordova, jQuery Mobile

IDE tools

JetBrains Rubymine, PyCharm, IntelliJ IDEA, PHPStorm, Apple Xcode

Hardware

Digital Integrated Circuit Design

FPGA hardware development, FPGA synthesis, and FPGA verification for SOC
Multi-channels-display in 4 /9 /16 split screens
On-screen display: Alpha Blending

Development Printed Circuit Board

ARM V4 processor with AMBA 2.0 interface
CMOS digital image sensor:
TV encoder/decoder
Stereo codec: