

Zachary Gray

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Objective

A **Digital Design** or related internship where I can expand my skills through meaningful contributions to projects and solutions.

Education

California Polytechnic State University, San Luis Obispo

Bachelor of Science in Electrical Engineering

GPA: 4.0 Major, 3.89 Overall

- Expected Graduation Date: June 2026
- Relevant Coursework: Digital Design, Fundamentals of Computer Science (Python), Electrical Manufacturing and Circuit Analysis
- President's List 2022-2023

Skills

Digital Design, **SystemVerilog**, RTL Design, Computer Arithmetic, Vivado Design Suite, Electrical Testing, Python

Experience

Power and Energy Society, Industry Coordinator, San Luis Obispo, CA

- February 2023 – Present
 - Coordinated events with industry leading companies including Eaton, SDG&E
 - Organized tour of local PG&E substation
 - Collaborated with fellow officers to run the society

ATS Automation, Controls Engineering Intern, Renton, WA

- June 2022 – August 2022
 - Developed 18 control system interfaces for \$400,000 school district project
 - Created 9 commercial building floorplan graphics using AutoCAD
 - Responsibly managed billable hours for client projects

Projects

LogicJackpot – Basys 3 FPGA Slot Machine

- Implemented a Clock Divider, Finite-state Machine, Linear Feedback Shift Register (Pseudo-RNG), MUXs, and Registers
- Designed, Simulated, Synthesized, and Implemented with SystemVerilog in Vivado
- Utilized 3 external modules through port-mapping

SmartSentry – Arduino Motion Detecting Anti-Theft Device

- Processed data from 3-axis accelerometer to assess movement
- Utilized IR sensor and remote for control

For videos and the code behind these projects, please see: <https://z4chh.github.io/resumesite/>