```
          Current settings 7/2020

              Port Select
Bus
Addr   Switch  Bit   Position  Status  Current Port selected ==> 0
 --      S1    ---     --       ---
 A2 --> S2      4      ON       Low
 A3 --> S3      8      ON       Low    PSG (A) address <== Current Port
 A4 --> S4     16      ON       Low    PSG (B) address <== Current Port + 1
 A5 --> S5     32      ON       Low
 A6 --> S6     64      ON       Low
 A7 --> S7    128      ON       Low
 --      S8    ---     --       ---

 A1 --> PSG select   0 (Low)  --> PSG (A)
                     1 (High) --> PSG (B)


 A0 --> PSG register 0 (Low)  Address (write only)
                     1 (High)   data (read/write)


----------------------------------------------------------------------
Sol-20 Reserved Ports

0FA   Status Port General
0F8   Serial Status Port
0F9   Serial Data
0FD   Parallel Data
0FC   Keyboard Data
0FE   VDM Control Port
0FA   Tape Status Port
0FB   Tape Data Port
0FF   Sense Switches
----------------------------------------------------------------------

          Wait States

CPU Clock
   1 MHz    Open (Trace Cut)
   2 MHz    Jumpered for use on Sol-20
   4 MHz    Open
```