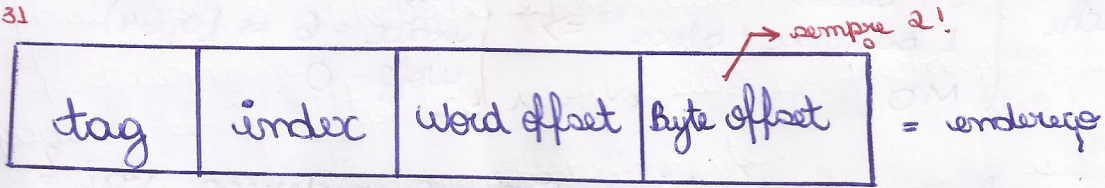
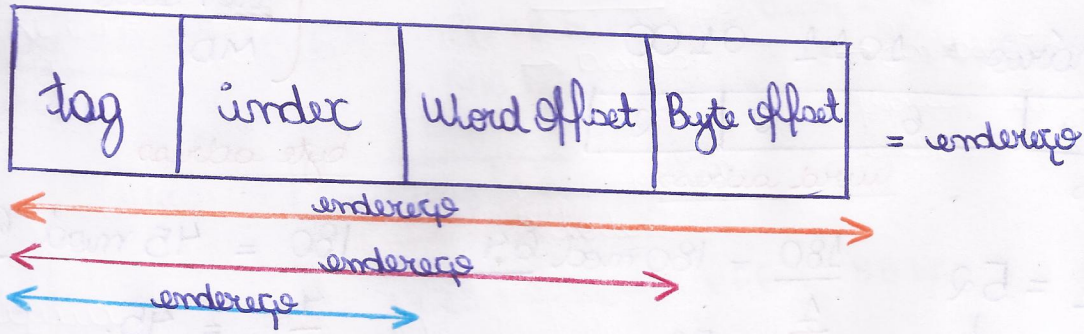


Mapeamento Direto

→ Definição: cada endereço da memória mapeia para um único bloco.



→ Block address vs Word Address vs Byte Address



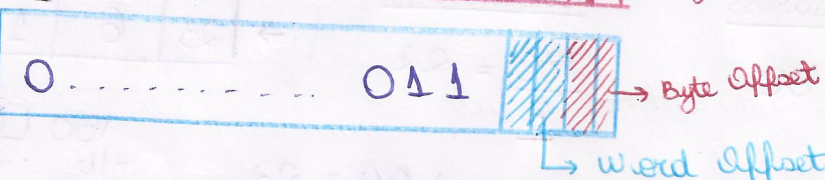
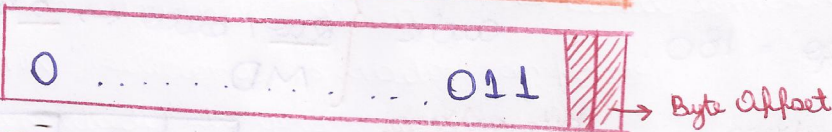
ou seja:

"tampa" os bits de Byte Offset (30 bits)

"tampa" os bits de Word Offset e de Byte Offset (30 - bits Word Offset)

os 32 bits representam o endereço.

Ex: endereço (inteiro) = 32 bits e Word Offset = 2 bits.



→ Descobrimos a quantidade de bits para tag, index e word offset.

Cache: $\left\{ \begin{array}{l} n \text{ blocos} \\ m \text{ palavras por bloco} \\ \text{mapeamento direto} \end{array} \right.$

=>

$\left\{ \begin{array}{l} \text{tag: } 32 - 2 - \log_2 n - \log_2 m \\ \text{index: } \log_2 n \\ \text{word: } \log_2 m \end{array} \right.$

Ex 1: Cache $\left\{ \begin{array}{l} 1024 \text{ blocos} \\ 4 \text{ palavras / bloco} \\ \text{MD} \end{array} \right. \Rightarrow \left\{ \begin{array}{l} \text{tag} = 32 - 2 - 10 - 2 = 18 \\ \text{index} = 10 \rightarrow [0, 1024) \\ \text{woff} = 2 \rightarrow [0, 4) \end{array} \right.$

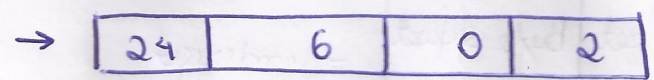
Ex 2: Cache $\left\{ \begin{array}{l} 64 \text{ blocos} \\ 1 \text{ palavra / bloco} \\ \text{MD} \end{array} \right. \Rightarrow \left\{ \begin{array}{l} \text{tag} = 32 - 2 - 6 - 0 = 24 \\ \text{index} = 6 \rightarrow [0, 64) \\ \text{woff} = 0 \end{array} \right.$

→ Como achar o índice onde o endereço vai?

Ex 1: endereço = 180.

Cache $\left\{ \begin{array}{l} 64 \text{ blocos} \\ 4 \text{ w / bloco} \leftrightarrow 4 \text{ bytes / bloco} \\ \text{MD} \end{array} \right.$

binário = 1011 0100



block address

word address

byte address

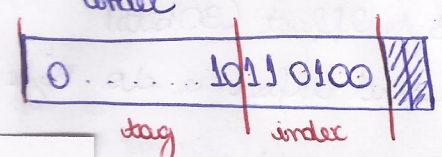
$180 \bmod 64 = 52$

$$\begin{array}{r} 71 \\ 180 \overline{) 64} \\ - 128 \\ \hline 52 \end{array} \quad \textcircled{2}$$

↓
index

$\frac{180}{1} = 180 \bmod 64$

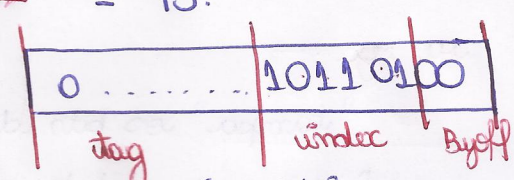
$\frac{1}{1} = 52$
↓
index



tag
index = 52
tag = 2

$180 = 45 \bmod 64$

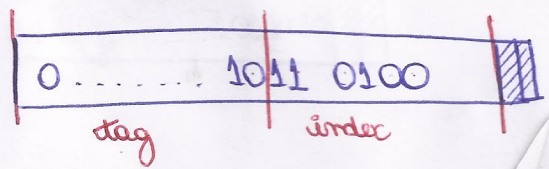
$\frac{180}{4} = 45$



index = $543210 = 32 + 8 + 4 + 1 = 45$

tag = 0

$$\begin{array}{r} 543210 \\ 45 \overline{) 64} \\ - 45 \\ \hline 19 \end{array} \quad \textcircled{0}$$



index = $110100 = 32 + 16 + 4 = 52$

tag = 0...10 = 2

Ex 2: endereço = 180.

Cache $\left\{ \begin{array}{l} 64 \text{ blocos} \\ 2 \text{ w / bloco} \leftrightarrow 8 \text{ bytes / bloco} \\ \text{MD} \end{array} \right.$

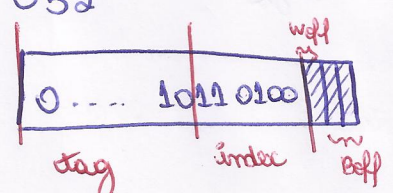
block address

word address

byte address

$180 \bmod 64 = 52$

$$\begin{array}{r} 81 \\ 180 \overline{) 64} \\ - 128 \\ \hline 52 \end{array} \quad \textcircled{2}$$

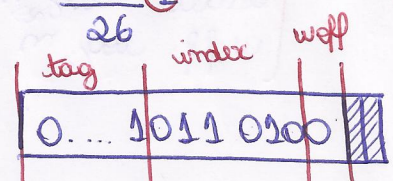


index = 52

tag = 2

$180 = 90$

$$\begin{array}{r} 81 \\ 90 \overline{) 64} \\ - 64 \\ \hline 26 \end{array} \quad \textcircled{1}$$



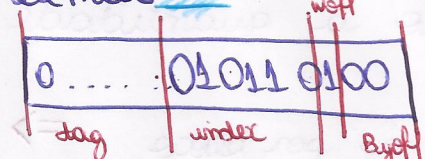
index = $011010 = 16 + 8 + 2 = 26$

woff = 0 → $180 \bmod 2$
tag = 1

$180 = 22$

$\frac{180}{8} = 22$

$22 \bmod 64 = 22$

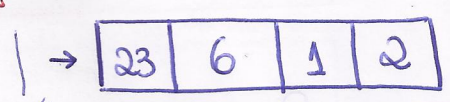


index = $010110 = 22$

woff = 1 → $180 \bmod 8$

tag = 0

$$\begin{array}{r} 180 \overline{) 8} \\ - 16 \\ \hline 20 \\ - 16 \\ \hline 4 \end{array} \quad \textcircled{0}$$



Quantidade de Bits que uma cache precisa

① Cache { 64 blocos
1w/bloco
MD

② Cache { 64 blocos
2w/bloco
MD

$i \in [0, 64)$

Cache 1: Bloco i :

válido	tag	word 0
--------	-----	--------

$\leftarrow 1 \rightarrow \leftarrow 24 \rightarrow \leftarrow 32 \rightarrow$

24	6	0	2
----	---	---	---

Bits = $1 + 24 + 32 = 57$ Bits
Bloco

Quantidade de Bits = $57 \cdot 64 = 3648$ bits
Cache = 456 bytes.

Cache 2:

Bloco i :

válido	tag	word 0	word 1
--------	-----	--------	--------

$\leftarrow 1 \rightarrow \leftarrow 23 \rightarrow \leftarrow 32 \rightarrow \leftarrow 32 \rightarrow$

23	6	1	2
----	---	---	---

Bits = $1 + 23 + 2 \cdot 32 = 24 + 64 = 98$ bits
Bloco

Quantidade de Bits = $98 \cdot 64 = 6272$ bits
Cache = 784 bytes.

Obs: Cache size (K.B) \rightarrow quantidade de dados em bytes
bits

Cache 1: $32 \cdot 64 = 2^5 \cdot 2^6 = 2^6 \text{ Kbits}$
= 2 Kbits.

Cache 2: $2 \cdot 32 \cdot 64 = 2^1 \cdot 2^5 \cdot 2^6$
= $2^{12} = 4 \text{ Kbits}$.