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1 -----
 2 -- Engineer: Zachary Montoya
3 -- Submitted Date: 12-15-22
4 -- Module Name:
                   Top - Behavioral
5 -- Project Name: Asteroids
6 -- Target Devices: Zybo Z7-10
7 -- Tool versions: Vivado 2020.2
9 -- Comment:
10 --
      This file was entirely created the engineer listed above.
11 --
12
13
14 --
15 --
  ______
16 library ieee;
17 use ieee.std_logic_1164.all;
18 use ieee.numeric_std.all;
19
20 entity walls is
21
     port(
22
        clk, reset: in std logic;
23
        -- btn: in std_logic_vector(3 downto 0); --R2 increasing the BTN array to 3
24
        video_on: in std_logic;
25
        pixel_x, pixel_y: in std_logic_vector(9 downto 0);
        wall1_on_out, wall2_on_out, wall3_on_out, wall4_on_out: out std_logic;
26
        wall1_rgb_out, wall2_rgb_out, wall3_rgb_out, wall4_rgb_out: out
  std_logic_vector(2 downto 0);
28
        Left_Wall_Out, Right_Wall_Out, Bottom_Wall_Out, Top_Wall_Out: out
  std_logic_vector(9 downto 0)
29
   );
30 end walls;
31
32 --
  ===========
33
34 architecture rtl of walls is
35
36 -- Signal used to control speed of ball and how often pushbuttons are checked for
  paddle movement.
37
     signal refr_tick: std_logic;
38
39 -- x, y coordinates (0,0 to (639, 479)
40
     signal pix_x, pix_y: unsigned(9 downto 0);
41
     signal wall1_on, wall2_on, wall3_on, wall4_on: std_logic;
42
     signal wall1_rgb, wall2_rgb, wall3_rgb, wall4_rgb: std_logic_vector(2 downto 0);
     signal Left_Wall, Right_Wall, Bottom_Wall, Top_Wall: std_logic_vector(9 downto 0);
43
44
45 -- Screen dimensions
     constant MAX_X: integer := 640;
46
     constant MAX_Y: integer := 480;
47
48
49 -- WALL1 - LEFT
50
     constant WALL1 X L: integer := 0;
51
     constant WALL1_X_R: integer := 20;
52
53 -- WALL2 - RIGHT
54
     constant WALL2_X_L: integer := 619;
55
     constant WALL2_X_R: integer := 639;
56
57 -- WALL3 - BOTTOM
58
     constant WALL3_X_T: integer := 409;
59
     constant WALL3_X_B: integer := 479;
60
61 -- WALL4 - TOP
62
     constant WALL4_X_T: integer := 0;
63
     constant WALL4_X_B: integer := 20;
64
65
  _____
66
     begin
67
     wall1_on_out <= wall1_on;</pre>
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68
      wall2_on_out <= wall2_on;</pre>
69
      wall3 on out <= wall3 on;</pre>
70
      wall4_on_out <= wall4_on;</pre>
71
      -- bar_on_out <= rom_bar_on;</pre>
 72
      -- asteroid2_on_out <= rd_ball_on;</pre>
73
74
      wall1_rgb_out <= wall1_rgb;</pre>
75
      wall2_rgb_out <= wall2_rgb;</pre>
76
      wall3 rgb out <= wall3 rgb;</pre>
77
      wall4_rgb_out <= wall4_rgb;</pre>
      Left Wall Out <= Left Wall;
78
79
      Right_Wall_Out <= Right_Wall;</pre>
80
      Bottom_Wall_Out <= Bottom_Wall;</pre>
81
      Top_Wall_Out <= Top_Wall;</pre>
      Left_Wall <= std_logic_vector(to_unsigned(WALL1_X_R,10));</pre>
82
83
      Right_Wall <= std_logic_vector(to_unsigned(WALL2_X_L,10));</pre>
84
      Bottom_Wall <= std_logic_vector(to_unsigned(WALL3_X_T,10));</pre>
85
      Top_Wall <= std_logic_vector(to_unsigned(WALL4_X_B,10));</pre>
86
87 --
   ______
   -----
88
      pix_x <= unsigned(pixel_x);</pre>
89
      pix_y <= unsigned(pixel_y);</pre>
91 -- Refr_tick: 1-clock tick asserted at start of v_sync, e.g., when the screen is
   refreshed -- speed is 60 Hz
     refr_tick <= '1' when (pix_y = 1) and (pix_x = 1) else '0';</pre>
93
94 --
   ______
95 -- wall1 left vertical stripe
96 wall1_on <= '1' when (WALL1_X_L <= pix_x) and (pix_x <= WALL1_X_R) else '0'; --
  convert pix_x to pix_y to make horizontal
    wall1_rgb <= "011"; -- paddle colors blue</pre>
98 -- wall2 right vertical stripe
99 wall2_on <= '1' when (WALL2_X_L <= pix_x) and (pix_x <= WALL2_x_R) else '0';
    wall2_rgb <= "011"; -- paddle colors blue</pre>
100
101 -- wall3 left vertical stripe
    wall3_on <= '1' when (WALL3_X_T <= pix_y) and (pix_y <= WALL3_X_B) else '0'; --
102
   convert pix x to pix y to make horizontal
103
      wall3_rgb <= "011"; -- paddle colors blue</pre>
104 -- wall4 right vertical stripe
      wall4_on <= '1' when (WALL4_X_T <= pix_y) and (pix_y <= WALL4_X_B) else '0';
105
      wall4_rgb <= "011"; -- paddle colors blue</pre>
106
108 end rtl;
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