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# Zachary Stow

## EDUCATION

### City College of San Francisco - San Francisco, CA - Electrical Engineering

- Received A.S. in Mathematics from CCSF
- GPA: 3.65

### San Jose State University - San Jose, CA - Electrical Engineering

- GPA: 3.73

## SKILLS

**Languages:** Verilog, Python, C++

**Tools:** FPGA Design - Xilinx, Ltspice, Microsoft office, AutoCAD, Oscilloscope

## Projects

- Solar Regatta competition - Solar powered one man boat
- Pulse Width Modulated Digital to Analog Converter

## Classes

- Silicon Wafer
- Digital Logic Circuit Design, Digital Signal Processing, Circuits and Systems, Physical Electronics, Microprocessor System Design

## EE Portfolio

- <https://zachs321.github.io/>

## WORK EXPERIENCE

### Private tutoring

January 2014 - Current

- Math and circuit analysis

### Student Instructor, City College of San Francisco

January 2014 - May 2014

- Worked with Dr. Drucker in aiding his students with additional help outside of class.
- Would meet privately with his students or during a small lecture once a week.

### Teacher Assistant, BEACON After School Program (Everett Middle School)

Sept. 2013 - December 2013

- Volunteered alongside teachers to help tutor their students during homework hour.
- Monitored and worked with kids during recreation hour (skateboarding, football, dodgeball etc).

## REFERENCES

**Keith J Mueller**  
Eng. Prof. (CCSF)  
(415)452-4729  
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**Chris Garcia**  
Cassava Owner  
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**Robert Novota**  
Teacher at Galileo High School  
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