



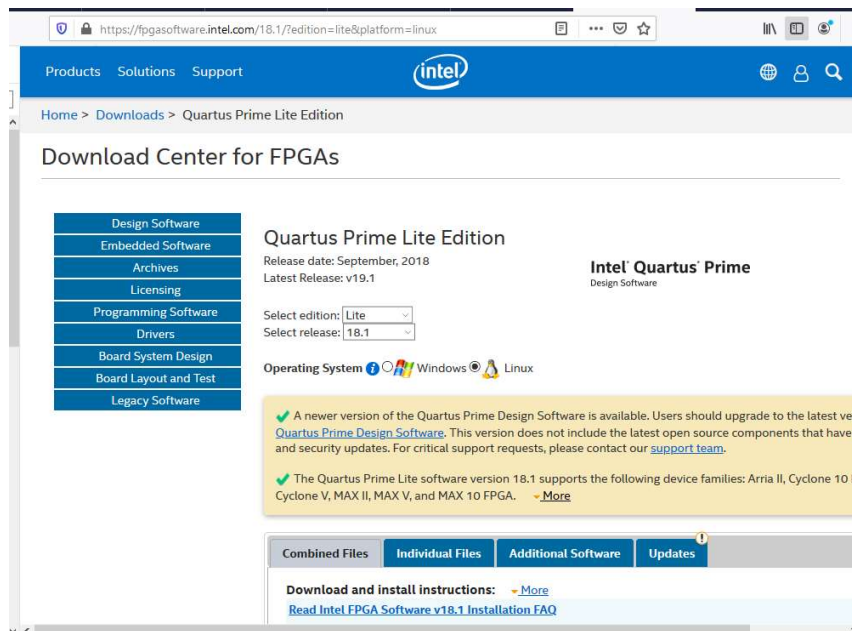
Quick Start Quartus

Create a VHDL project

Configure Design tools



Create the project:

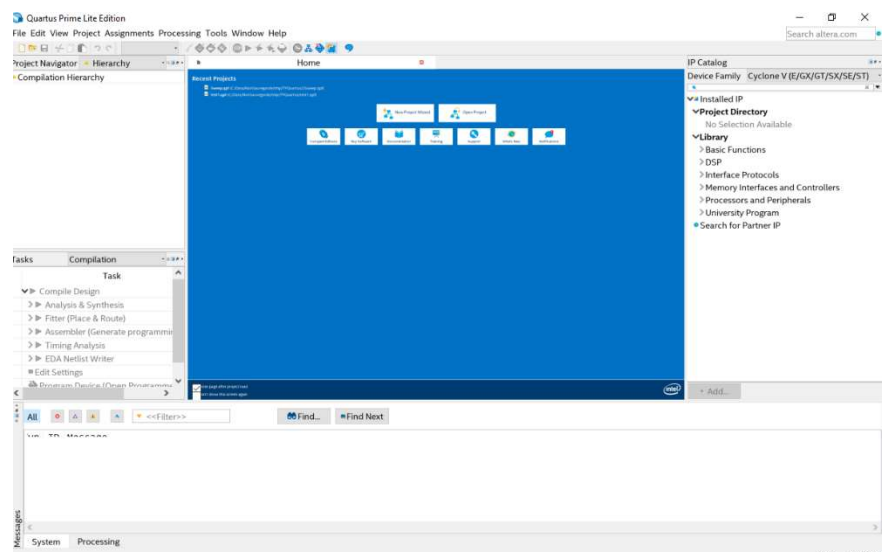


Download the Quartus Prime Lite Edition : (To find link google : “intel quartus prime lite”)

This tutorial uses the version 18.1

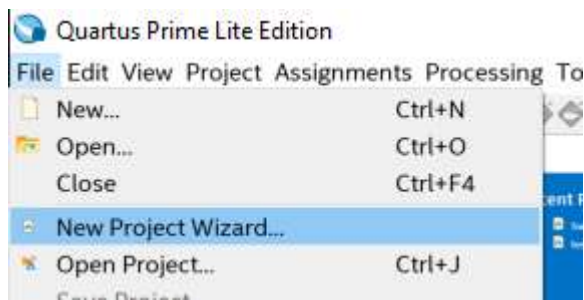
If you choose another version, some windows or options may differ.

Install Quartus and launch it. You should see something like this:



Create A VHDL Project

V1.0



Select "File->New Project Wizard"

Select your project Folder.

**WARNING :**

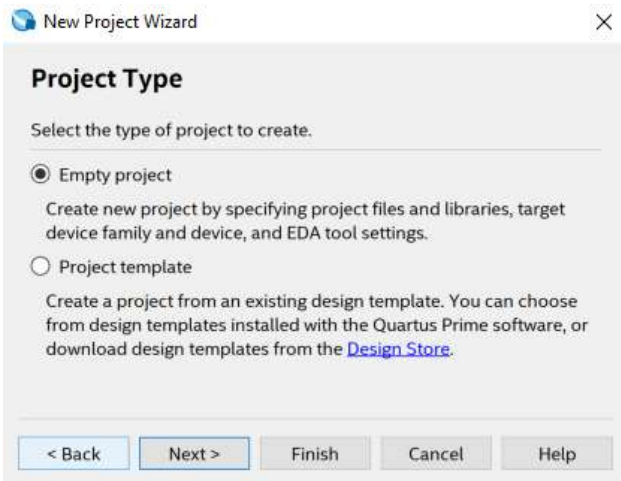
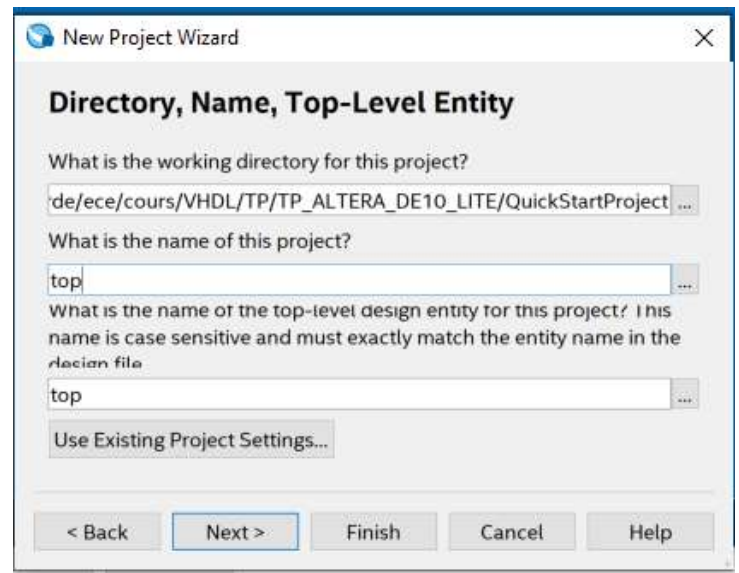
In the path name, don't use :

- any blank (space)
- accentuation

Only use:

- "a...z"
- "A...Z"
- "0...9"
- " \_ "

Click "Next"

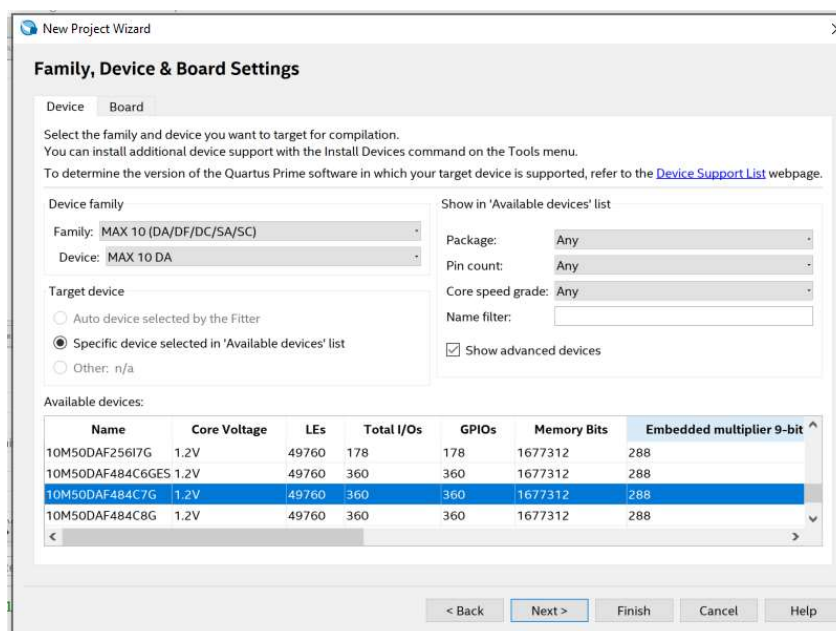
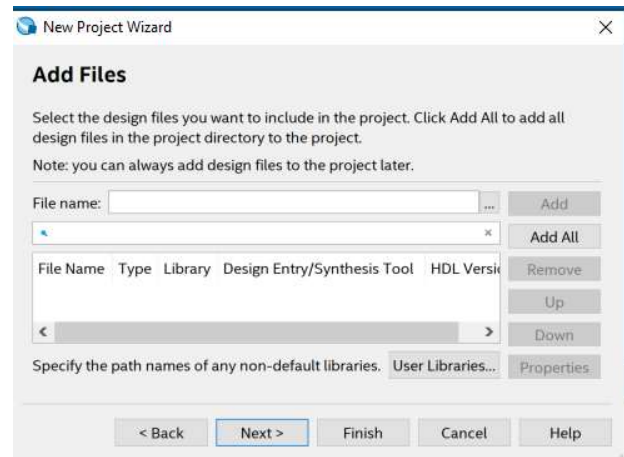


Select "Empty project"

Click "Next"

No file to Add.

Click “Next”



Select the FPGA used on the DE10 LITE Board:

Family : MAX

Device : 10 DA

Available Device:

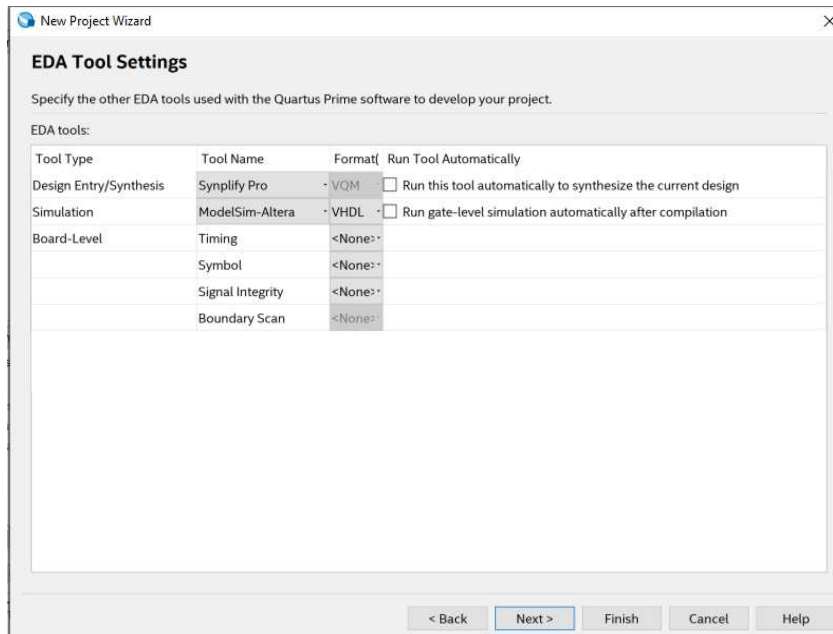
10M50DAF484C7G

The device reference is written on the component itself.

Look at the Altera Max10 component.

Click “Next”





Choose the EDA (Electronic Design Automation) tools:

“Synplify pro” for the synthesis

“ModelSim-Altera” for the simulation

Click “Next”

You should see :

Click “Finish”

