

DateCode

PCB108

Factory LOGO

PCB109

UL MARK

PCB110

PCB102


PCB103

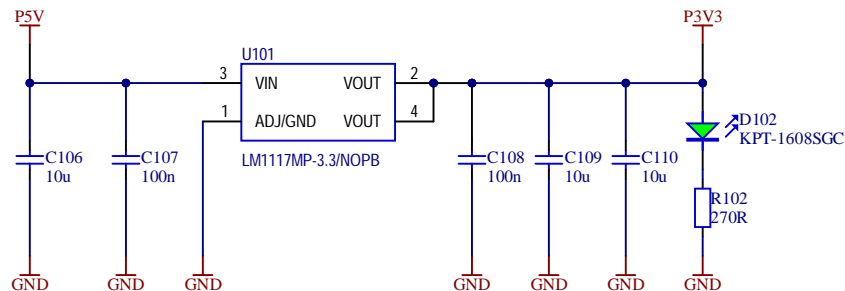
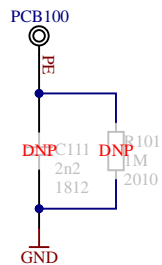
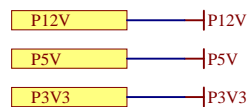
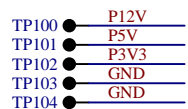
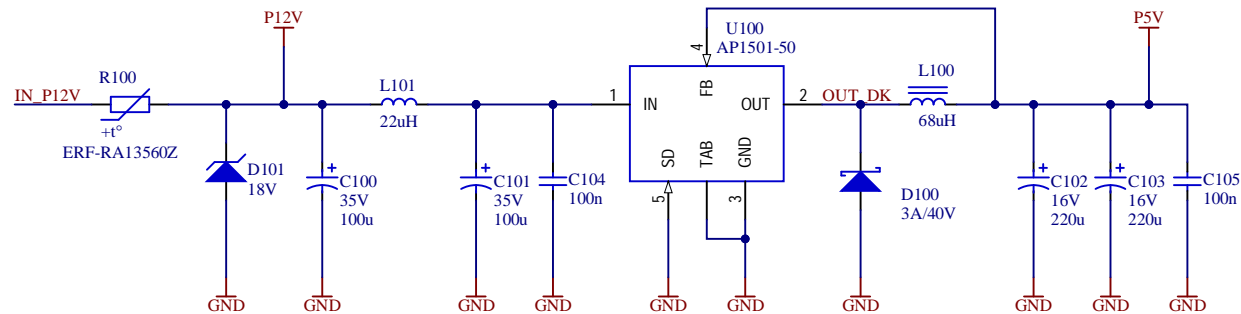
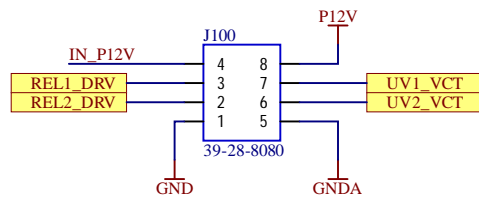
PCB104


PCB105

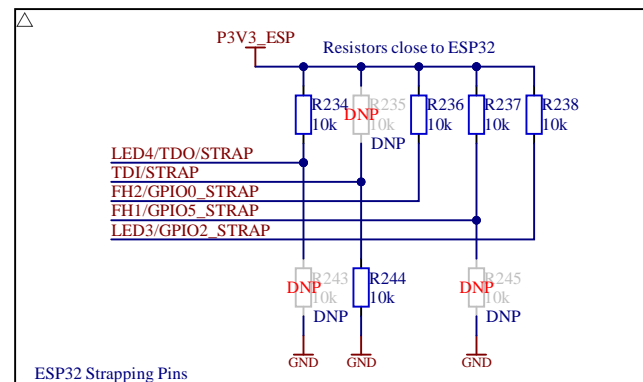
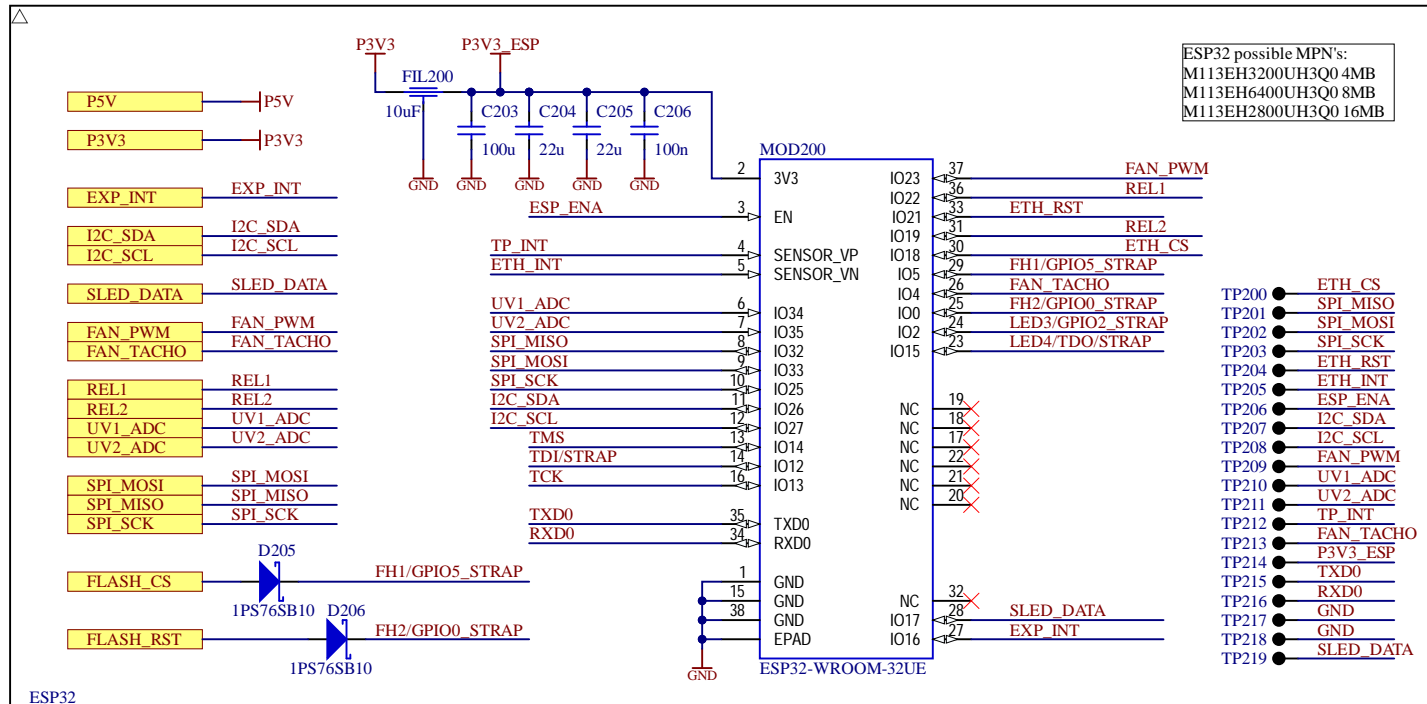
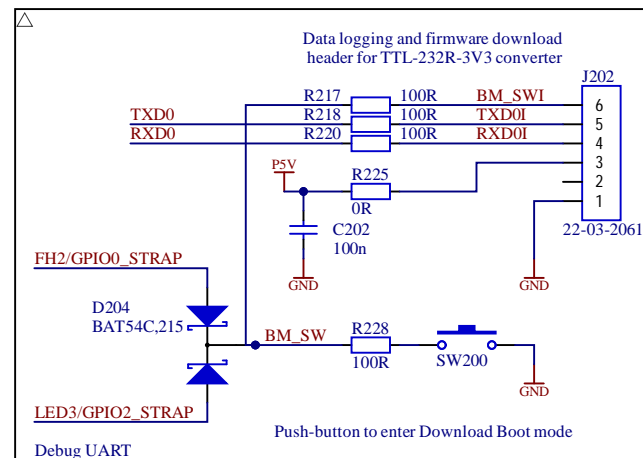
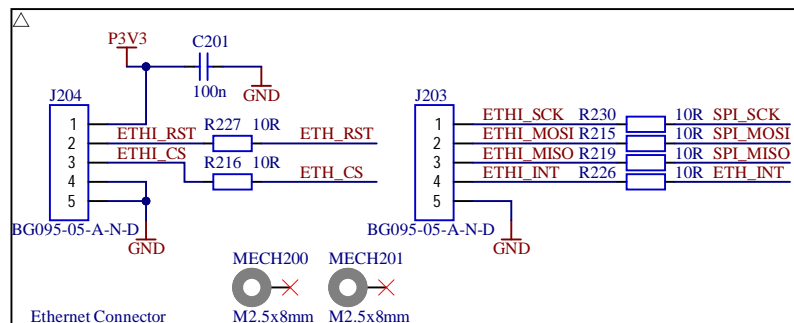
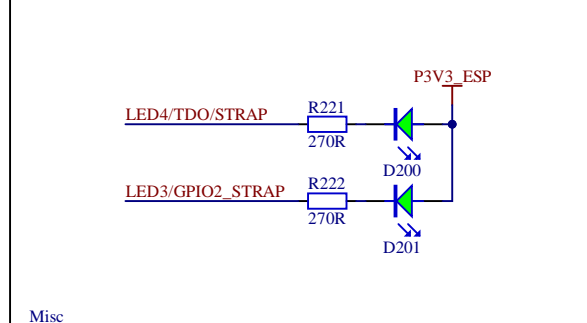
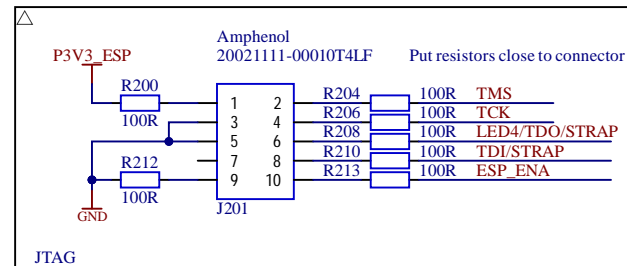
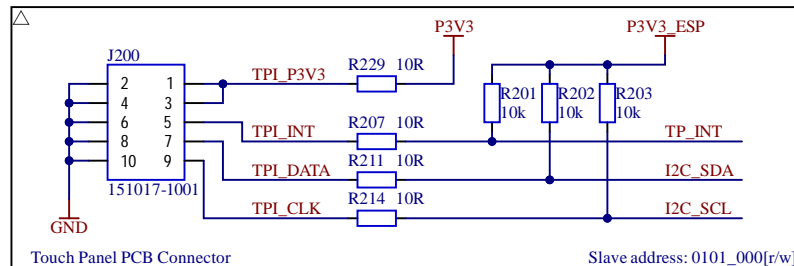
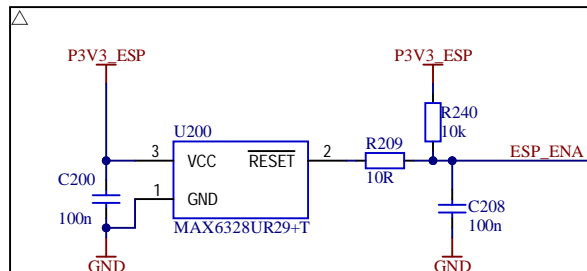
PCB106

PCB107

 Fideltronik		Fideltronik Poland, R&D Office Cystersów 19, 31-553 Kraków		Size A4
Title Main Board - TOP				Version 1.1
Project: 10665tco21		RefDes:		Revision B
Variant: Production		Sheet: 1 / 10		
Designer: PZ		Update: 2022-05-11		
File: Main_SchDoc				



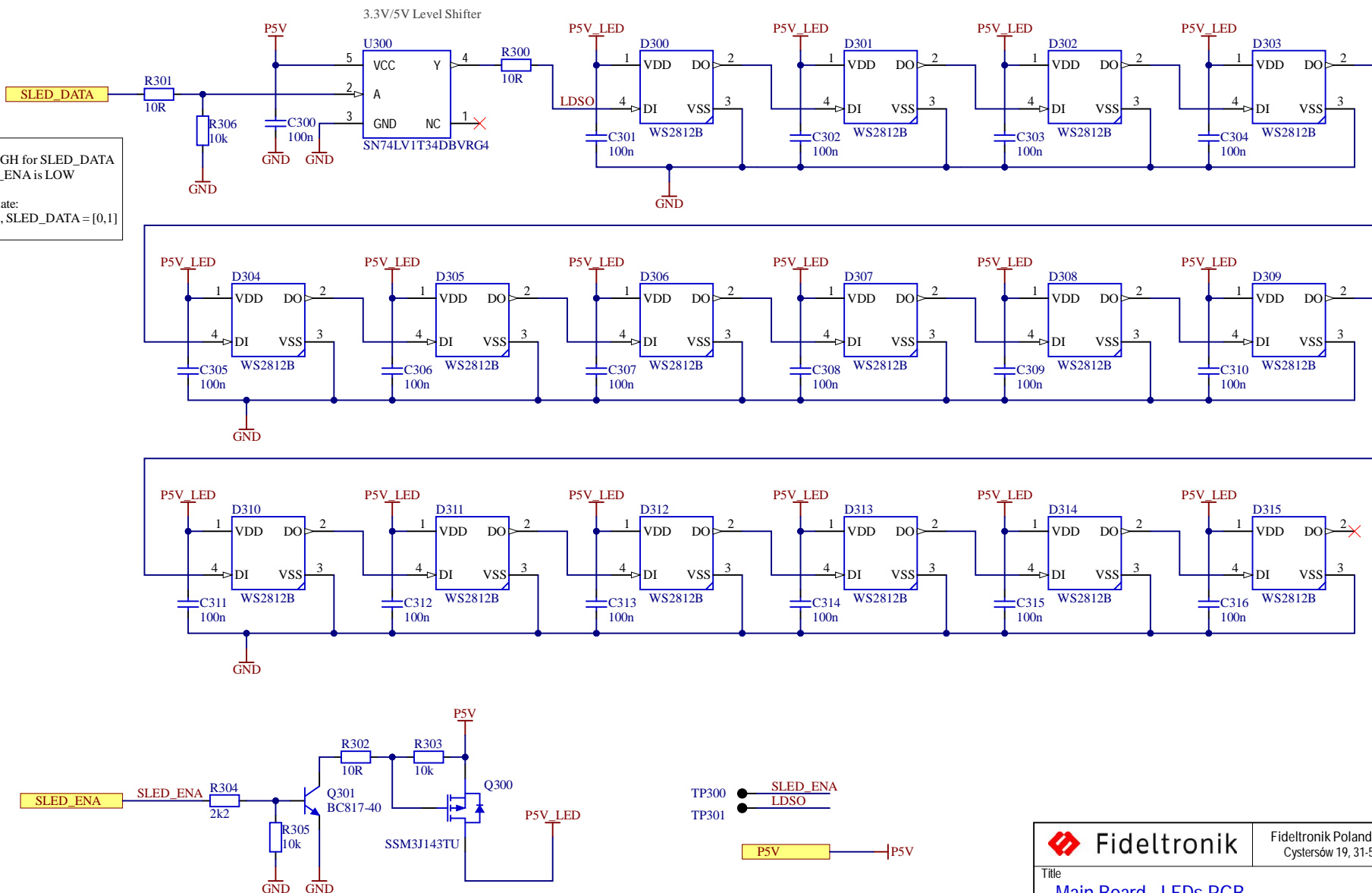
 Fideltronik		Fideltronik Poland, R&D Office Cystersów 19, 31-553 Kraków	Size A4
Title Main Board - Power Supply			Version 1.1
Project:	10665tco21		Revision B
Variant:	Production		
Designer:	PZ		
File:	PSU.SchDoc		
		RefDes:	
		Sheet:	2 / 10
		Update:	2022-05-11




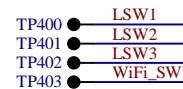
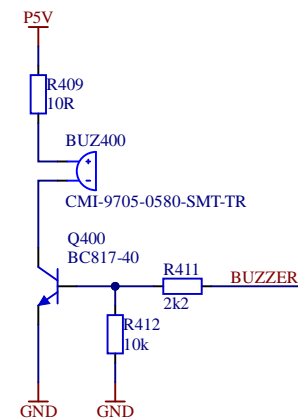
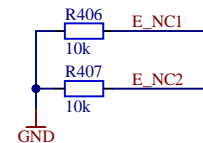
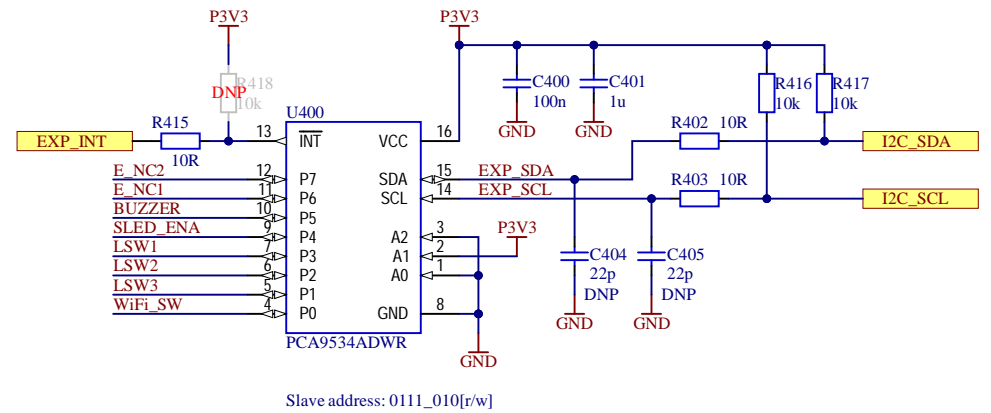
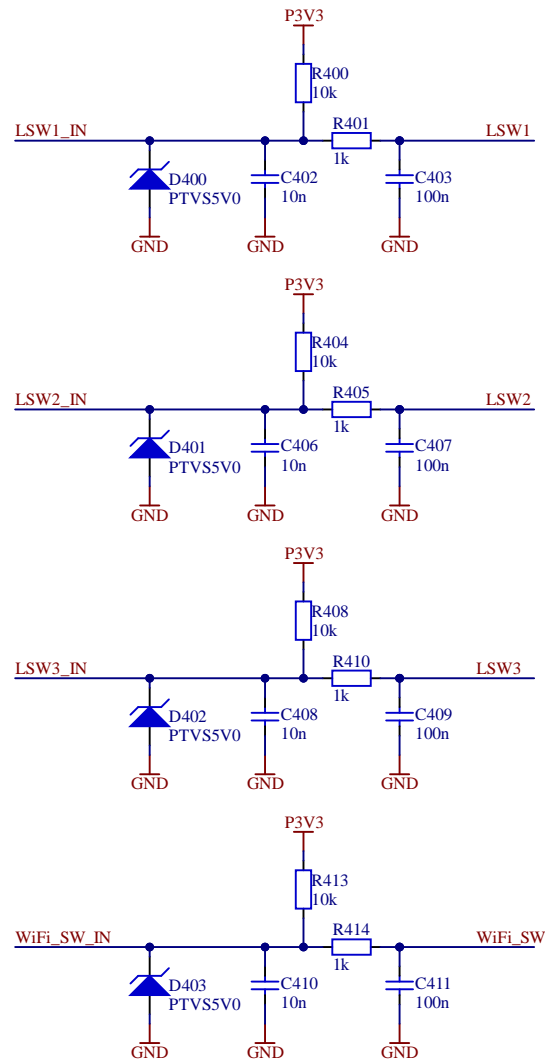
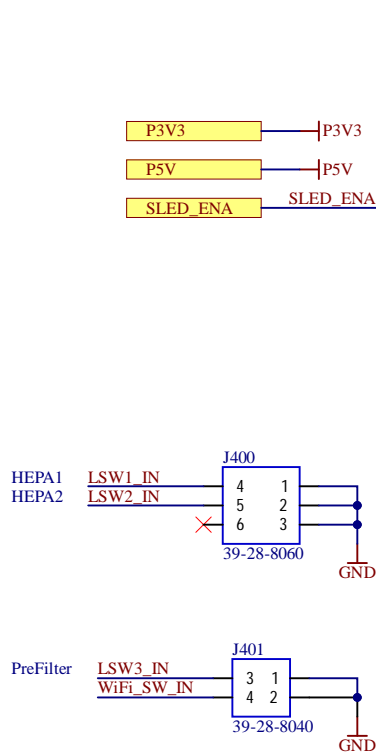
Fideltronik		Fideltronik Poland, R&D Office Cystersów 19, 31-553 Kraków	Size A4
Title Main Board - ESP32		Version 1.1	Revision B
Project: 10665tco21	Variant: Production	RefDes:	
Designer: PZ	Sheet: 3 / 10	Update: 2022-05-11	


SW note:
Never set HIGH for SLED_DATA
when SLED_ENA is LOW

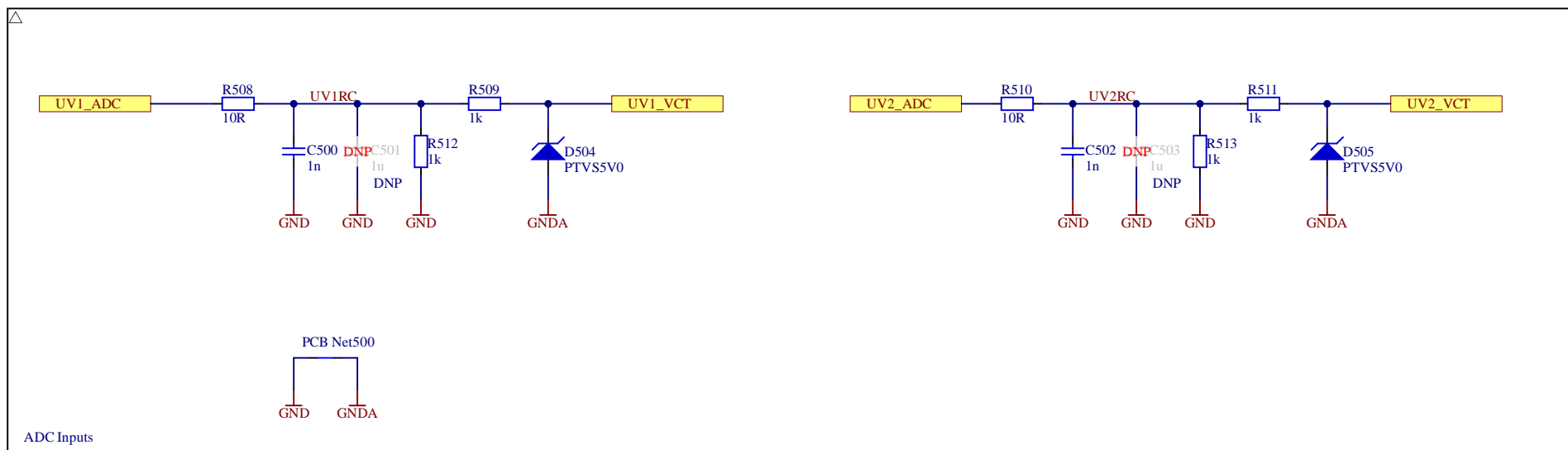
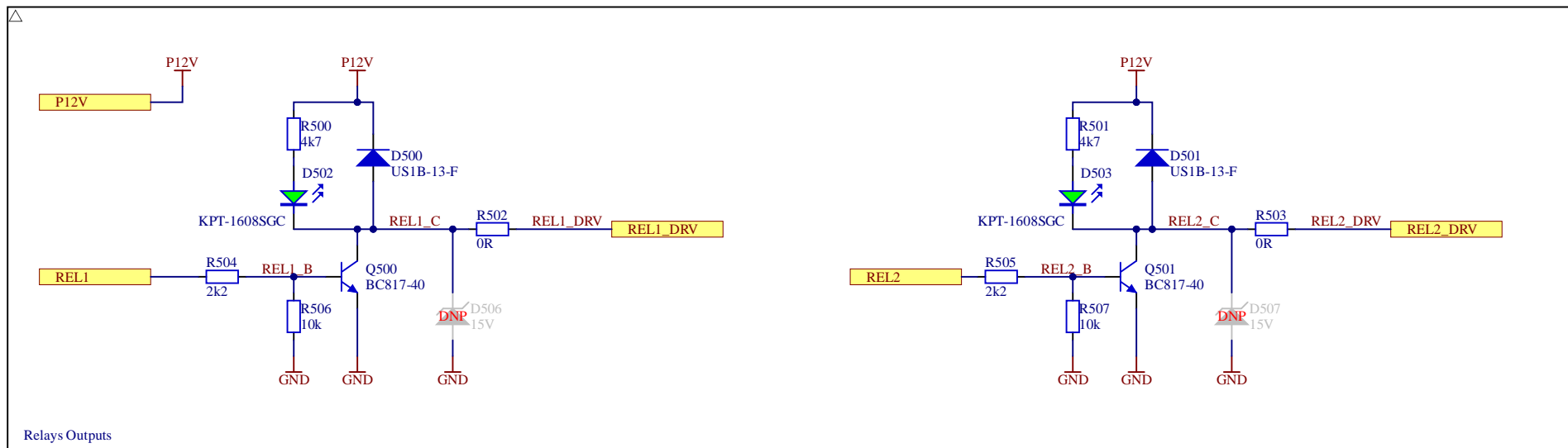
Prohibited state:
SLED_ENA, SLED_DATA = [0,1]



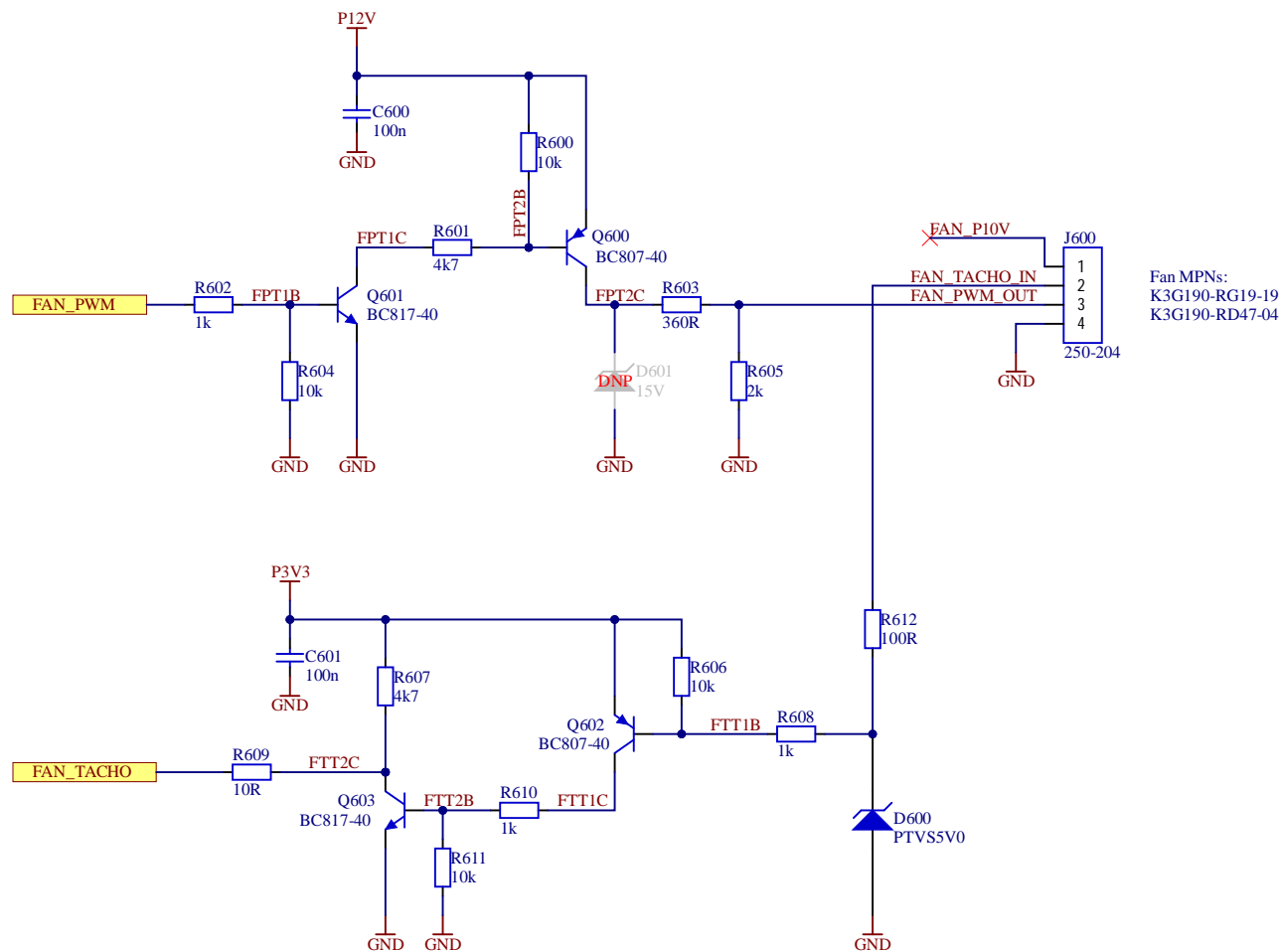
 Fideltronik		Fideltronik Poland, R&D Office Cystersów 19, 31-553 Kraków	Size A4
Title Main Board - LEDs RGB			Version 1.1
Project: 10665tco21		RefDes: Sheet: 4 / 10 Update: 2022-05-11	Revision B
Variant: Production			
Designer: PZ			
File: LEDs.SchDoc			



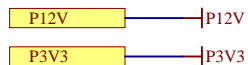
 Fideltronik		Fideltronik Poland, R&D Office Cystersów 19, 31-553 Kraków	Size A4
Title Main Board - IO Expander			Version 1.1
Project: 10665tco21		RefDes:	Revision B
Variant: Production			
Designer: PZ		Sheet: 5 / 10	
File: IO Expander.SchDoc		Update: 2022-05-11	




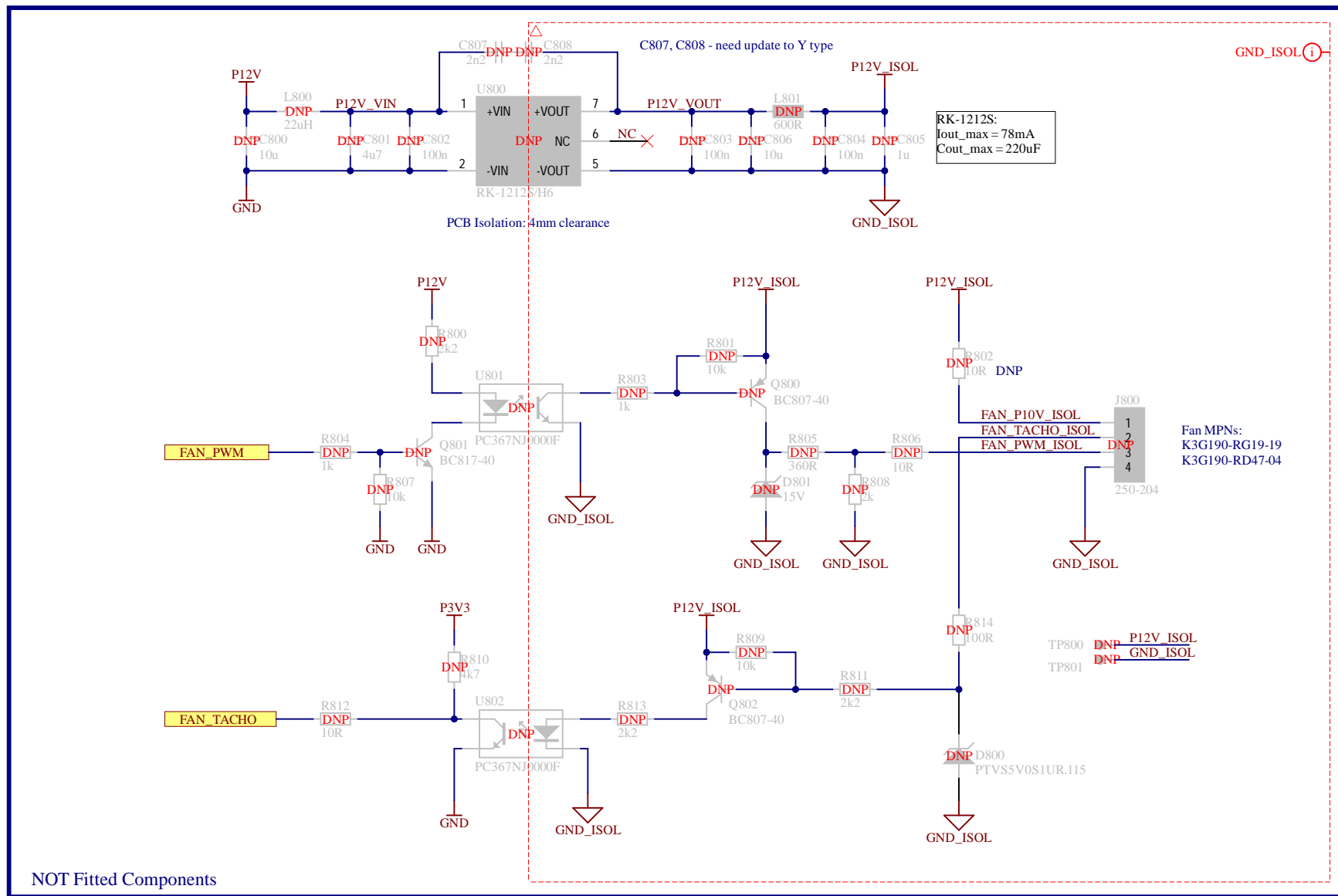
		Fideltronik Poland, R&D Office Cystersów 19, 31-553 Kraków	Size A4
Title		Version 1.1	
Main Board - Relays Outputs / ADC Inputs		Revision B	
Project:	10665tco21	RefDes:	
Variant:	Production	Sheet:	6 / 10
Designer:	PZ	Update:	2022-05-11
File:	Relays.SchDoc		

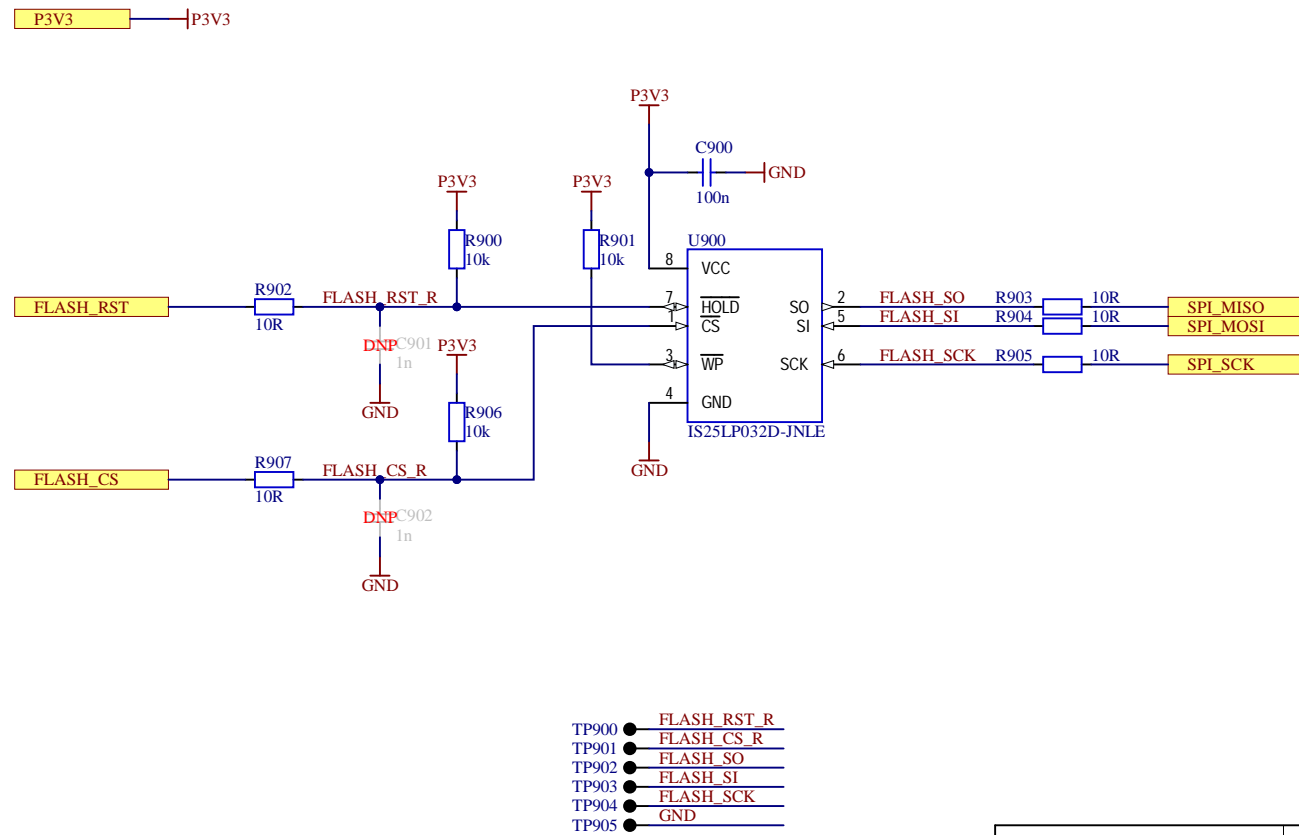



Fan MPNs:
K3G190-RG19-19
K3G190-RD47-04



 Fideltronik		Fideltronik Poland, R&D Office Cystersów 19, 31-553 Kraków	Size A4
Title Main Board - FAN Control			Version 1.1
Project: 10665tco21		RefDes: Sheet: 7 / 10 Update: 2022-05-11	Revision B
Variant: Production			
Designer: PZ			
File: FAN.SchDoc			





 Fideltronik		Fideltronik Poland, R&D Office Cystersów 19, 31-553 Kraków	Size A4
Title Main Board - Flash Memory			Version 1.1
Project: 10665tco21		RefDes: Sheet: 10 / 10 Update: 2022-05-11	Revision B
Variant: Production			
Designer: PZ			
File: FLASH.SchDoc			