

Memory Bus Bit Transition Statistics for 531-deepsjeng-r-4430399

August 15, 2018

1 Scalar Statistics

1. L3 miss count: 621100713
2. L3 store evict count: 492467575
3. Total number of bit transitions: 86142764226
4. Bit entropy: 0.172673

2 Graphs

The graphs give the data either **transfer-wise** or **bus-wise**. Bus-wise refers to data sent within the same transfer (i.e. as part of the same bus transfer, as defined by bus-width). Transfer-wise refers to data from different bus transfers, which cause the bit transitions to occur.

2.1 Distribution of all byte values transmitted over the bus

Figures 1 and 2 give the distribution of all transmitted bytes, either through a memory read or a memory write. The first one (pareto chart) is in terms of total number of occurrences, while at the same time showing the accumulation in the total percentage of transmitted byte values. The second one displays the absolute percentage of the most popular values transferred over the bus.

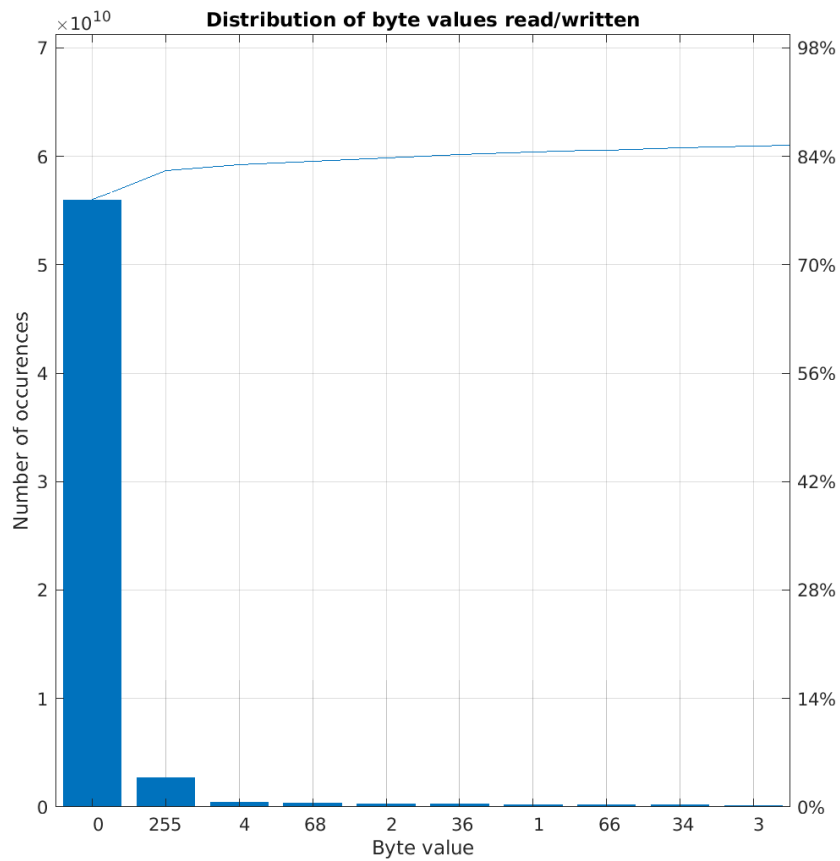


Figure 1: Distribution of all bytes transmitted over the bus - pareto chart

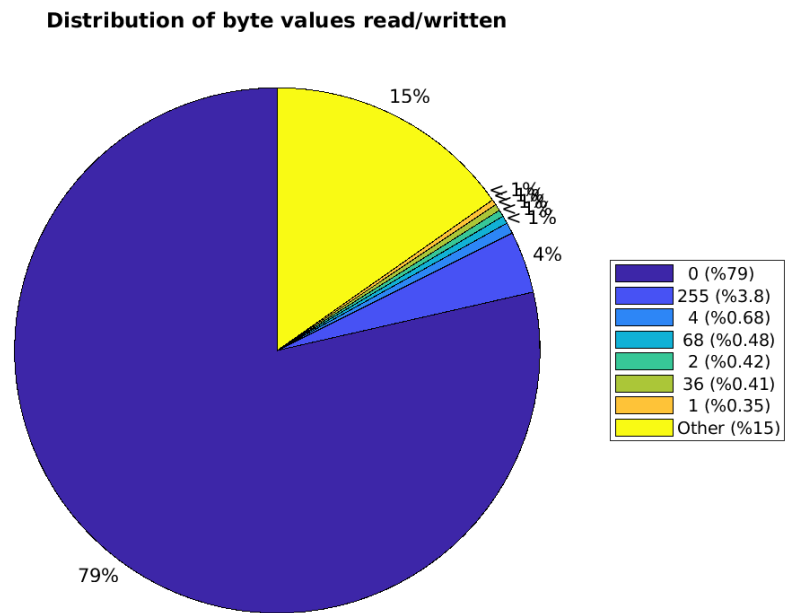


Figure 2: Distribution of all bytes transmitted over the bus - pie chart

2.2 Distribution of repeated byte values in each bus transfer (bus-wise)

Figure 3 gives the number of repeated byte values in each bus transfer.

Distribution of repeated byte values in each bus transfer - bus-wise

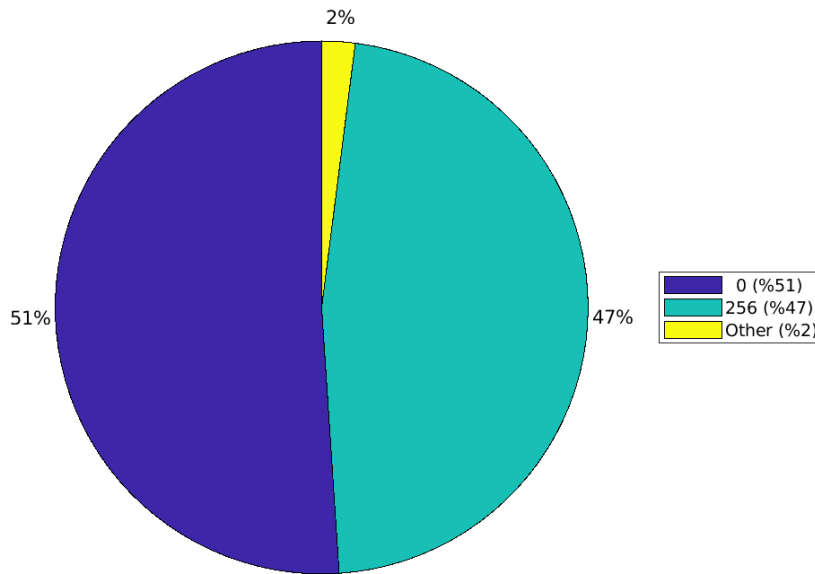


Figure 3: Distribution of repeated byte values in each bus transfer - bus-wise

2.3 Distribution of transitions (transfer-wise)

2.3.1 All transfers

Figure 4 illustrates the most popular transitions happened during a whole benchmarking. Tuples (n, n) have been used as (old_value, new_value) to show the transition.

Distribution of transitioning bytes - transfer-wise

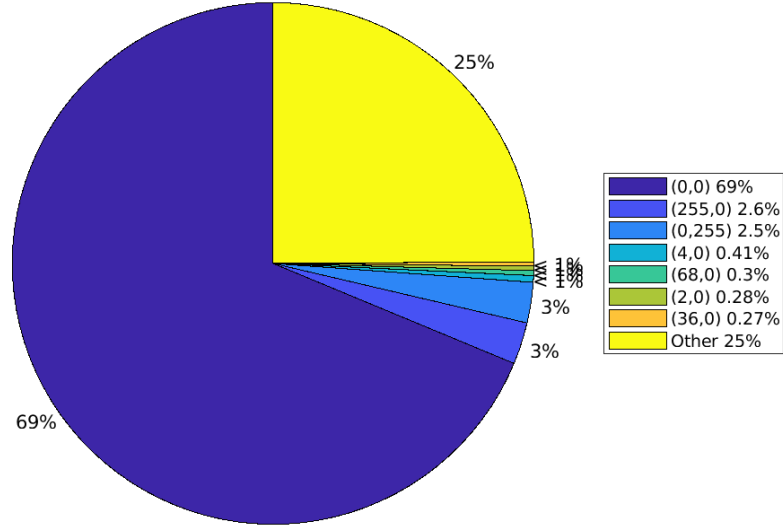


Figure 4: Distribution of transitioning bytes - transfer-wise

2.3.2 Only differing byte transfers (hamming distance > 0)

Figure 5 illustrates the most popular transitions happened during a whole benchmarking. Tuples (n, n) have been used as (old_value, new_value) to show the transition.

Distribution of transitioning bytes - transfer-wise (only differing bytes)

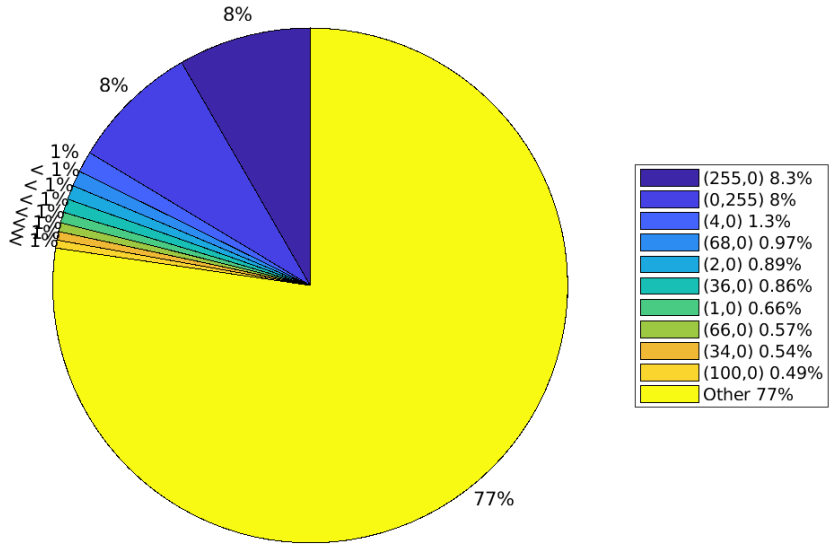


Figure 5: Distribution of transitioning bytes - transfer-wise (only differing bytes)

2.3.3 Only differing byte transfers (hamming distance > 0)

Figure 6 illustrates only the non-changing byte transfers, within a whole benchmarking transfer-wise, but this time the percentages displayed are in proportion to this.

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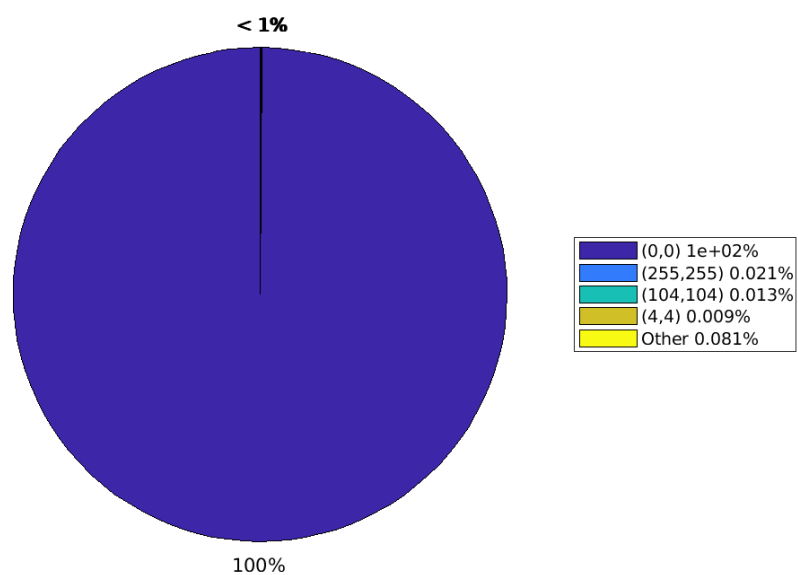


Figure 6: Distribution of transitioning bytes - transfer-wise (only same byte transitions)