

Laboratory Work: Thread-Safe Data Structures

1. Student Information

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2. Task Condition (Variant 11)

Develop a thread-safe data structure with `m=3` integer fields.

- Requirements:** Independent `read / write` for fields, and `operator string` for the whole structure.
- Constraint:** No `std::atomic`, only mutexes.
- Workloads:**
 - Scenario A (Var 11):** Weighted distribution.
 - Scenario B:** Equal distribution.
 - Scenario C (Worst Case):** 90% `string` operations (global lock).

3. Data Protection Scheme

The solution uses **Fine-Grained Locking** optimized for Apple Silicon.

- Per-Field Mutexes:** Each field has its own `std::mutex`.
 - Justification:* Allows threads to access different indices simultaneously without blocking.
- Global Snapshot (`operator string`):** Uses `std::scoped_lock` to acquire all mutexes at once.
 - Justification:* Ensures a consistent state (atomic snapshot) and prevents deadlocks.
- Cache Line Alignment (`alignas(128)`):**
 - Justification:* Prevents "False Sharing" on the M1 processor by keeping mutexes on separate cache lines.

4. Experimental Results (Time in ms)

Scenario	1 Thread	2 Threads	3 Threads
A (Variant 11)	[FILL_VAL]	[FILL_VAL]	[FILL_VAL]
B (Equal)	[FILL_VAL]	[FILL_VAL]	[FILL_VAL]
C (Worst Case)	[FILL_VAL]	[FILL_VAL]	[FILL_VAL]

(Fill in values from program output)

5. Conclusions

- Scenarios A & B:** Performance improves with more threads as operations target different fields (low contention).
- Scenario C:** Performance degrades or plateaus because the `string` operation locks the entire structure, effectively serializing execution.
- Optimization:** The architecture-specific alignment was critical to minimize hardware-level cache contention.

6. Individual Contribution

- Designed `ConcurrentStructure` using fine-grained locking strategy.
- Implemented memory alignment to optimize for CPU cache lines.
- Developed the `TestUtils` module for generating weighted test files.
- Created the benchmarking logic to measure execution time across multiple threads.