California State University
EGCP 450
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Lab 3
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INTRODUCTION:

This experiment I have been advised to make a traffic light using hardware components and software, my design was based on a Finite State Machine that would switch through states and flip different LEDs on and off.

Procedure/Discussion:

First part of this lab was to make a traffic light from 2 sides west and south.

I first started by building the hardware components used :

- a. Red Led x2
- b. Green Led x2
- c. Yellow Led x2
- d. 450 ohm resistors x6
- e. 10 k ohm resistors x2
- f. Switches x2

I have connected the LEDs to the 450 ohm resistors to make a pull down resistors since we are making a positive logic output

The switches are also connected to the 10 k ohm resistors.

By designing the hardware in the beginning I was able to choose the ports on the MSP432 . Port 4 was used to Output data to the LEDs the first 6 pins were used .

Port 5 was used to Input data from the switches to the msp432 pins 5.1 and 5.2 were used for the switches the switches act as sensors for the cars.

Port 1 was used to Input data from the pedestrian light the msp432 uses negative logic so I coded my port to have a pull up resistor.

Lastly port 2 was used to output the light for the pedestrian to walk or not to walk.

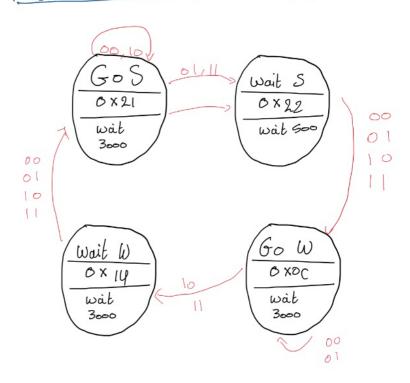
After figuring out which ports and pins to use everything else was a little bit more straight forward.

I have started part 1 of the lab to design the 2 way traffic light by building the finite state machine diagram and state transition diagram the diagram had 2 inputs from the switches and 6 outputs to the LEDs the design had 4 transitions which are shown below:

F 4.D 6 P 4.1 y Joutput 40-60 P 4.2 R P 5.2 Button Input	Bits 76543210 Go 5 100010 Wait 5 100010 ROB W	
P. 4.3 6 Touteut P. 4.9 8 Touteut P. 4.5 R P. 5. Button Input	Waita 010100	22

Finite State Machine

_			_			
(Input	No cors	west	South	Both	Tuble
	60 3	G0 5	wait S	605	wait 5	100.
	wait 5	Go W	Go W	60 W	G w	
	60 W	Gow	Go W	Wait W	waitw	
	wait W				Gos	



After this part was designed and tested, I started designing part 2 of the lab which was basically add-ons from the first part of the lab.

I started with a new finite state machine diagram which had the same inputs and outputs as before but in addition to the pedestrian traffic light which had 3 inputs and 8 outputs this design is shown below:

design is shown below.									
	-c Machine	_	. Jrh	50 soft w	et/walk	work so	th esan	ſ	
	cors	inst toffon	butto le	0		Ma.	5 4	th & w	
Input"	000	001	00				The same of the sa	Rels	
10 603	905	Waits	905	' Out	10	* V			
1 wait 5	gew	90 W	gow	gow	Rugul		Real W	Regu	
2 60 W	geW	90W	waith	washin	Redu	Redw	Reg S	Rel 5	
3 wait W.		905	905		Reas	Rely S	Reas	Redis	
u walk	walk	numy	norry	MUM	Na[x	unison.	vaugos	millou	
1 5 horgani	hus goff	RUVEY	horry	hysry	hurr	hurry	hurry		
26 horrage	013								
3 7 horry on	off 9							1	
8 huridy	019								
4 hurry	1								
lo Truffed 6	ONI								
(1 mild of	088	B			1)		
12 hurry &	019						1	+	
13 hurg	on				,				-
horeg	off	08	Off.		l				
Nulla		60W		605	6°S	Pah	905	650	+
walk	G 65	Gon	605	605	Wall	walk	Walk	i Walh	
walki	100		600	Gow		1 1 1 1 1 1 1 1 1	wax	Walk	
Red	605	60 BO	665		- 1		wait w	waitu	
Red		5 60	5 (60)	G01	m m	0 0	walks	In alt W)

W Part 2
Bits 176543210 Go 5 100001 91
71
Waita 010100 DC / 01
0 1
TION L
H013 24, 000
1007
HOND 29/ 60
Hoff 6
HON7 24, 00 1
FIOH8 24, 00
FIOIL 1
Hoff 24100
W W W 19, 81
Q C W
21,01
Port 4 Port 2
t

As we can see the first table provides how my design works the rows show the state that the system is in and shows what states it transitions to the column shows the inputs that our system has.

The second table shows the outputs that are given to the LED.

Code:

```
#include <stdint.h>
#include "SysTick.h"
#include "msp432p401r.h"
struct State {
    uint32 t Out;
    uint32_t Outw;
    uint32_t Time; // 10 ms units
    const struct State *Next[8];
             };
typedef const struct State styp;
#define GOS
                &FSM [0]
#define WAITS
                &FSM[1]
#define GOW
                &FSM[2]
#define WAITW
                &FSM[3]
#define WALK
                &FSM[4]
#define HON1
                &FSM[5]
#define HOFF2
                &FSM[6]
#define HON3
                &FSM[7]
#define HOFF4
                &FSM[8]
#define HON5
                &FSM[9]
#define HOFF6
                &FSM[10]
#define HON7
                &FSM[11]
#define HOFF8
                &FSM[12]
#define HON9
                &FSM[13]
#define HON
                &FSM[14]
#define HOFF
                &FSM[15]
#define WAITWW
                &FSM[16]
#define WAITWS
                &FSM[17]
#define REOW
                &FSM[18]
#define REQS
                &FSM[19]
styp FSM[20] = {
 {0x21,0x01,100,{GOS,WAITS,GOS,WAITS,REQS,REQS,REQS,REQS}},
                                                              //1 gos
 {0x22,0x01,100,{GOW,GOW,GOW,REQS,REQS,REQS,REQS}},
                                                              //2 waitS
 {0x0C,0x01,100,{GOW,GOW,WAITW,WAITW,REQW,REQW,REQW,REQW}},
                                                              //3 goW
 {0x14,0x01,100,{GOS,GOS,GOS,REQW,REQW,REQW,REQW}},
                                                              //4 waitW
 {0x24,0x02,100,{WALK,HON1,HON1,HON1,WALK,HON1,HON1,HON1}},
                                                               //WALK
 {0x24,0x01,100,{H0FF2,H0FF2,H0FF2,H0FF2,H0FF2,H0FF2,H0FF2,H0FF2}},
//HURRYON1
 {0x24,0x00,100,{H0N3,H0N3,H0N3,H0N3,H0N3,H0N3,H0N3,H0N3}},
//HURRYOFF2
```

```
{0x24,0x01,100,{H0FF4,H0FF4,H0FF4,H0FF4,H0FF4,H0FF4,H0FF4,H0FF4}},
//HURRYON3
 {0x24,0x00,100,{H0N5,H0N5,H0N5,H0N5,H0N5,H0N5,H0N5,H0N5}},
//HURRYOFF4
 {0x24,0x01,50,{H0FF6,H0FF6,H0FF6,H0FF6,H0FF6,H0FF6,H0FF6}},
//HURRYON5
 \{0 \times 24, 0 \times 00, 50, \{HON7, HON7, HON7, HON7, HON7, HON7, HON7, HON7\}\}
//HURRYOFF6
 {0x24,0x01,50,{H0FF8,H0FF8,H0FF8,H0FF8,H0FF8,H0FF8,H0FF8}},
//HURRYON7
 \{0 \times 24, 0 \times 00, 50, \{HON9, HON9, HON9, HON9, HON9, HON9, HON9\}\},
                                                                   //HURRYOFF8
 //HURRYON9
 \{0\times24,0\times00,50,\{HON,HON,HON,HON,HON,HON,HON,HON,\}\},
                                                                    //HURRYON
 {0x24,0x01,100,{GOS,GOW,GOS,GOS,GOS,GOW,GOS,GOS}},
                                                                   //HURRYOFF
 {0x14,0x01,100,{GOS,GOW,GOS,GOS,WALK,WALK,WALK,WALK}},
                                                                   //WAITWALKS
 {0x22,0x01,100,{GOS,GOW,GOS,GOW,WALK,WALK,WALK,WALK}},
                                                                  //WAITWALKW
 {0x0C,0x01,100,{GOS,GOW,GOS,GOS,WAITWW,WAITWW,WAITWW,}},
                                                                         //REQW
 {0x21,0x01,100,{GOS,GOS,GOS,GOW,WAITWS,WAITWS,WAITWS}}
                                                                         //REQS
              };
void ports(void){
   //output pins
        P4SEL0 &= \sim 0 \times 3F;
        P4SEL1 &= \sim 0 \times 3F;
        P4DIR \mid = 0 \times 3F;
   // output led
        P2SEL0 &= \sim 0 \times 03;
        P2SEL1 &= ~0x03;
        P2DS
                | = 0 \times 03;
        P2DIR
                 |= 0 \times 03;
        P20UT
                |= 0x03;
   //input buttons
        P1SEL0 = 0 \times 00;
        P1SEL1 = 0 \times 00;
        P1DS
                = 0 \times 00;
        P1DIR = 0 \times 00;
        P1REN
               = 0X02:
        P10UT
                = 0 \times 02;
   //input buttons
        P5SEL0 &= ~0x06;
        P5SEL1 &= ~0x06;
        P5DIR &= \sim 0 \times 06;
void main(void){
    uint32_t Input;
    uint32_t Input2;
    styp *Pt;
    SysTick_Init();
    ports();
    Pt = GOS; // start state
while(1){
             P40UT=(Pt \rightarrow Out);
```

```
P20UT=((Pt -> Outw)&0x03);
SysTick_Wait10ms(Pt->Time);
Input = P5IN&06;
Input = Input/2;
Input2 = ((~P1IN&0x02) <<1 );
Pt = Pt->Next[Input2 + Input];
}
}
```

The first part of the code shows the defining of our states, the second part shows the transitions from state to state, the third part shows the initialization of the ports, lastly we see the main part of our code where we get the inputs and according to the we get the output from the Finite State machine, we also used the Systick timer to provide delay to the system.

In my Finite State Machine design there are 20 states there are 4 main states for the two traffic lights which I started my first program with after that I added 2 requests for the states if a pedestrian wants to request to walk and after that another 2 states which waits for the button to still be pressed which provides the 2 second delay for the press, the other states are basically walk for the pedestrians as well as hurry for the pedestrians which turns on and off the light and speeds up at the end.

This makes up a design of 20 states, in my design I used pointers and a linked data structure to achieve my finite state machine.

The only problem that I had while designing this system was with port 5 the first pin wasn't reading my data correctly, so I changed the pins and divided the value by 2 to make it appropriate for reading.

After designing the first part of the lab the second part was only a matter of designing the transition diagram and figuring out the rest of the input and output for the pedestrian, the most time-consuming part of this lab was doing the table since it was a little bit long but other than that everything was pretty straight forward.

After finishing all these parts my system was tested with all of the possible inputs, my system transitions between all of the states with no problem it transitions correctly according to the diagram I have provided.

Improvements:

Since I have started with the design on my notes and every input, output and code was translated from visual diagrams my code worked correctly and no improvements were required.

The only part of the lab that was required to be altered with is the delay from each state to state to make it appropriate for the demonstration video.

Analysis:

My design of the Finite State Machine used a moore machine since our output depends on the current state only.

If I were to use a mealy machine we would've seen a direct connection between the inputs and the output.

The data that should be collected to make sure my system works correctly is first of all the time delay that was assigned to each state and make sure the inputs are read correctly as well as the outputs if everything matches what the diagram has then this proves all of my system is working correctly.

My Finite state machine has 20 states which are discussed earlier in the report.

Fewer states may be used to but I believe I wouldn't have achieved the time delay that I wanted for the walk and hurry states

What data to check:

My design works correctly and for demonstration purposes I have reduced the delay time to demonstrate the effectiveness of my design.

My design right now the signals change if both sensors are active change every 1 second. If one car per second arrives at each intersection, I would have an average of 2 car lined up at each intersection and again this is for demonstration purposes.

If 2 cars arrive per second, then I would have an average of 4 cars lined up at each intersection. Crossing the street would take an average of 2 seconds and again this is for demonstration purposes.

Conclusion:

Lab 3 really helped me understand how Finite State Machine works in a real time embedded system the difference between mealy and moore machines. Timing and delay in such situations are important to be taken into consideration in these type of systems for they assure the safety for the users of the road, the safety of these users rely a lot on the timing and delay of such systems and off course the working condition of these systems.

If I were to do this experiment, I wouldn't change anything since I had everything right from the beginning no glitches or bugs were found in the system from my side of the development.

Reference:

material provided by the professor.

Students that helped me with understanding how some ports needed to be coded David Mouser

Moniel Flores