

1 2 5

Sheet Title: FPGA1.SCHDOC

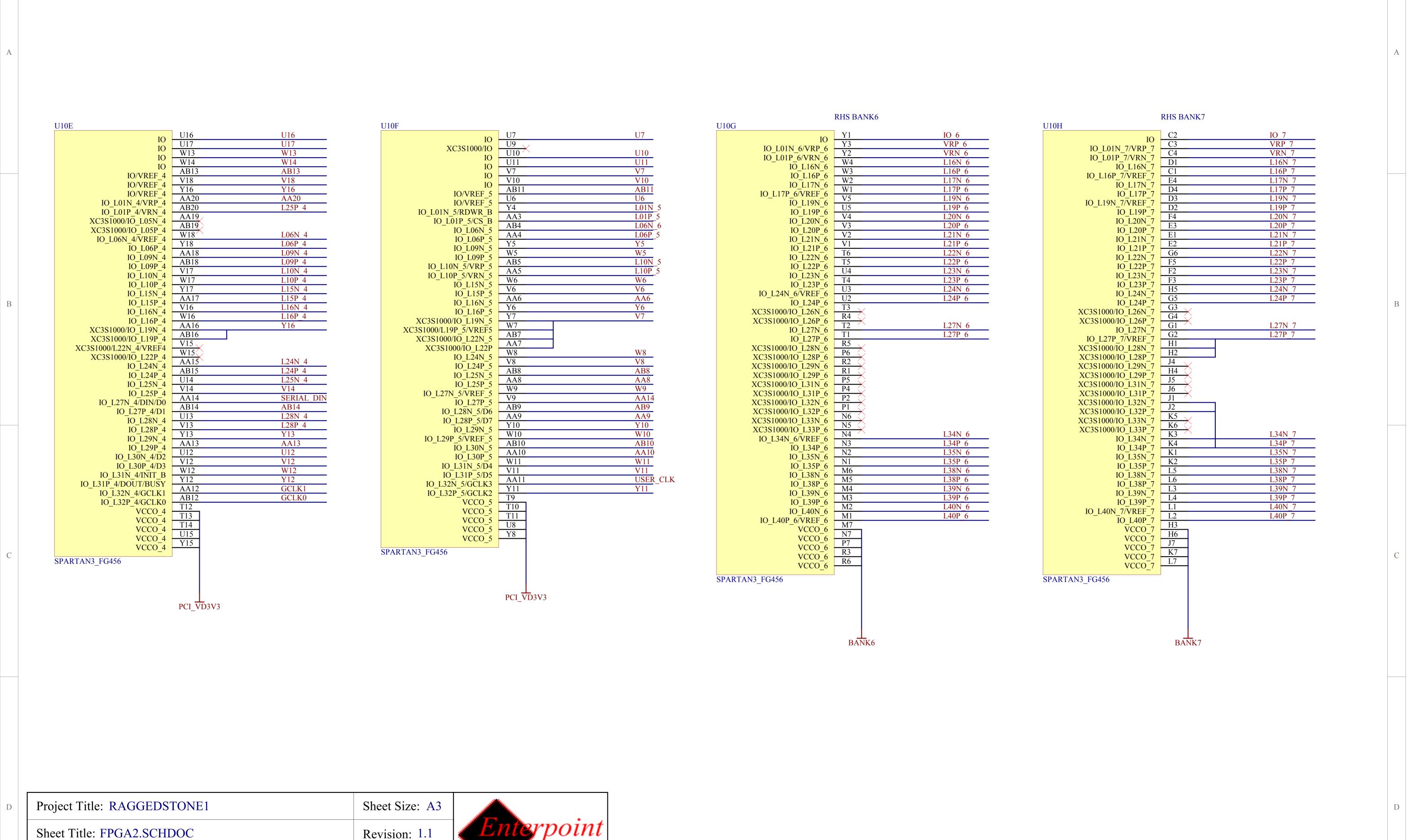
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