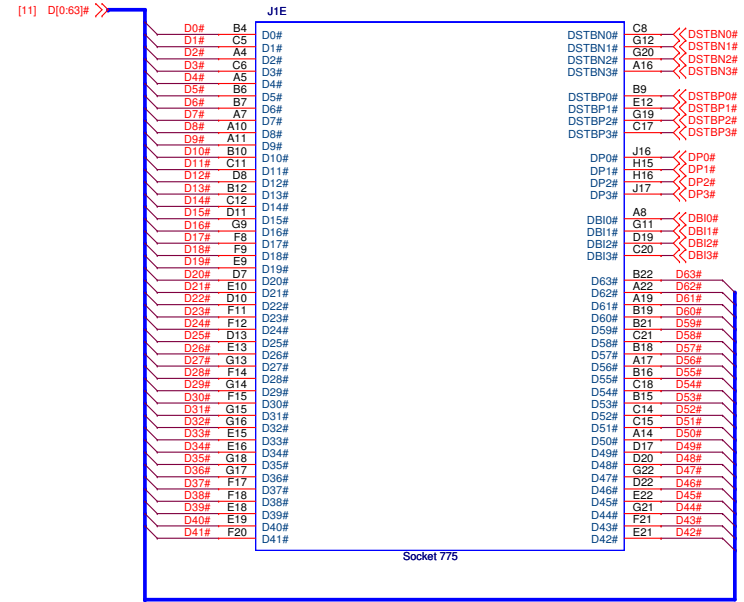
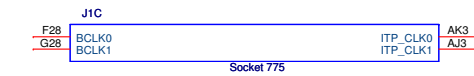
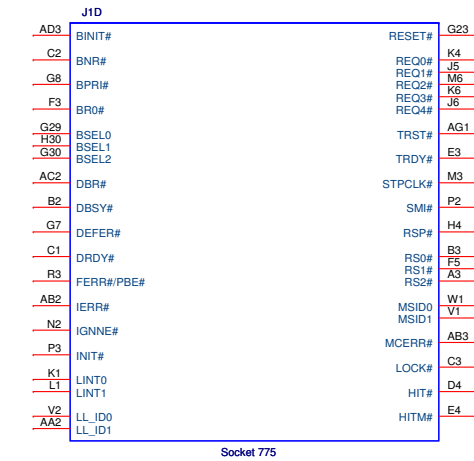
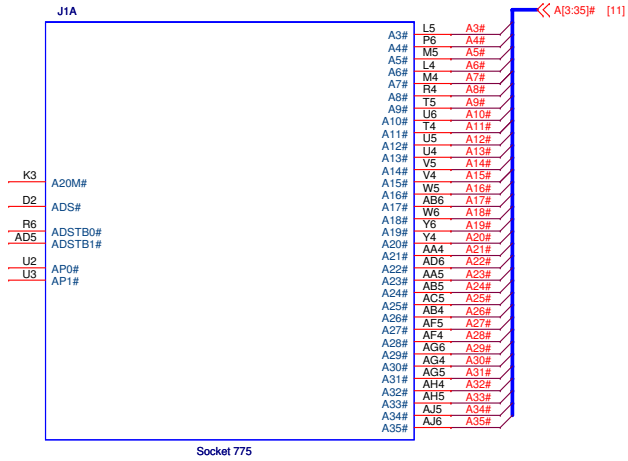
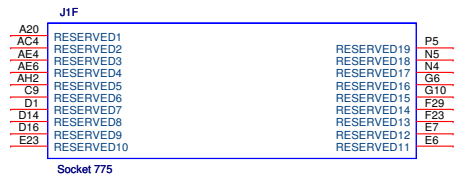
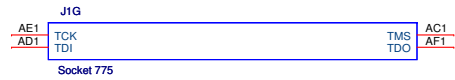
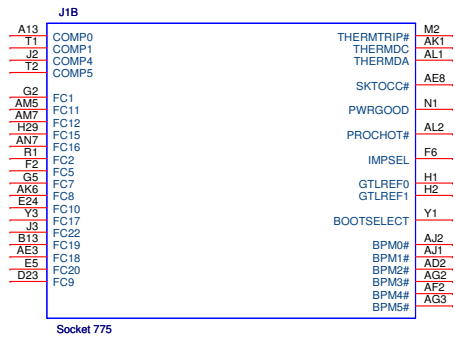


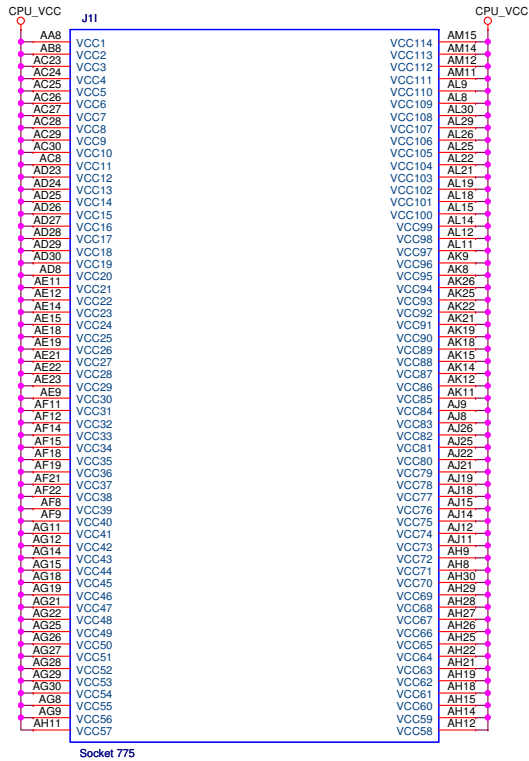
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12	X48 VSS
13	DDR3 Channel 0 Slot 0
14	DDR3 Channel 0 Slot 1
15	DDR3 Channel 1 Slot 0
16	DDR3 Channel 1 Slot 1

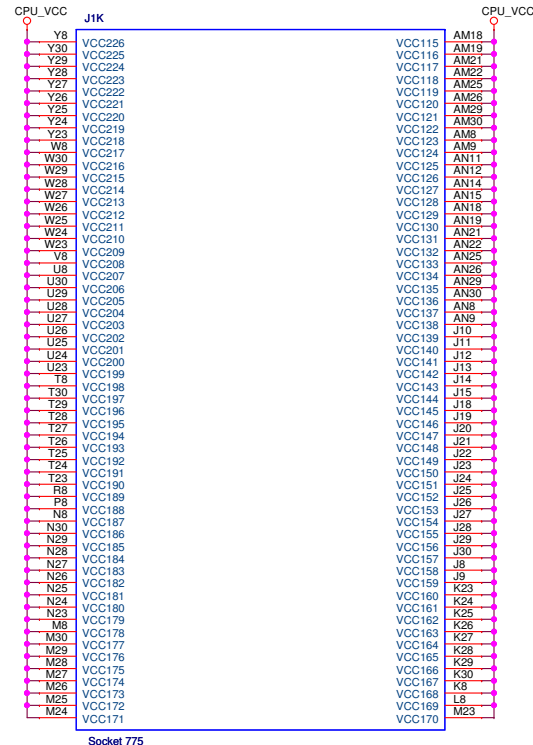


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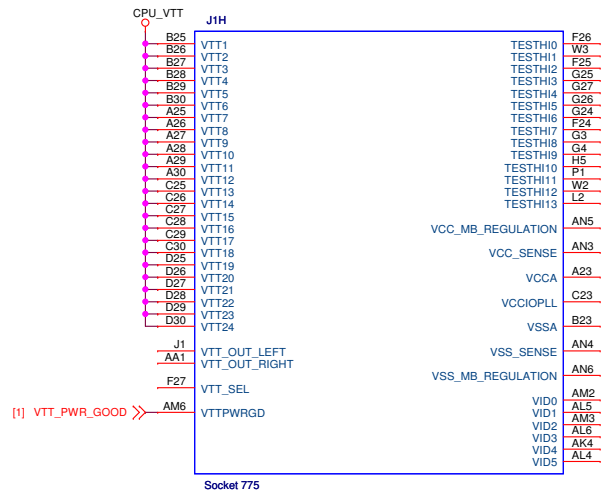


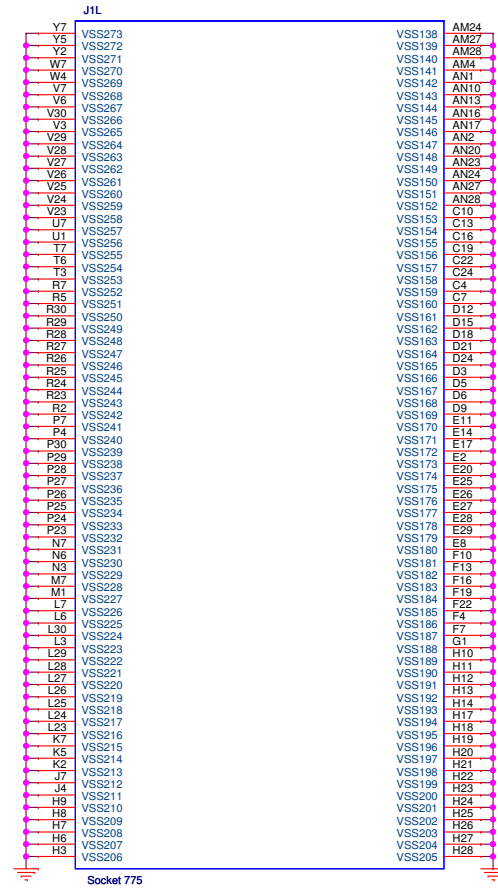
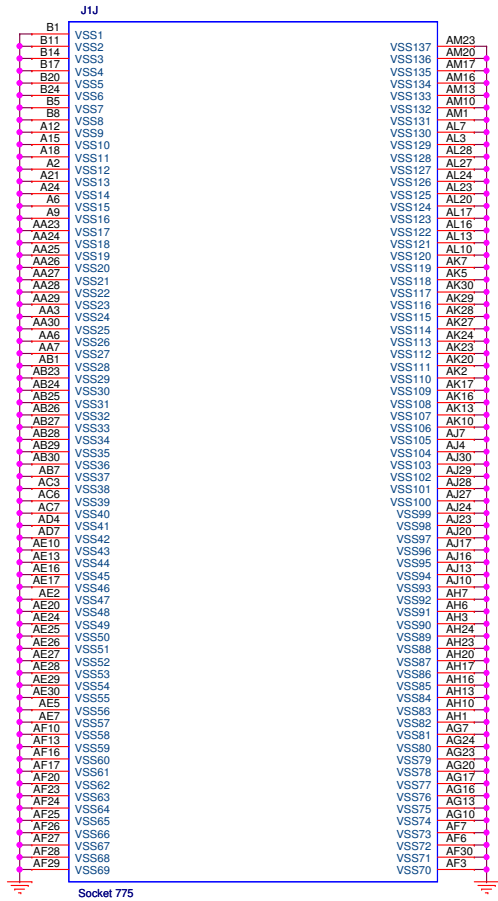


Socket 775



Socket 775





U1K			
F43	FSB_AB_3	FSB_ADSTBB_1	V34
M38	FSB_AB_4		
M36	FSB_AB_5	FSB_ADSTBB_0	M40
K38			
L40	FSB_AB_6		U44
N36	FSB_AB_7	FSB_ADSB	
N40	FSB_AB_8		
R36	FSB_AB_9		AA38
N39	FSB_AB_10	FSB_AB_35	AB35
N34	FSB_AB_11	FSB_AB_34	
N38	FSB_AB_12	FSB_AB_33	AA40
R39	FSB_AB_13	FSB_AB_32	AA35
K42	FSB_AB_14	FSB_AB_31	AA33
R35	FSB_AB_15	FSB_AB_30	AB34
T40	FSB_AB_16	FSB_AB_29	V43
T36	FSB_AB_17	FSB_AB_28	W33
T34	FSB_AB_18	FSB_AB_27	V35
T38	FSB_AB_19	FSB_AB_26	W34
P43	FSB_AB_20	FSB_AB_25	V39
W38	FSB_AB_21	FSB_AB_24	V38
	FSB_AB_22	FSB_AB_23	

X48 Express

U1M			
U42	FSB_BNRB	FSB_REOB_4	H39
H38		FSB_REOB_3	K36
	FSB_BPRIB	FSB_REOB_2	L39
W44	FSB_BREQ0B	FSB_REOB_1	G40
		FSB_REOB_0	C44
T42	FSB_DBSYB	FSB_TRDYB	W40
R42	FSB_HITB	FSB_DRDYB	U41
V42	FSB_HITMB	FSB_LOCKB	T45
T39	FSB_DEFERB	FSB_CPURSTB	D35

X48 Express

U1L			
D27	FSB_ACCVREF	FSB_SCOMP	D28
E27	FSB_DVREF	FSB_SCOMPB	C28
R44	FSB_RSB_0	FSB_RCOMP	C26
W41	FSB_RSB_1		
R41	FSB_RSB_2	FSB_SWING	A28

X48 Express

U1N			
P42	FSB_DB_0	FSB_DINVB_3	B37
N41	FSB_DB_1	FSB_DINVB_2	N28
N44	FSB_DB_2	FSB_DINVB_1	E40
N42	FSB_DB_3	FSB_DINVB_0	L41
M45	FSB_DB_4		C40
L44	FSB_DB_5	FSB_DSTBPB_3	N25
L42	FSB_DB_6	FSB_DSTBPB_2	H34
J43	FSB_DB_7	FSB_DSTBPB_1	J44
H42	FSB_DB_8	FSB_DSTBPB_0	
J41	FSB_DB_9		D41
G42	FSB_DB_10	FSB_DSTBNB_3	M25
H45	FSB_DB_11	FSB_DSTBNB_2	G34
G44	FSB_DB_12	FSB_DSTBNB_1	K43
F41	FSB_DB_13	FSB_DSTBNB_0	
E42	FSB_DB_14		
F38	FSB_DB_15		E35
F39	FSB_DB_16	FSB_DB_63	B35
B43	FSB_DB_17	FSB_DB_62	E37
L36	FSB_DB_18	FSB_DB_61	C37
G38	FSB_DB_19	FSB_DB_60	D36
K35	FSB_DB_20	FSB_DB_59	C35
G36	FSB_DB_21	FSB_DB_58	D39
G35	FSB_DB_22	FSB_DB_57	B39
K34	FSB_DB_23	FSB_DB_56	B42
H33	FSB_DB_24	FSB_DB_55	D38
F33	FSB_DB_25	FSB_DB_54	D43
L34	FSB_DB_26	FSB_DB_53	D44
N33	FSB_DB_27	FSB_DB_52	C42
L33	FSB_DB_28	FSB_DB_51	E41
N31	FSB_DB_29	FSB_DB_50	A38
M31	FSB_DB_30	FSB_DB_49	F35
F31	FSB_DB_31	FSB_DB_48	G24
K31	FSB_DB_32	FSB_DB_47	K24
H31	FSB_DB_33	FSB_DB_46	H24
M30	FSB_DB_34	FSB_DB_45	L24
L30	FSB_DB_35	FSB_DB_44	L25
N30	FSB_DB_36	FSB_DB_43	N24
G30	FSB_DB_37	FSB_DB_42	L28
H30	FSB_DB_38	FSB_DB_41	K28
	FSB_DB_39	FSB_DB_40	

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U1P		
B15	PEG_RXN_0	PEG_TXP_15
G14	PEG_RXN_1	PEG_TXP_14
K13	PEG_RXN_2	PEG_TXP_13
M12	PEG_RXN_3	PEG_TXP_12
L12	PEG_RXN_4	PEG_TXP_11
H10	PEG_RXN_5	PEG_TXP_10
D5	PEG_RXN_6	PEG_TXP_9
G6	PEG_RXN_7	PEG_TXP_8
C2	PEG_RXN_8	PEG_TXP_7
K8	PEG_RXN_9	PEG_TXP_6
L10	PEG_RXN_10	PEG_TXP_5
M8	PEG_RXN_11	PEG_TXP_4
J2	PEG_RXN_12	PEG_TXP_3
N10	PEG_RXN_13	PEG_TXP_2
	PEG_RXN_14	PEG_TXP_1
	PEG_RXN_15	PEG_TXP_0
A16	PEG_RXP_0	PEG_TXN_15
B13	PEG_RXP_1	PEG_TXN_14
H13	PEG_RXP_2	PEG_TXN_13
L13	PEG_RXP_3	PEG_TXN_12
N13	PEG_RXP_4	PEG_TXN_11
H12	PEG_RXP_5	PEG_TXN_10
K11	PEG_RXP_6	PEG_TXN_9
G10	PEG_RXP_7	PEG_TXN_8
E6	PEG_RXP_8	PEG_TXN_7
F7	PEG_RXP_9	PEG_TXN_6
D2	PEG_RXP_10	PEG_TXN_5
K7	PEG_RXP_11	PEG_TXN_4
M11	PEG_RXP_12	PEG_TXN_3
M7	PEG_RXP_13	PEG_TXN_2
K3	PEG_RXP_14	PEG_TXN_1
N8	PEG_RXP_15	PEG_TXN_0

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U1Q		
AA13	PEG2_RXN_0	PEG2_TXP_15
AA11	PEG2_RXN_1	PEG2_TXP_14
AA7	PEG2_RXN_2	PEG2_TXP_13
AB12	PEG2_RXN_3	PEG2_TXP_12
AC10	PEG2_RXN_4	PEG2_TXP_11
AC7	PEG2_RXN_5	PEG2_TXP_10
AD12	PEG2_RXN_6	PEG2_TXP_9
AE11	PEG2_RXN_7	PEG2_TXP_8
AE6	PEG2_RXN_8	PEG2_TXP_7
AH13	PEG2_RXN_9	PEG2_TXP_6
AH10	PEG2_RXN_10	PEG2_TXP_5
AH8	PEG2_RXN_11	PEG2_TXP_4
AK13	PEG2_RXN_12	PEG2_TXP_3
AL10	PEG2_RXN_13	PEG2_TXP_2
AL7	PEG2_RXN_14	PEG2_TXP_1
AP11	PEG2_RXN_15	PEG2_TXP_0
W12	PEG2_RXP_0	PEG2_TXN_15
AA10	PEG2_RXP_1	PEG2_TXN_14
AA6	PEG2_RXP_2	PEG2_TXN_13
AG13	PEG2_RXP_3	PEG2_TXN_12
AC11	PEG2_RXP_4	PEG2_TXN_11
AC6	PEG2_RXP_5	PEG2_TXN_10
AE13	PEG2_RXP_6	PEG2_TXN_9
AE10	PEG2_RXP_7	PEG2_TXN_8
AE7	PEG2_RXP_8	PEG2_TXN_7
AG12	PEG2_RXP_9	PEG2_TXN_6
AH11	PEG2_RXP_10	PEG2_TXN_5
AH7	PEG2_RXP_11	PEG2_TXN_4
AK12	PEG2_RXP_12	PEG2_TXN_3
AL11	PEG2_RXP_13	PEG2_TXN_2
AL6	PEG2_RXP_14	PEG2_TXN_1
AP10	PEG2_RXP_15	PEG2_TXN_0

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U1R		
D18	EXP_CLKINN	EXP_COMPI
D19	EXP_CLKINP	EXP_COMPO
AD14	EXP2_CLKINN	EXP2_COMPI
AE14	EXP2_CLKINP	EXP2_COMPO
K19	EXP_SLR	

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		DDR_A_DQS_0	BA6
		DDR_A_DQS_1	BA11
		DDR_A_DQS_2	BA15
		DDR_A_DQS_3	AT21
		DDR_A_DQS_4	AT43
		DDR_A_DQS_5	AM43
		DDR_A_DQS_6	AD43
		DDR_A_DQS_7	AA42
		DDR_A_DQS_8	AL38
			BC6
		DDR_A_DQSB_0	AY11
		DDR_A_DQSB_1	AY15
		DDR_A_DQSB_2	AV21
		DDR_A_DQSB_3	AT42
		DDR_A_DQSB_4	AM42
		DDR_A_DQSB_5	AD42
		DDR_A_DQSB_6	AA44
		DDR_A_DQSB_7	AL36
		DDR_A_DQSB_8	
		DDR_A_DM_0	BB5
		DDR_A_DM_1	BC10
		DDR_A_DM_2	BC14
		DDR_A_DM_3	AP21
		DDR_A_DM_4	AU44
		DDR_A_DM_5	AN44
		DDR_A_DM_6	AE44
		DDR_A_DM_7	AB40

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		DDR_A_CASB	AK39
		DDR_A_RASB	AK34
		DDR_A_WEB	AL34
		DDR_A_CB_7	AK33
		DDR_A_CB_6	AK35
		DDR_A_CB_5	AK38
		DDR_A_CB_4	AN35
		DDR_A_CB_3	AL33
		DDR_A_CB_2	
		DDR_A_CB_1	
		DDR_A_CB_0	
		DDR_A_CSB_3	AY43
		DDR_A_CSB_2	BB39
		DDR_A_CSB_1	BD42
		DDR_A_CSB_0	BA40
			AW44
		DDR_A_ODT_3	BA41
		DDR_A_ODT_2	AY41
		DDR_A_ODT_1	BB43
		DDR_A_ODT_0	

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		DDR_A_DQ_0	Y45
		DDR_A_DQ_1	AA41
		DDR_A_DQ_2	AB39
		DDR_A_DQ_3	AC40
		DDR_A_DQ_4	W42
		DDR_A_DQ_5	Y42
		DDR_A_DQ_6	AB43
		DDR_A_DQ_7	AB42
		DDR_A_DQ_8	AC39
		DDR_A_DQ_9	AD40
		DDR_A_DQ_10	AF45
		DDR_A_DQ_11	AF42
		DDR_A_DQ_12	AC45
		DDR_A_DQ_13	AC42
		DDR_A_DQ_14	AE42
		DDR_A_DQ_15	AE41
		DDR_A_DQ_16	AL41
		DDR_A_DQ_17	AL40
		DDR_A_DQ_18	AP45
		DDR_A_DQ_19	AP42
		DDR_A_DQ_20	AL42
		DDR_A_DQ_21	AL44
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		DDR_A_DQ_23	AN41
		DDR_A_DQ_24	AR40
		DDR_A_DQ_25	AR41
		DDR_A_DQ_26	AU41
		DDR_A_DQ_27	AW42
		DDR_A_DQ_28	AR42
		DDR_A_DQ_29	AR44
		DDR_A_DQ_30	AU43
		DDR_A_DQ_31	AV42

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		DDR3_DRAMRSTB	BB23
		DDR3_A_CS01	BB44
		DDR3_A_WEB	BC40
		DDR3_B_ODT3	BA37
		DDR3_DRAM_PWR_OK	AN11
		DDR3_A_MAO	BD35
		DDR_VREF	AV7

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U1E		
	DDR_B_DQSB_8	AH42
AY22	DDR_B_MA_0	AD33
BC22	DDR_B_MA_1	AG38
BB22	DDR_B_MA_2	AP40
BA21	DDR_B_MA_3	AV38
BD21	DDR_B_MA_4	AP24
BB21	DDR_B_MA_5	AP16
BB20	DDR_B_MA_6	AR12
AY19	DDR_B_MA_7	AT10
BE20	DDR_B_MA_8	AH43
BB19	DDR_B_MA_9	AC33
AW24	DDR_B_MA_10	AG39
BA19	DDR_B_MA_11	AP39
AY18	DDR_B_MA_12	AW39
BA33	DDR_B_MA_13	AR25
BC18	DDR_B_MA_14	AN18
	DDR_B_DQS_1	AR13
	DDR_B_DQS_0	AW10
	DDR_B_DM_7	AD35
BB24	DDR_B_BS_0	AG35
AW23	DDR_B_BS_1	AN36
BB18	DDR_B_BS_2	AY40
	DDR_B_DM_4	AY25
	DDR_B_DM_3	AW16
	DDR_B_DM_2	AT13
	DDR_B_DM_1	AY8
	DDR_B_DM_0	

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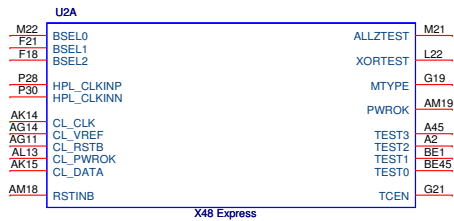
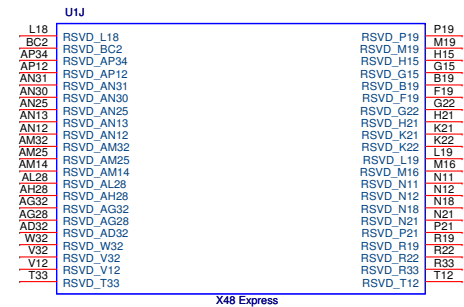
U1G		
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AY7	DDR_B_DQ_1	AC36
AT11	DDR_B_DQ_2	AE34
AT12	DDR_B_DQ_3	AE35
AV8	DDR_B_DQ_4	AB38
AW6	DDR_B_DQ_5	AB32
AR11	DDR_B_DQ_6	AD36
AW11	DDR_B_DQ_7	AD39
AY12	DDR_B_DQ_8	AE33
AY13	DDR_B_DQ_9	AE40
AT15	DDR_B_DQ_10	AH36
AV15	DDR_B_DQ_11	AH33
AW12	DDR_B_DQ_12	AE38
AW13	DDR_B_DQ_13	AE39
AN15	DDR_B_DQ_14	AG33
AP15	DDR_B_DQ_15	AH34
AY16	DDR_B_DQ_16	AN40
AV16	DDR_B_DQ_17	AN39
AP19	DDR_B_DQ_18	AR34
AT19	DDR_B_DQ_19	AV36
AN16	DDR_B_DQ_20	AN33
AR16	DDR_B_DQ_21	AP35
AT18	DDR_B_DQ_22	AP36
AR18	DDR_B_DQ_23	AR36
AW25	DDR_B_DQ_24	AV40
AV25	DDR_B_DQ_25	AV39
AT27	DDR_B_DQ_26	AW36
AN24	DDR_B_DQ_27	AT40
AP24	DDR_B_DQ_28	AT38
AT25	DDR_B_DQ_29	AW38
AP27	DDR_B_DQ_30	AY39
	DDR_B_DQ_31	
	DDR_B_DQ_63	
	DDR_B_DQ_62	
	DDR_B_DQ_61	
	DDR_B_DQ_60	
	DDR_B_DQ_59	
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	DDR_B_DQ_37	
	DDR_B_DQ_36	
	DDR_B_DQ_35	
	DDR_B_DQ_34	
	DDR_B_DQ_33	
	DDR_B_DQ_32	

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U1F		
BB32	DDR_B_CASB	
BD31	DDR_B_RASB	
AY31	DDR_B_WEB	
	DDR_B_CK_5	AW34
BD17	DDR_B_CKE_0	AY30
BD19	DDR_B_CKE_1	AR31
BB17	DDR_B_CKE_2	AV33
BA17	DDR_B_CKE_3	AR28
	DDR_B_CK_1	AW30
	DDR_B_CK_0	
AK45	DDR_B_CB_0	AY34
AJ44	DDR_B_CB_1	AY28
AG42	DDR_B_CB_2	AP31
AG40	DDR_B_CB_3	AW33
AJ42	DDR_B_CB_4	AP28
AK42	DDR_B_CB_5	AV30
AG41	DDR_B_CB_6	
AG44	DDR_B_CB_7	
	DDR_B_CSB_3	BA35
BD33	DDR_B_CSB_2	BC32
BB34	DDR_B_CSB_1	BB35
BB33	DDR_B_ODT_0	BA31
AY35	DDR_B_ODT_1	
	DDR_B_ODT_2	
	DDR_B_ODT_3	

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U1U			
V31	VCC_CL_V31	VCC_CL_V29	V29
AM30	VCC_CL_AM30	VCC_CL_W29	W29
AM23	VCC_CL_AM23	VCC_CL_Y29	Y29
AM22	VCC_CL_AM22	VCC_CL_AA29	AA29
AL30	VCC_CL_AL30	VCC_CL_AA31	AA31
AL27	VCC_CL_AL27	VCC_CL_AB29	AB29
AL25	VCC_CL_AL25	VCC_CL_AB31	AB31
AL24	VCC_CL_AL24	VCC_CL_AC29	AC29
AL23	VCC_CL_AL23	VCC_CL_AC31	AC31
AL22	VCC_CL_AL22	VCC_CL_AD29	AD29
AL21	VCC_CL_AL21	VCC_CL_AD31	AD31
AL19	VCC_CL_AL19	VCC_CL_AE29	AE29
AL18	VCC_CL_AL18	VCC_CL_AE31	AE31
AL16	VCC_CL_AL16	VCC_CL_AF29	AF29
AK31	VCC_CL_AK31	VCC_CL_AG29	AG29
AJ29	VCC_CL_AJ29	VCC_CL_AG31	AG31
AJ28	VCC_CL_AJ28	VCC_CL_AH14	AH14
AJ27	VCC_CL_AJ27	VCC_CL_AH15	AH15
AJ26	VCC_CL_AJ26	VCC_CL_AH29	AH29
AJ25	VCC_CL_AJ25	VCC_CL_AH31	AH31
AJ24	VCC_CL_AJ24	VCC_CL_AJ17	AJ17
AJ23	VCC_CL_AJ23	VCC_CL_AJ18	AJ18
AJ22	VCC_CL_AJ22	VCC_CL_AJ19	AJ19
AJ21	VCC_CL_AJ21	VCC_CL_AJ20	AJ20

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U1T			
AB13	VCC_EXT_PLL	VCC_DDR_AY23	AY23
A23	VCC3_3_A23	VCC_DDR_AY45	AY45
G16	VCC3_3_G16	VCC_DDR_BC20	BC20
L16	VCC3_3_L16	VCC_DDR_BC23	BC23
D20	VCCA_EXP	VCC_DDR_BC26	BC26
AU5	VCCA_EXP2	VCC_DDR_BC30	BC30
D26	VCCA_HPL_D26	VCC_DDR_BC34	BC34
D25	VCCA_HPL_D25	VCC_DDR_BE22	BE22
B27	VCCA_MPL	VCC_DDR_BE24	BE24
U29	VCCAUX_U29	VCC_DDR_BE28	BE28
T31	VCCAUX_T31	VCC_DDR_BE32	BE32
R30	VCCAUX_R30	VCC_DDR_BE36	BE36
R28	VCCAUX_R28	VCC_DDR_BE40	BE40
R27	VCCAUX_R27	VCC_CKDDR_BC44	BC44
R25	VCCAUX_R25	VCC_CKDDR_BD43	BD43
R24	VCCAUX_R24	VCC_CKDDR_BD44	BD44
R23	VCCAUX_R23	VCC_CKDDR_BE43	BE43
		VCC_CKDDR_BE44	BE44

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U1V			
AD11	VCC_EXP_AD11	VCCR_EXP_C8	C8
AD10	VCC_EXP_AD10	VCCR_EXP_C12	C12
AD8	VCC_EXP_AD8	VCCR_EXP_C16	C16
AD7	VCC_EXP_AD7	VCCR_EXP_H3	H3
AB11	VCC_EXP_AB11	VCCR_EXP_M3	M3
AB10	VCC_EXP_AB10	VCCR_EXP_T14	T14
AB8	VCC_EXP_AB8	VCCR_EXP_T15	T15
AB7	VCC_EXP_AB7	VCCR_EXP_V14	V14
AB6	VCC_EXP_AB6	VCCR_EXP_V15	V15
AB4	VCC_EXP_AB4	VCCR_EXP_W15	W15
AA5	VCC_EXP_AA5	VCCR_EXP_AA14	AA14
AA4	VCC_EXP_AA4	VCCR_EXP_AA15	AA15
AA2	VCC_EXP_AA2	VCCR_EXP_AB14	AB14
Y4	VCC_EXP_Y4	VCCR_EXP_AC3	AC3
Y3	VCC_EXP_Y3	VCCR_EXP_AC3	AC3
Y1	VCC_EXP_Y1	VCCR_EXP_AC15	AC15
W11	VCC_EXP_W11	VCCR_EXP_AD15	AD15
W10	VCC_EXP_W10	VCCR_EXP_AE15	AE15
W8	VCC_EXP_W8	VCCR_EXP_AF3	AF3
W7	VCC_EXP_W7	VCCR_EXP_AK3	AK3
W5	VCC_EXP_W5	VCCR_EXP_AP3	AP3
W4	VCC_EXP_W4	VCCAPLL_EXP2	AR10
W2	VCC_EXP_W2	VCCAPLL_EXP	A20
W4	VCC_EXP_W4		
W2	VCC_EXP_W2		
V4	VCC_EXP_V4		
V3	VCC_EXP_V3		
V1	VCC_EXP_V1		
U5	VCC_EXP_U5		
U4	VCC_EXP_U4		
U2	VCC_EXP_U2		
T4	VCC_EXP_T4		
T3	VCC_EXP_T3		

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U1Y			
M27	VTT_FSB_M27	VTT_FSB_A30	A30
L27	VTT_FSB_L27	VTT_FSB_A32	A32
K27	VTT_FSB_K27	VTT_FSB_B31	B31
K25	VTT_FSB_K25	VTT_FSB_B33	B33
H28	VTT_FSB_H28	VTT_FSB_C32	C32
H27	VTT_FSB_H27	VTT_FSB_D30	D30
H25	VTT_FSB_H25	VTT_FSB_D31	D31
G28	VTT_FSB_G28	VTT_FSB_D32	D32
G27	VTT_FSB_G27	VTT_FSB_D33	D33
G25	VTT_FSB_G25	VTT_FSB_E29	E29
F30	VTT_FSB_F30	VTT_FSB_E31	E31
F28	VTT_FSB_F28	VTT_FSB_E33	E33
F27	VTT_FSB_F27	VTT_FSB_F25	F25

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U1S			
N15	VCC_N15	VCC_E25	E25
AH26	VCC_AH26	VCC_C18	C18
AH24	VCC_AH24	VCC_B25	B25
AH22	VCC_AH22	VCC_R16	R16
AH20	VCC_AH20	VCC_R18	R18
AH19	VCC_AH19	VCC_U17	U17
AH18	VCC_AH18	VCC_U18	U18
AH17	VCC_AH17	VCC_U19	U19
AG27	VCC_AG27	VCC_U20	U20
AG25	VCC_AG25	VCC_U21	U21
AG23	VCC_AG23	VCC_U22	U22
AG21	VCC_AG21	VCC_U23	U23
AG19	VCC_AG19	VCC_U24	U24
AG18	VCC_AG18	VCC_U25	U25
AG17	VCC_AG17	VCC_U26	U26
AG15	VCC_AG15	VCC_U27	U27
AF28	VCC_AF28	VCC_U28	U28
AF26	VCC_AF26	VCC_U29	U29
AF24	VCC_AF24	VCC_U30	U30
AF22	VCC_AF22	VCC_U31	U31
AF18	VCC_AF18	VCC_U32	U32
AF17	VCC_AF17	VCC_U33	U33
AE28	VCC_AE28	VCC_U34	U34
AE27	VCC_AE27	VCC_U35	U35
AE25	VCC_AE25	VCC_U36	U36
AE23	VCC_AE23	VCC_U37	U37
AE21	VCC_AE21	VCC_U38	U38
AE19	VCC_AE19	VCC_U39	U39
AE18	VCC_AE18	VCC_U40	U40
AE17	VCC_AE17	VCC_U41	U41
AD28	VCC_AD28	VCC_U42	U42
AD26	VCC_AD26	VCC_U43	U43
AD24	VCC_AD24	VCC_U44	U44
AD22	VCC_AD22	VCC_U45	U45
AD20	VCC_AD20	VCC_U46	U46
AD18	VCC_AD18	VCC_U47	U47
AD17	VCC_AD17	VCC_U48	U48
AC28	VCC_AC28	VCC_U49	U49
AC27	VCC_AC27	VCC_U50	U50
AC25	VCC_AC25	VCC_U51	U51
AC23	VCC_AC23	VCC_U52	U52
AC21	VCC_AC21	VCC_U53	U53
AC19	VCC_AC19	VCC_U54	U54
AC18	VCC_AC18	VCC_U55	U55
AC17	VCC_AC17	VCC_U56	U56
AB28	VCC_AB28	VCC_U57	U57
AB26	VCC_AB26	VCC_U58	U58
AB24	VCC_AB24	VCC_U59	U59
AB22	VCC_AB22	VCC_U60	U60
AB20	VCC_AB20	VCC_U61	U61

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U1W		
B2	VSS_B2	VSS_AA20
BE38	VSS_BE38	VSS_AA22
BE34	VSS_BE34	VSS_AA24
BE30	VSS_BE30	VSS_AA26
BE26	VSS_BE26	VSS_AA28
BE22	VSS_BE22	VSS_AA30
BE18	VSS_BE23	VSS_AA34
BE14	VSS_BE18	VSS_AA36
BE10	VSS_BE14	VSS_AA39
BE6	VSS_BE10	VSS_AB19
BE3	VSS_BE6	VSS_AB21
BD2	VSS_BE3	VSS_AB23
BC43	VSS_BE2	VSS_AB25
BC16	VSS_BC43	VSS_AB27
BC12	VSS_BC16	VSS_AB29
BC8	VSS_BC12	VSS_AB36
BC3	VSS_BC8	VSS_AB38
BC1	VSS_BC3	VSS_AB45
BB2	VSS_BC1	VSS_AC1
AY36	VSS_BB2	VSS_AC8
AY33	VSS_AY36	VSS_AC12
AW40	VSS_AY33	VSS_AC14
AW35	VSS_AY10	VSS_AC20
AW31	VSS_AW40	VSS_AC22
AW28	VSS_AW35	VSS_AC24
AW27	VSS_AW31	VSS_AC26
AW21	VSS_AW28	VSS_AC28
AW18	VSS_AW21	VSS_AC32
AW15	VSS_AW18	VSS_AC35
AW7	VSS_AW15	VSS_AC38
AW4	VSS_AW7	VSS_AC43
AV45	VSS_AW4	VSS_AD1
AV43	VSS_AV45	VSS_AD6
AV34	VSS_AV43	VSS_AD13
AV28	VSS_AV34	VSS_AD19
AV24	VSS_AV28	VSS_AD21
AV23	VSS_AV24	VSS_AD23
AV22	VSS_AV23	VSS_AD25
AV18	VSS_AV22	VSS_AD27
AV13	VSS_AV18	VSS_AD34
AV12	VSS_AV13	VSS_AD38
AV11	VSS_AV12	VSS_AD45
AV10	VSS_AV11	VSS_AE4
AV6	VSS_AV10	VSS_AE8
AV4	VSS_AV6	VSS_AE12
AV3	VSS_AV4	VSS_AE20
AV1	VSS_AV3	VSS_AE22
AL3	VSS_AV1	VSS_AE24
AT45	VSS_AL3	VSS_AE26
AT39	VSS_AT45	VSS_AE32
AT30	VSS_AT39	VSS_AE36
AT28	VSS_AT30	VSS_AF19
AT24	VSS_AT28	VSS_AF21
AT23	VSS_AT24	VSS_AF23
AT16	VSS_AT23	VSS_AF25
AT8	VSS_AT16	VSS_AF27
AT1	VSS_AT8	VSS_AF43
AR39	VSS_AT1	VSS_AG4
AR38	VSS_AR39	VSS_AG6
AR35	VSS_AR38	VSS_AG7
AR30	VSS_AR35	VSS_AG8
AR27	VSS_AR30	VSS_AG10
AR23	VSS_AR27	VSS_AG13
AR22	VSS_AR23	VSS_AG20
AR21	VSS_AR22	VSS_AG22
AR19	VSS_AR21	VSS_AG24
AR15	VSS_AR19	VSS_AG26
AR8	VSS_AR15	VSS_AG34
AR7	VSS_AR8	VSS_AG36
AR6	VSS_AR7	VSS_AH1
AR4	VSS_AR6	VSS_AH12
AP43	VSS_AR4	VSS_AH21
AP38	VSS_AP43	VSS_AH23
AP33	VSS_AP38	VSS_AH25
AP30	VSS_AP33	VSS_AH27
AP25	VSS_AP30	VSS_AH32
AP23	VSS_AP25	VSS_AH35
AP22	VSS_AP23	VSS_AH38
AP18	VSS_AP22	VSS_AH39
AP13	VSS_AP18	VSS_AH40
AP8	VSS_AP13	VSS_AH45
AN38	VSS_AP8	VSS_AJ4
AN34	VSS_AN38	VSS_AJ41
AN23	VSS_AN34	VSS_AK6
AN7	VSS_AN23	VSS_AK7
AN6	VSS_AN7	VSS_AK8
AN4	VSS_AN6	VSS_AK10
AM45	VSS_AN4	VSS_AK11
AM24	VSS_AM45	VSS_AK32
AM21	VSS_AM24	VSS_AK36
AM16	VSS_AM21	VSS_AK40
AM1	VSS_AM16	VSS_AK43
AL39	VSS_AM1	VSS_AL4
	VSS_AL39	VSS_AL6
		VSS_AL12
		VSS_AL35

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U1X		
AA12	VSS_AA12	VSS_W31
AA8	VSS_AA8	VSS_M15
Y43	VSS_Y43	VSS_K16
Y27	VSS_Y27	VSS_H16
Y25	VSS_Y25	VSS_F22
Y23	VSS_Y23	VSS_D24
Y21	VSS_Y21	VSS_D22
Y19	VSS_Y19	VSS_D21
W39	VSS_W39	VSS_D21
W35	VSS_W35	VSS_C24
W26	VSS_W26	VSS_C23
W24	VSS_W24	VSS_B33
W22	VSS_W22	VSS_B45
W23	VSS_W23	VSS_B44
W14	VSS_W14	VSS_B21
W13	VSS_W13	VSS_B17
W6	VSS_W6	VSS_AY3
W45	VSS_W45	VSS_AY1
V40	VSS_V40	VSS_AW2
V36	VSS_V36	VSS_A24
V33	VSS_V33	VSS_A3
V27	VSS_V27	VSS_A6
V25	VSS_V25	VSS_A10
V23	VSS_V23	VSS_A14
V21	VSS_V21	VSS_A18
V19	VSS_V19	VSS_A22
V13	VSS_V13	VSS_A26
V8	VSS_V8	VSS_A34
T43	VSS_T43	VSS_A36
T35	VSS_T35	VSS_A40
T32	VSS_T32	VSS_A43
T13	VSS_T13	VSS_B29
T11	VSS_T11	VSS_B44
T6	VSS_T6	VSS_C1
R40	VSS_R40	VSS_C3
R38	VSS_R38	VSS_C9
R34	VSS_R34	VSS_C20
R21	VSS_R21	VSS_C22
R13	VSS_R13	VSS_C30
R12	VSS_R12	VSS_C34
R11	VSS_R11	VSS_C38
R8	VSS_R8	VSS_C43
P45	VSS_P45	VSS_C45
P32	VSS_P32	VSS_D4
P27	VSS_P27	VSS_D7
P25	VSS_P25	VSS_D11
P24	VSS_P24	VSS_D13
P23	VSS_P23	VSS_D15
P18	VSS_P18	VSS_D17
P14	VSS_P14	VSS_D23
N35	VSS_N35	VSS_D29
N27	VSS_N27	VSS_D34
N23	VSS_N23	VSS_D42
N22	VSS_N22	VSS_E5
N19	VSS_N19	VSS_E19
N16	VSS_N16	VSS_E21
N7	VSS_N7	VSS_F1
N6	VSS_N6	VSS_F6
M43	VSS_M43	VSS_F8
M39	VSS_M39	VSS_F10
M35	VSS_M35	VSS_F11
M34	VSS_M34	VSS_F12
M33	VSS_M33	VSS_F13
M28	VSS_M28	VSS_F15
M24	VSS_M24	VSS_F16
M23	VSS_M23	VSS_F23
M18	VSS_M18	VSS_F24
M12	VSS_M12	VSS_F34
M10	VSS_M10	VSS_F36
L38	VSS_L38	VSS_F40
L35	VSS_L35	VSS_F45
L31	VSS_L31	VSS_G4
L23	VSS_L23	VSS_G7
L21	VSS_L21	VSS_G8
L15	VSS_L15	VSS_G11
L11	VSS_L11	VSS_G18
L8	VSS_L8	VSS_G23
L7	VSS_L7	VSS_G31
L6	VSS_L6	VSS_G33
L4	VSS_L4	VSS_G39
K45	VSS_K45	VSS_H6
K43	VSS_K43	VSS_H7
K39	VSS_K39	VSS_H8
K33	VSS_K33	VSS_H11
K30	VSS_K30	VSS_H18
K23	VSS_K23	VSS_H19
K18	VSS_K18	VSS_H22
K15	VSS_K15	VSS_H23
K12	VSS_K12	VSS_H35
K10	VSS_K10	VSS_H36
		VSS_H40
		VSS_H43
		VSS_J3
		VSS_K1
		VSS_K6

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