







U10 B1 GND B1
C1 NII GND C1
NII GND C1
NII GND C1
ANII GND C1
ANII GND AC1
ANII GND AC1
ANII GND AC2
AC2 GND AC2
AC3 GND AC2
AC3 GND AC2
AC3 GND AC2
AC3 GND AC3
AC3 GND AC3
AC3 GND AC3
AC3 GND AC3
AC4 GND AC4
AC5 GND AC5
AC6 GND AC6
AC6 GND AC6
AC6 GND AC6
AC7 GND AC7
AC7 GND AC7
AC7 GND AC7
AC7 GND AC7
AC7 GND AC6
AC6 GND AC6
AC6 GND AC6
AC6 GND AC6
AC7 GND AC6
AC7 GND AC7
AC7 G GND\_D18 D18
GND\_D18 D18
GND\_P18 AD18
GND\_P18 AD18
GND\_A19 GT9
GND\_A19 AT19
GND\_A19 XC4VLX80 FF1148 PCI FPGA Card FPGA GND Document Number Rev 1.0 Tuesday, September 28, 2010



