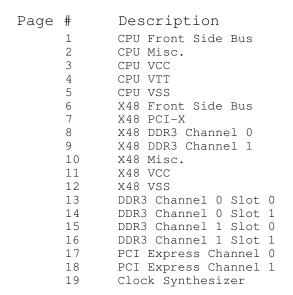
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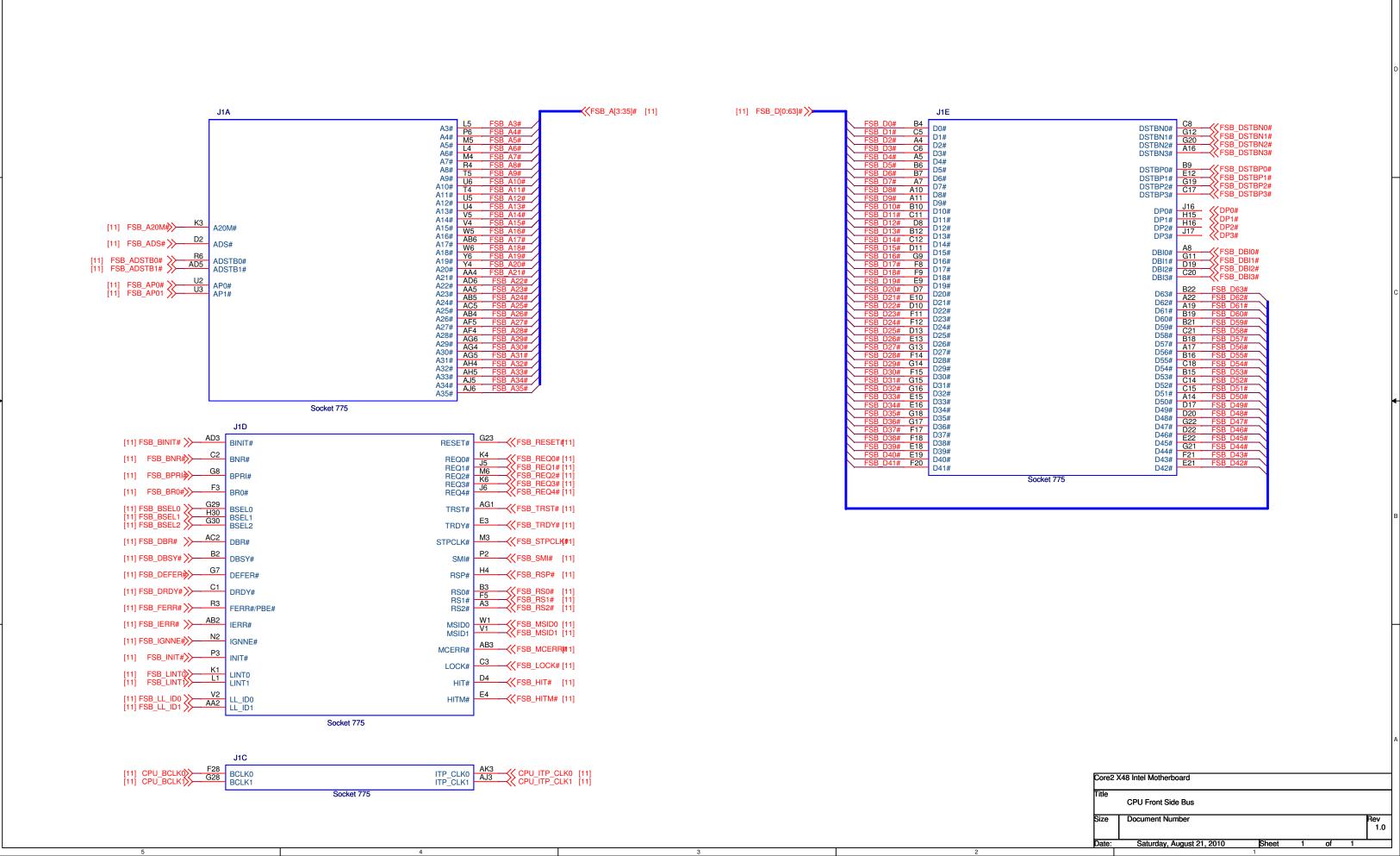
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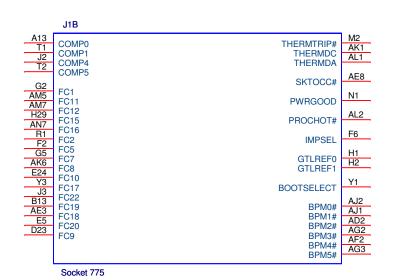
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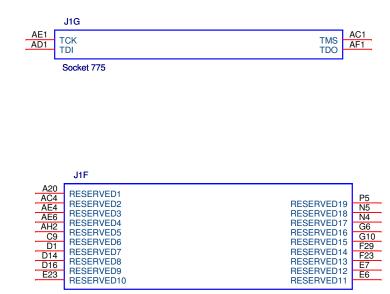
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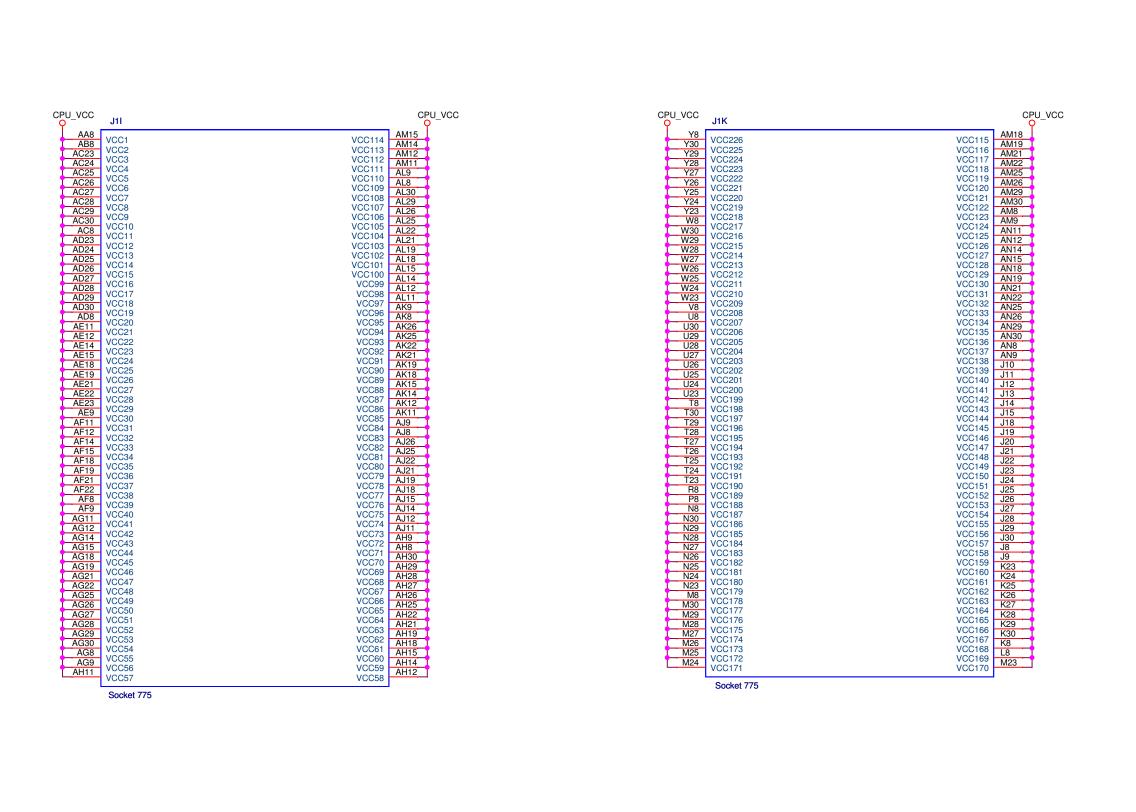




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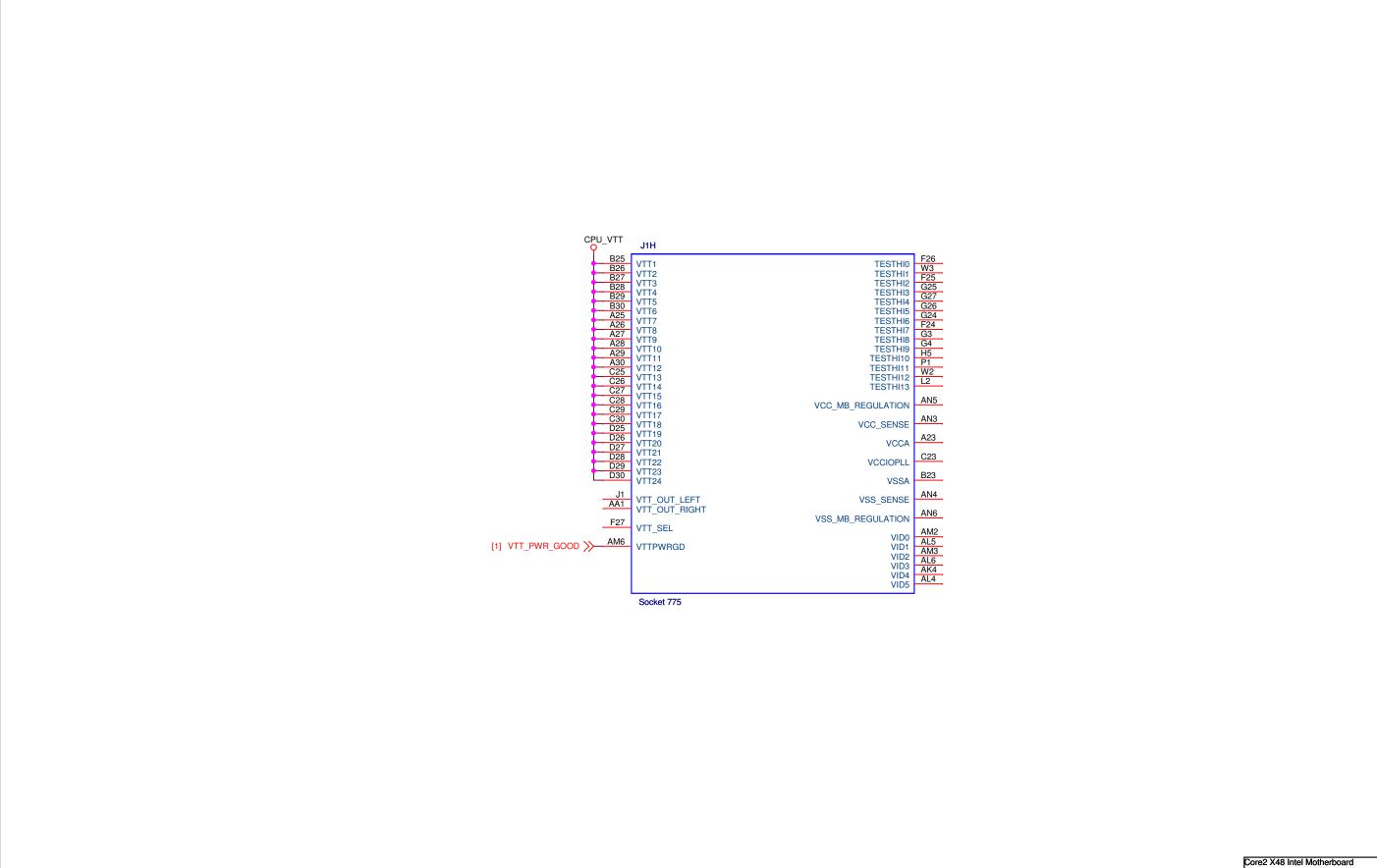
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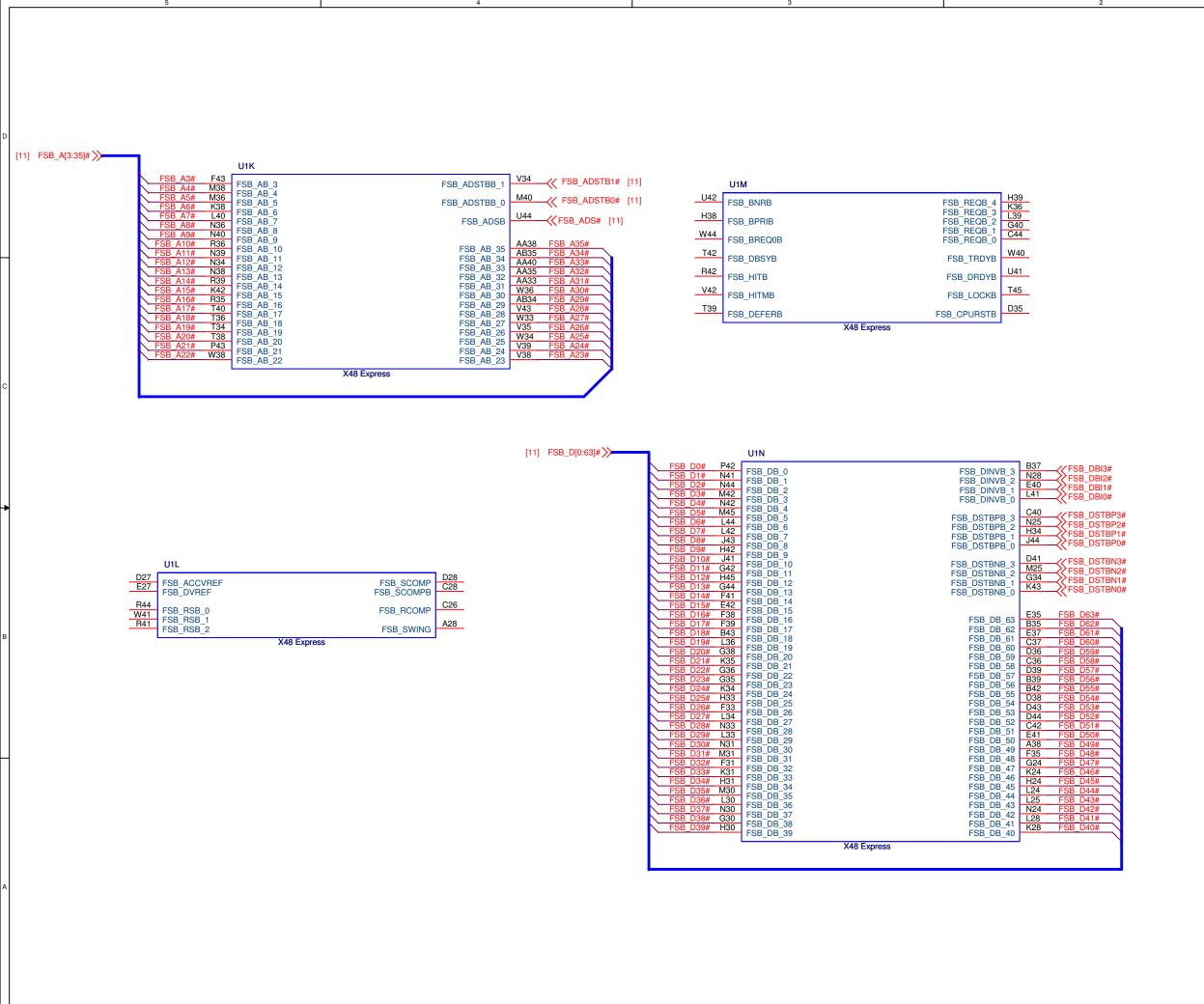
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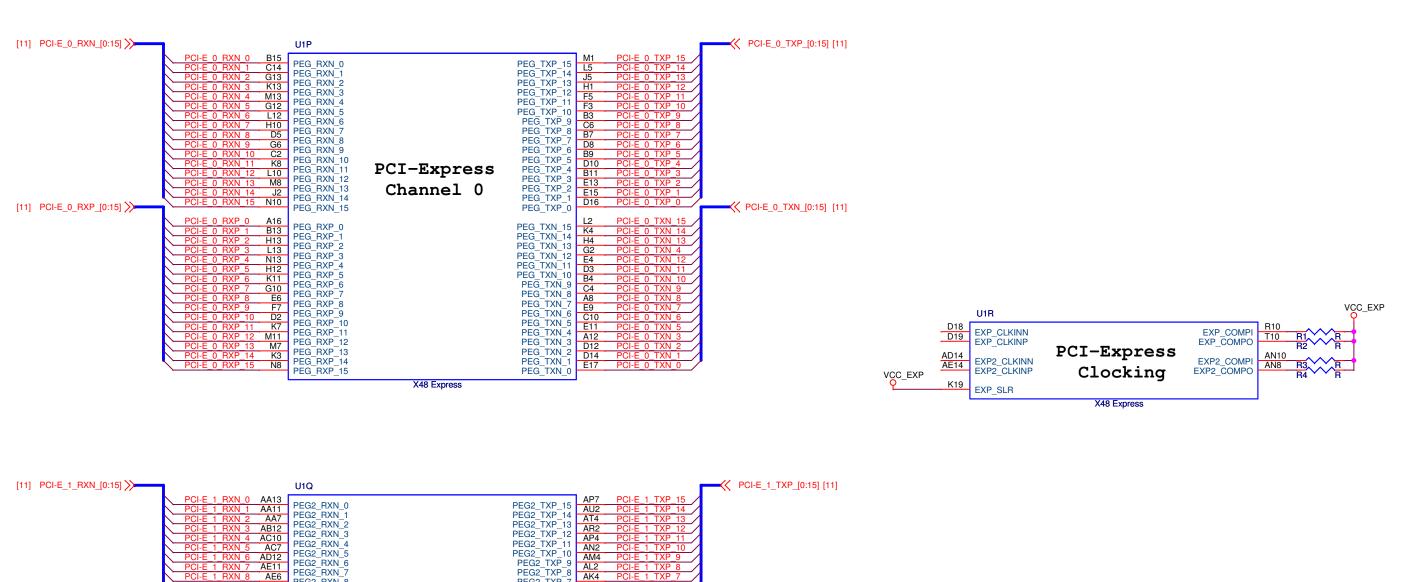
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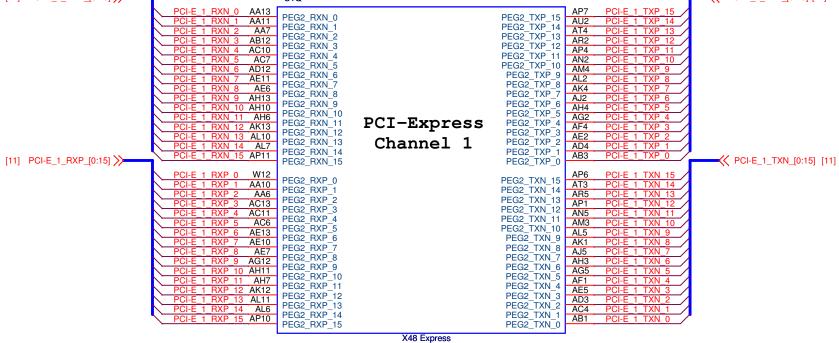
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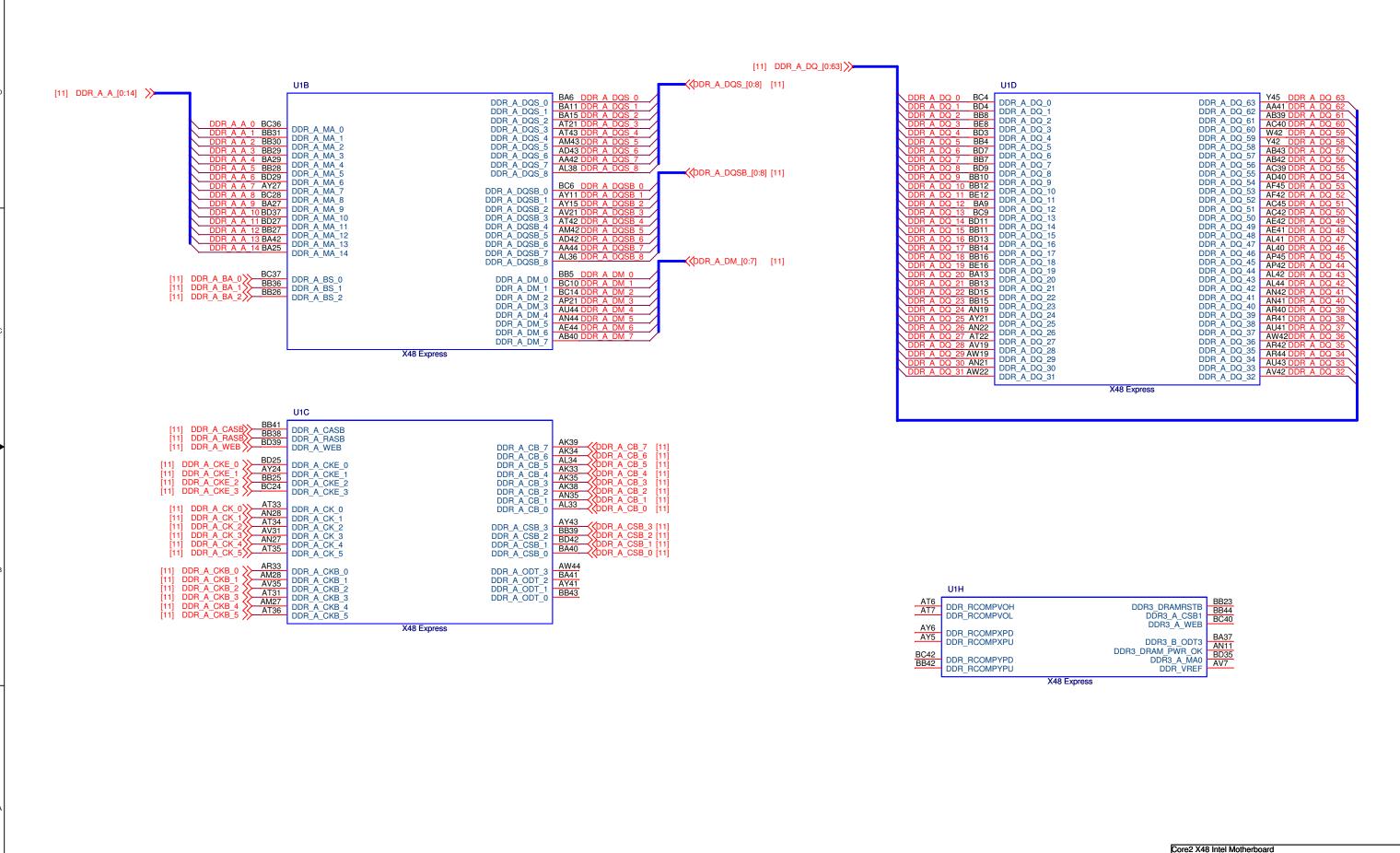
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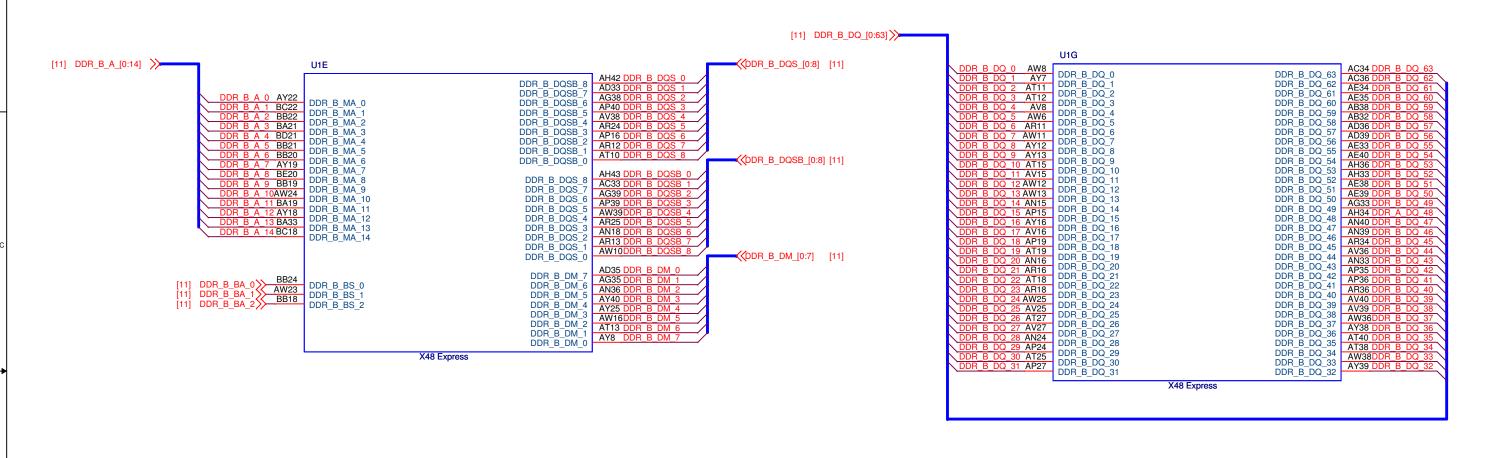
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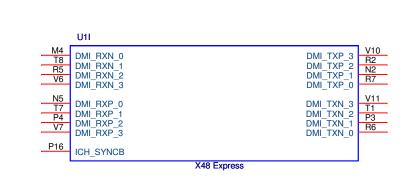


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DDR B CB 4
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DDR B CB 1
DDR B CB 0 DDR_B_RASB **⟨**DDR_B_CB_7 [11] DDR_B_WEB [11] DDR B CKE 0 [11] DDR B CKE 1 [11] DDR B CKE 2 [11] DDR B CKE 2 [11] DDR B CKE 3 [11] DDR B CKE 2 AW30 DDR_B_CK_0 DDR_B_CK_1 DDR_B_CK_2 AR28 AV33 DDR B CK DDR B CK DDR B CSB 3 DDR B CSB 2 [11] BC32 DDR B CSB 2 [11] DDR B CSB 1 DDR B CSB 2 [11] DDR B CSB 0 [11] AR31 AY30 AW34 DDR_B_CK_ DDR_B_CK DDR_B_CK_3 DDR_B_CK_4 DDR_B_CK_5 DDR_B_CKB_0 DDR_B_CKB_1 DDR_B_CKB_2 DDR_B_ODT_ BB33 BB34 DDR_B_ODT_1
DDR_B_ODT_1
BD33 DDR_B_CKB_3 DDR_B_ODT_0 DDR_B_CKB_4 DDR_B_CKB_5 X48 Express

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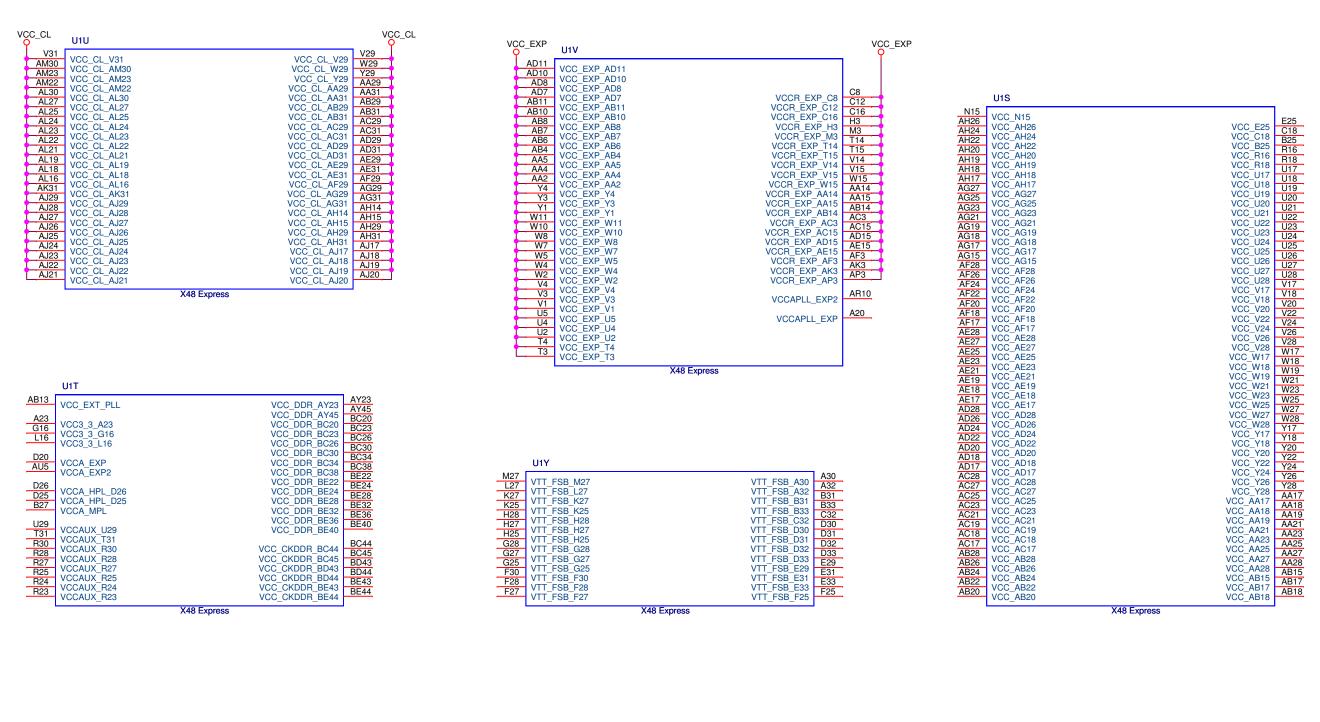


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TEST2 BE1
TEST1 BE45 TCEN G21 AM18 RSTINB X48 Express

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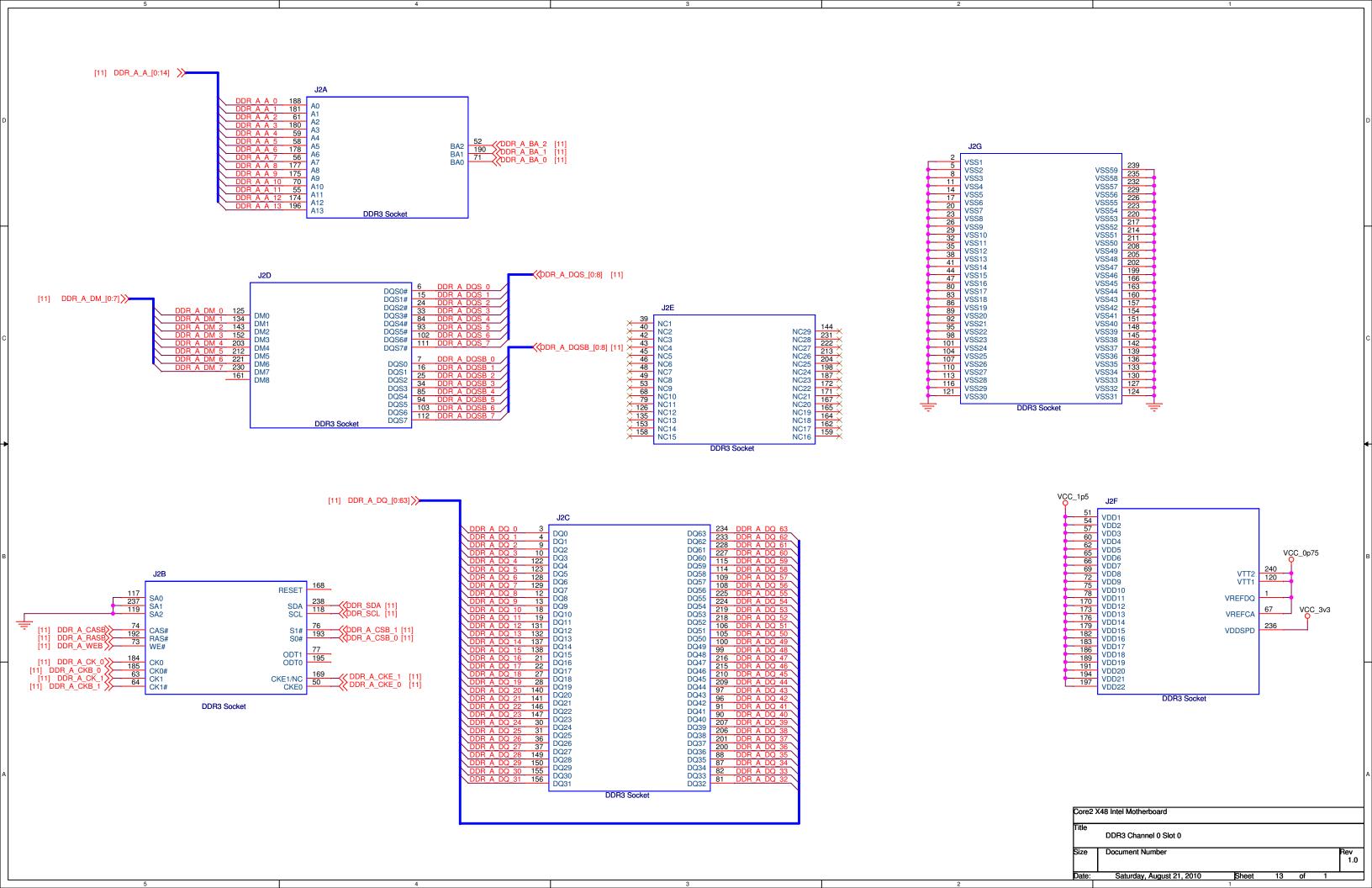
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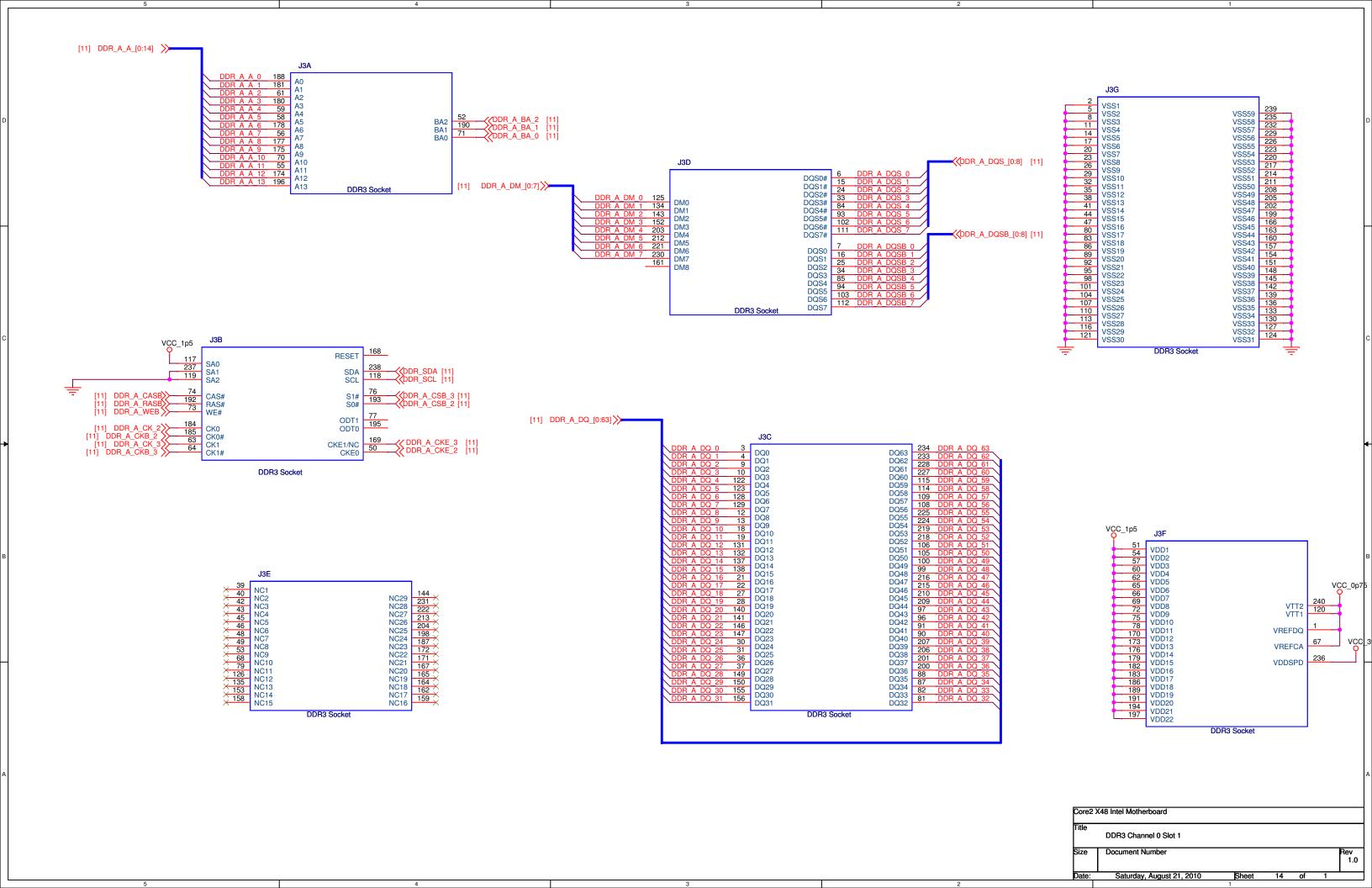
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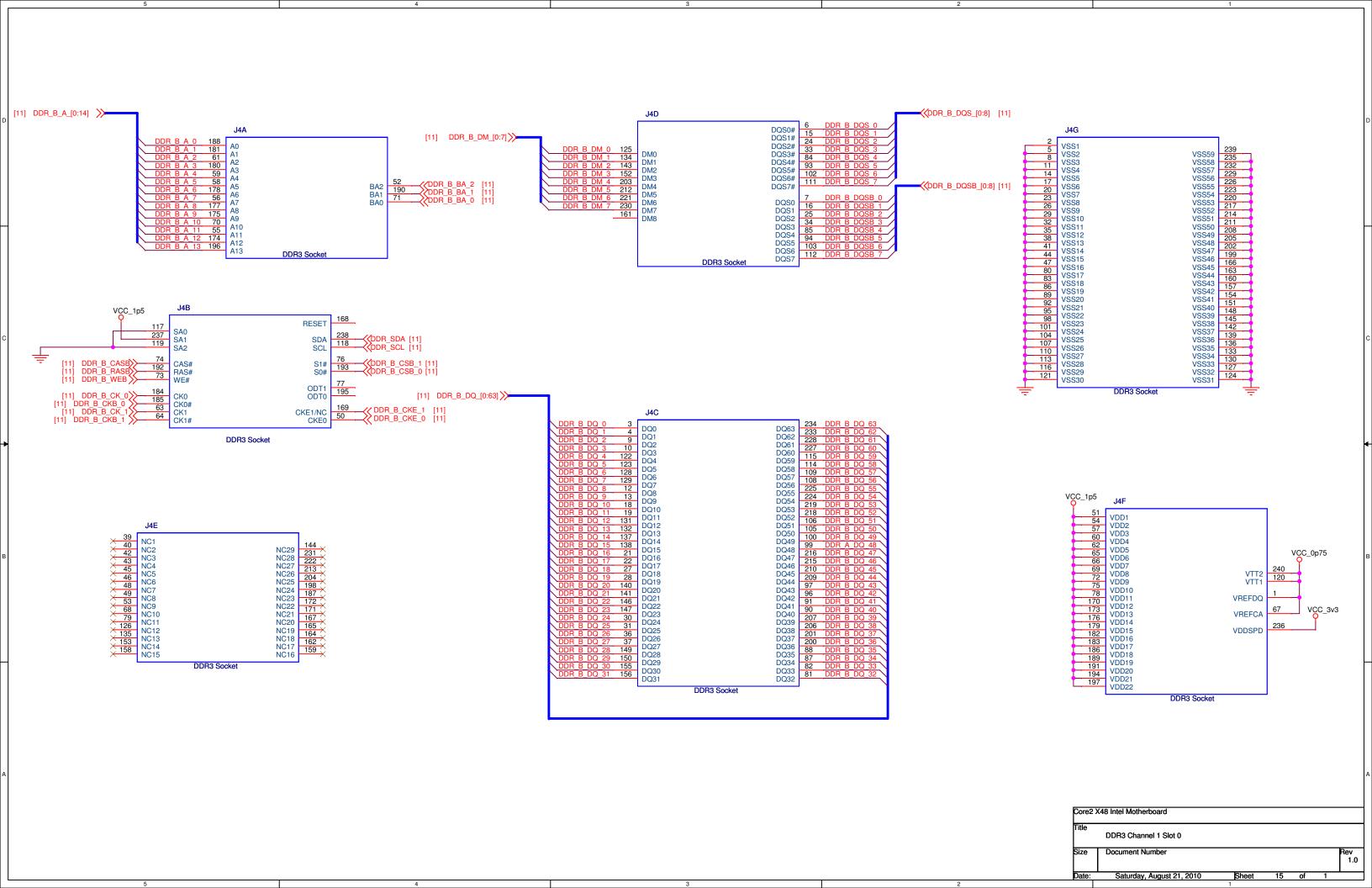


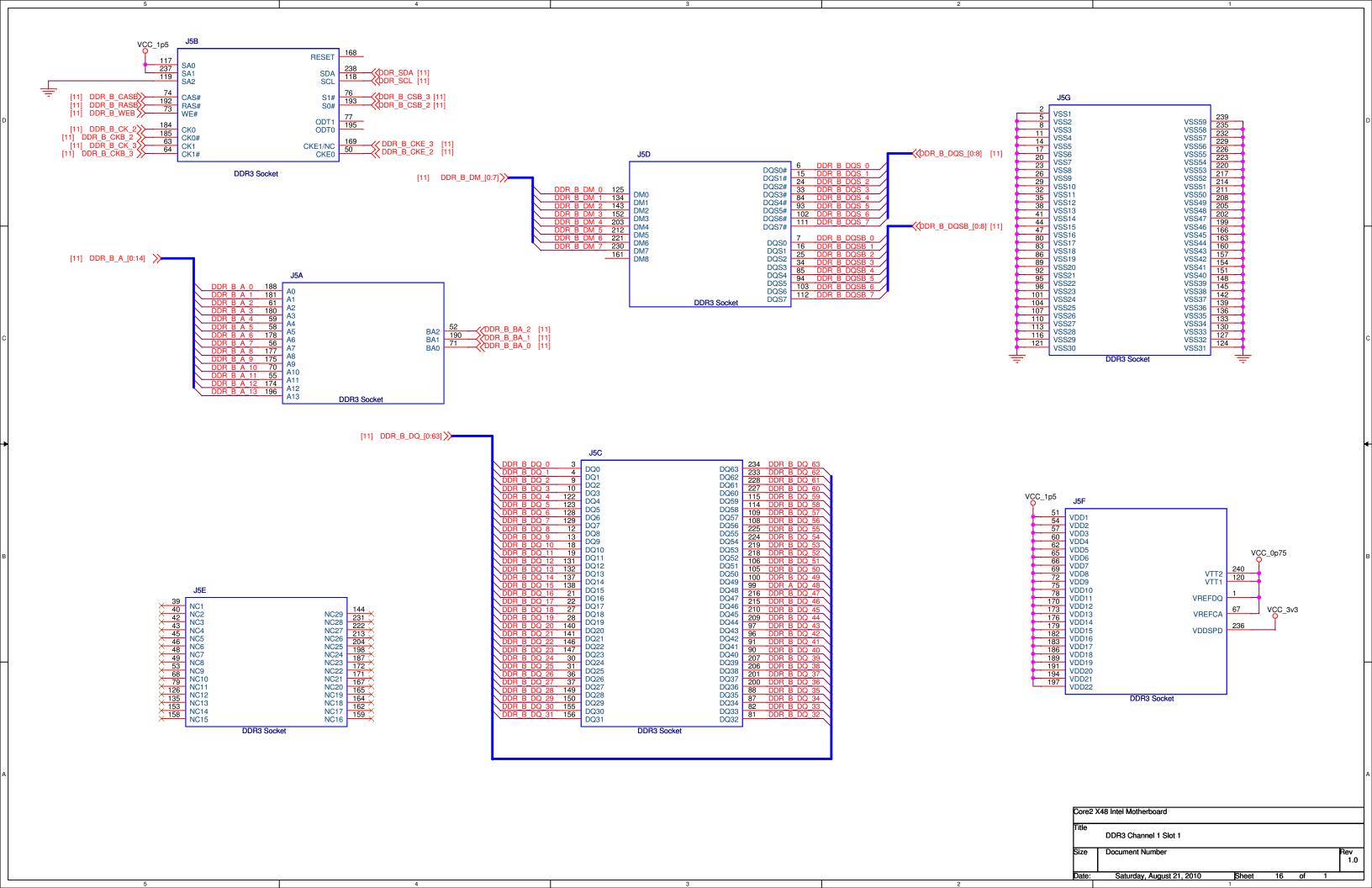
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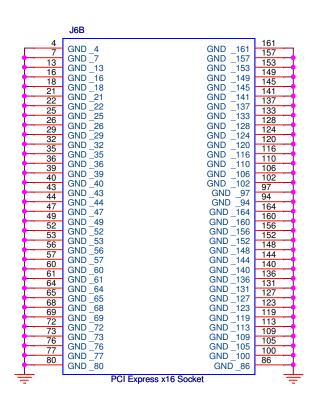
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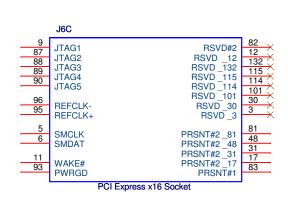


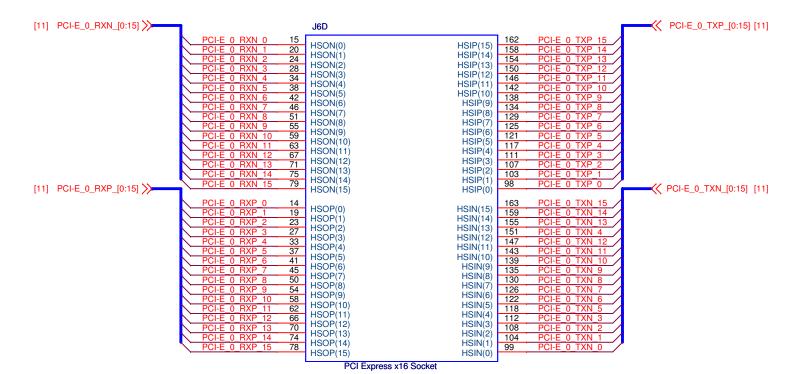






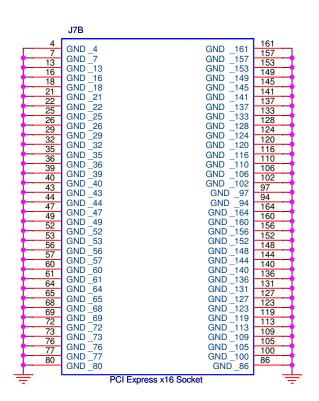


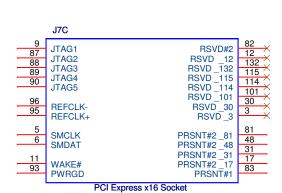






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