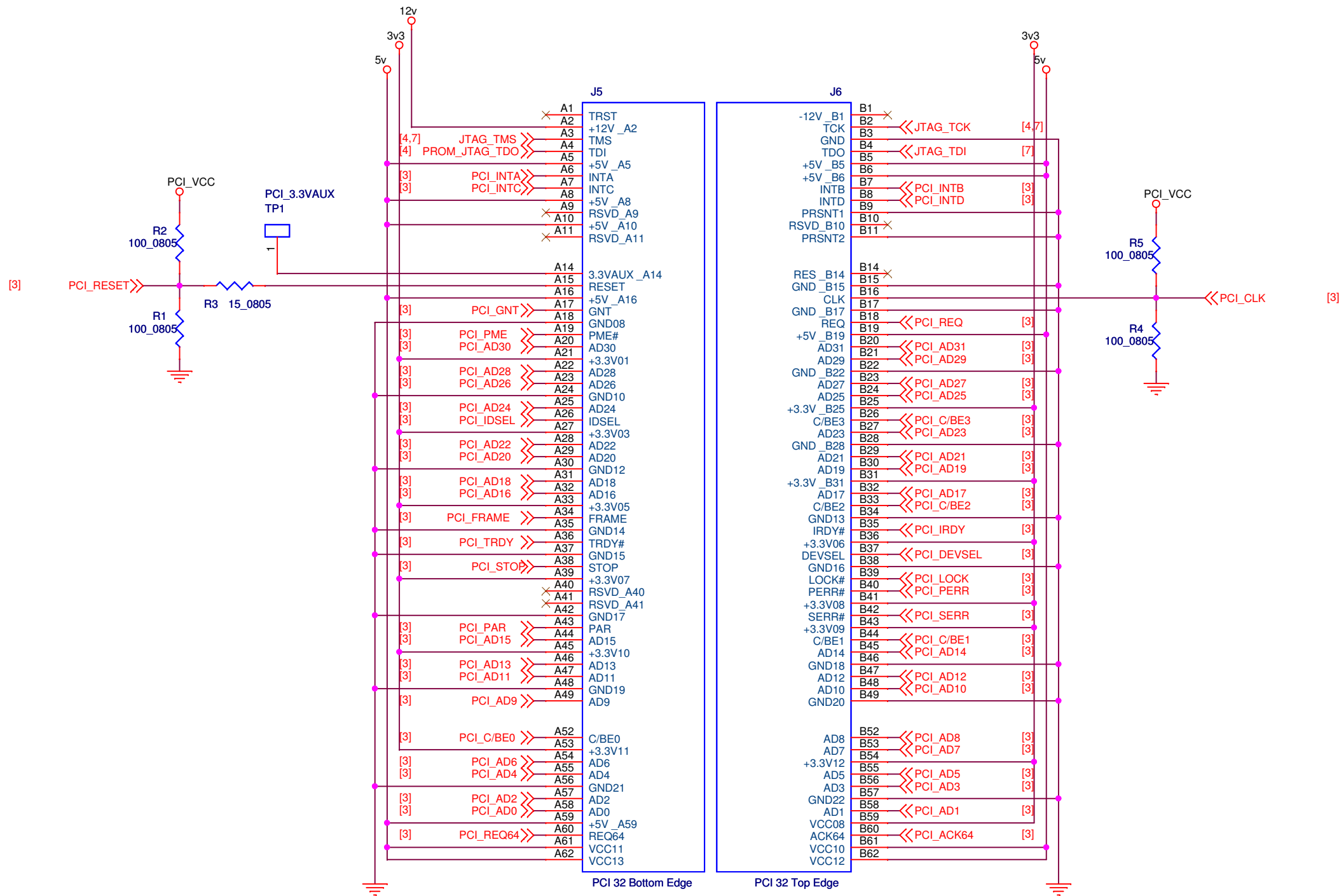
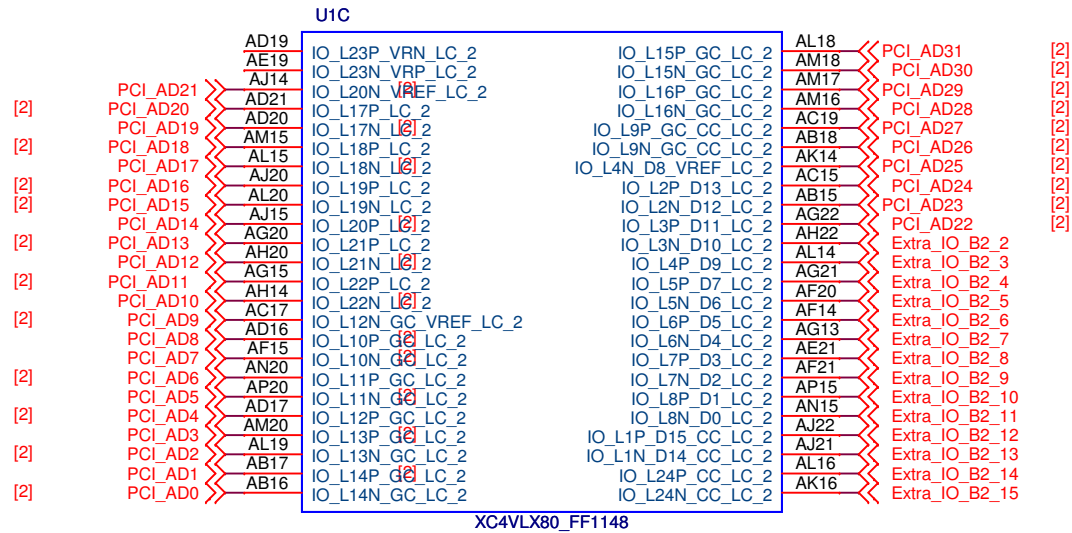
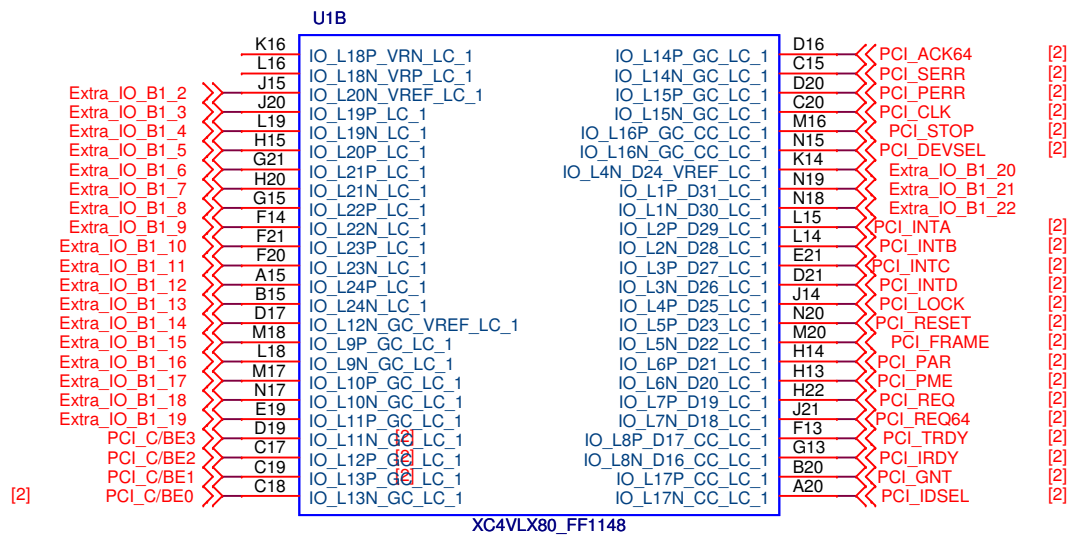


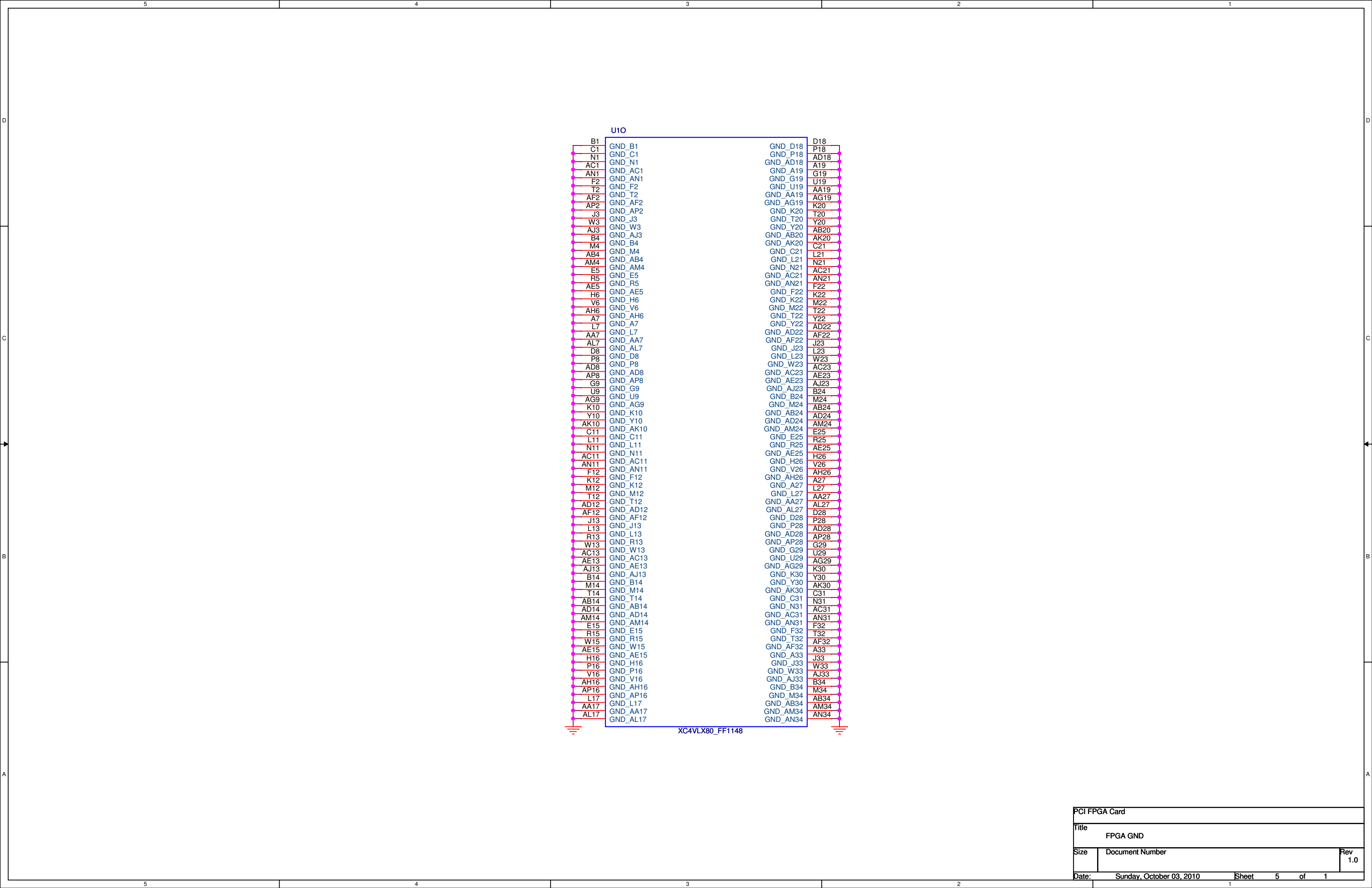
Table of Contents

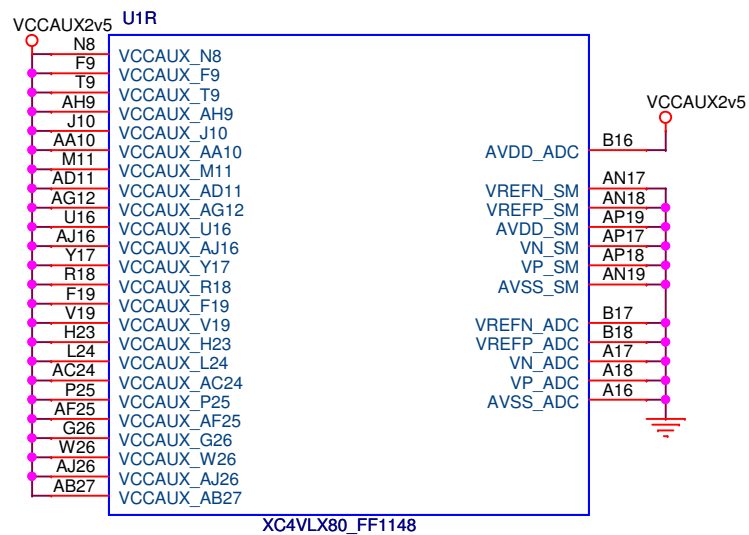
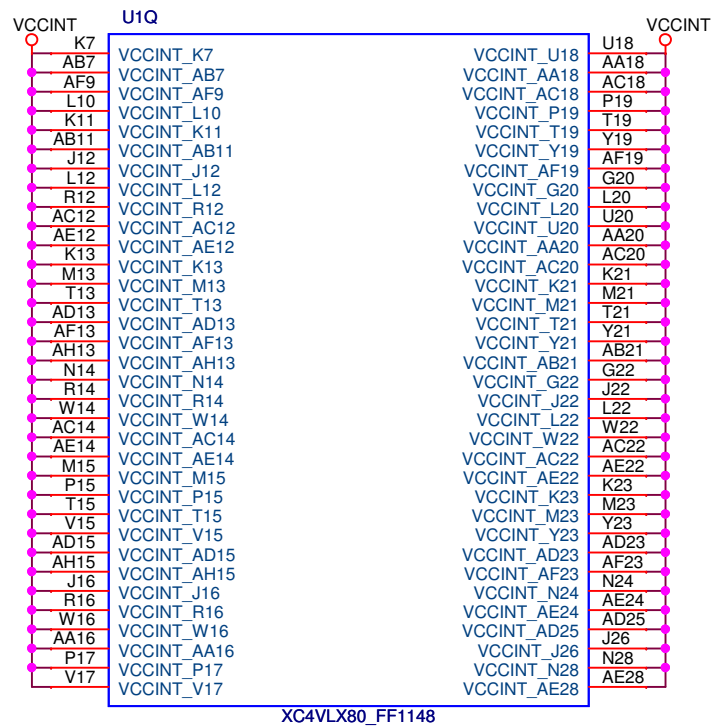
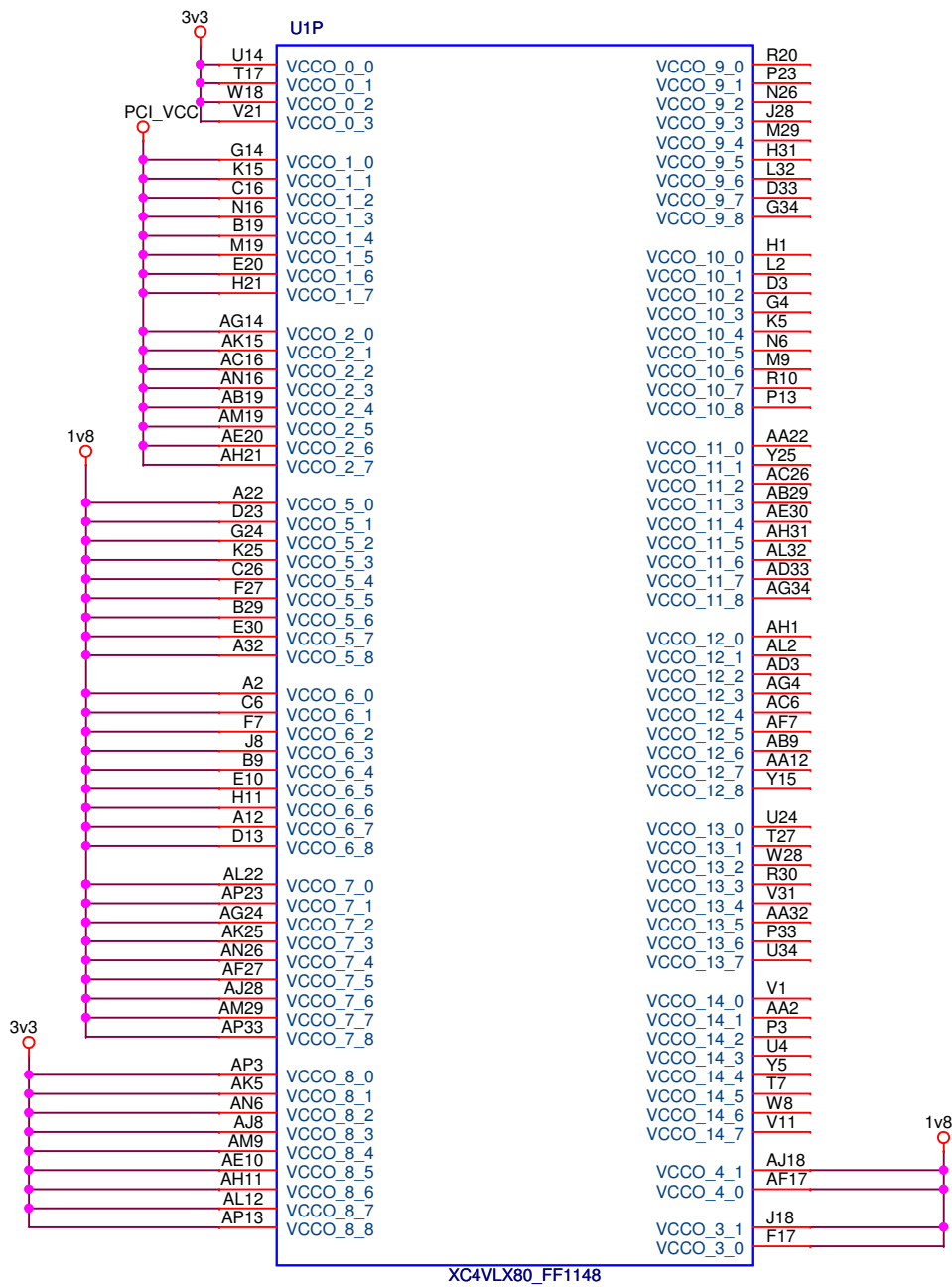
- 01 Info Page
- 02 PCI Connector
- 03 FPGA PCI Banks
- 04 FPGA Config
- 05 FPGA GND
- 06 FPGA Power
- 07 JTAG
- 08 FPGA Extra I/O
- 09 DDR Socket
- 10 FPGA DDR Banks 5,6
- 11 FPGA DDR Banks 3,4,7
- 12 Power Regulation
- 13 Power Distribution

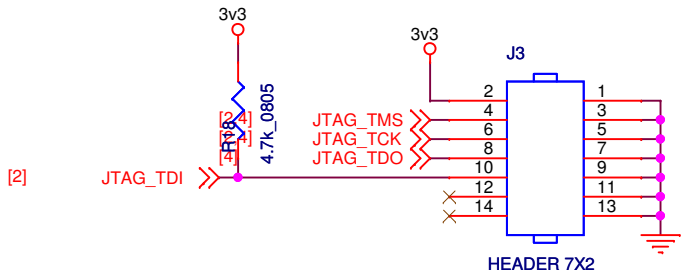


PCI FPGA Card		
Title		
PCI Connector		
Size	Document Number	Rev 1.0
Date:	Sunday, October 03, 2010	Sheet 2 of 1

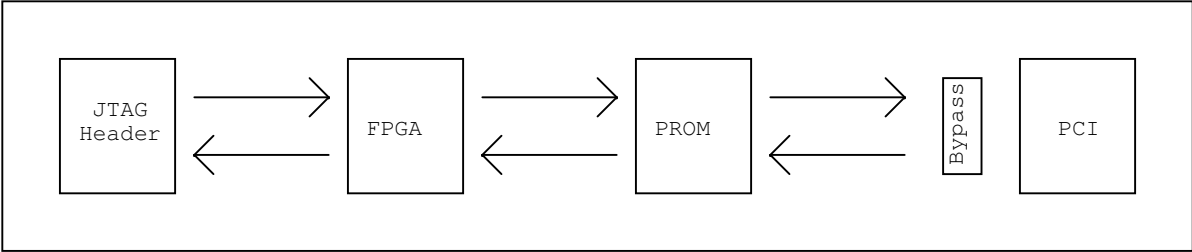


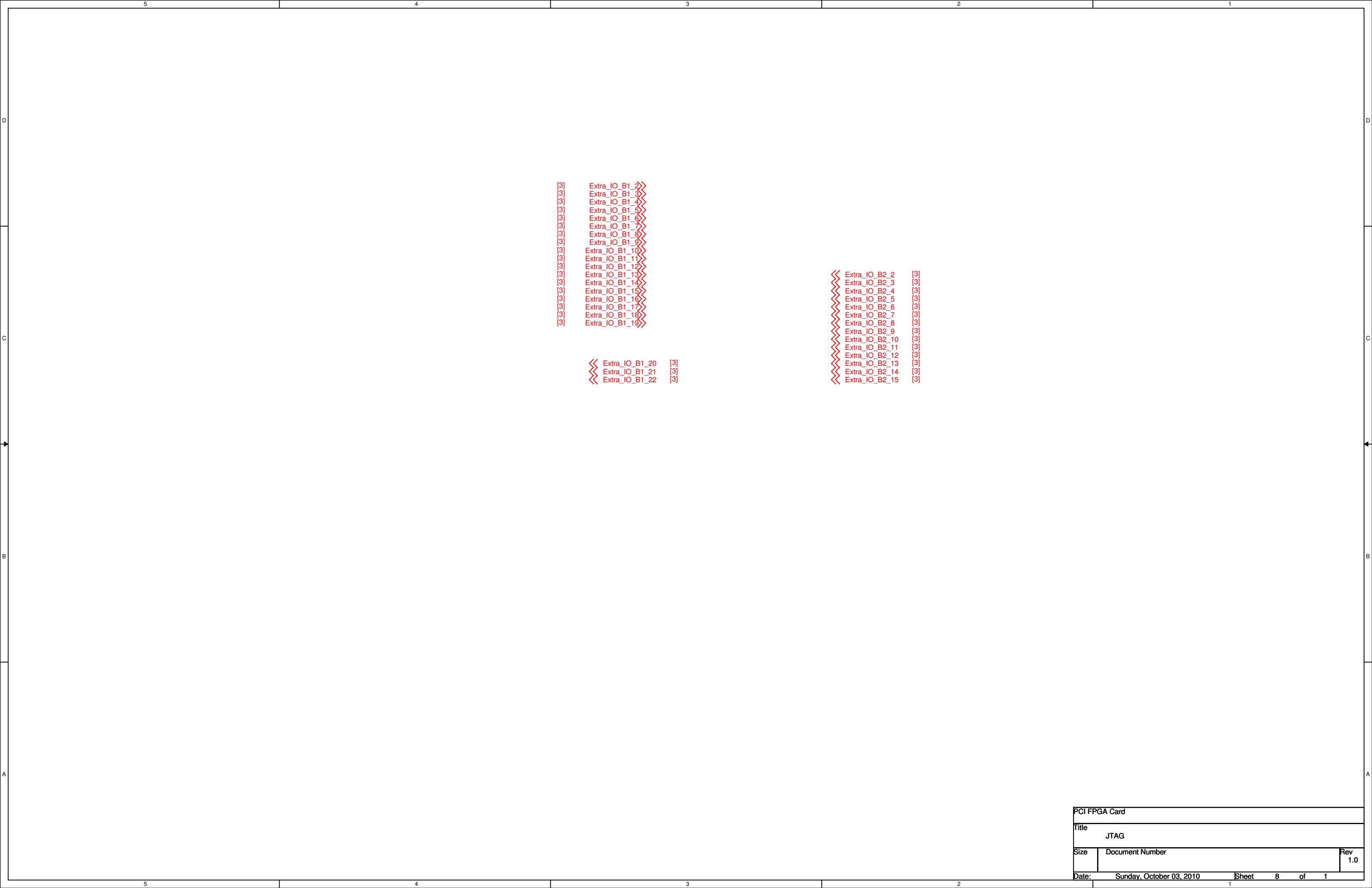


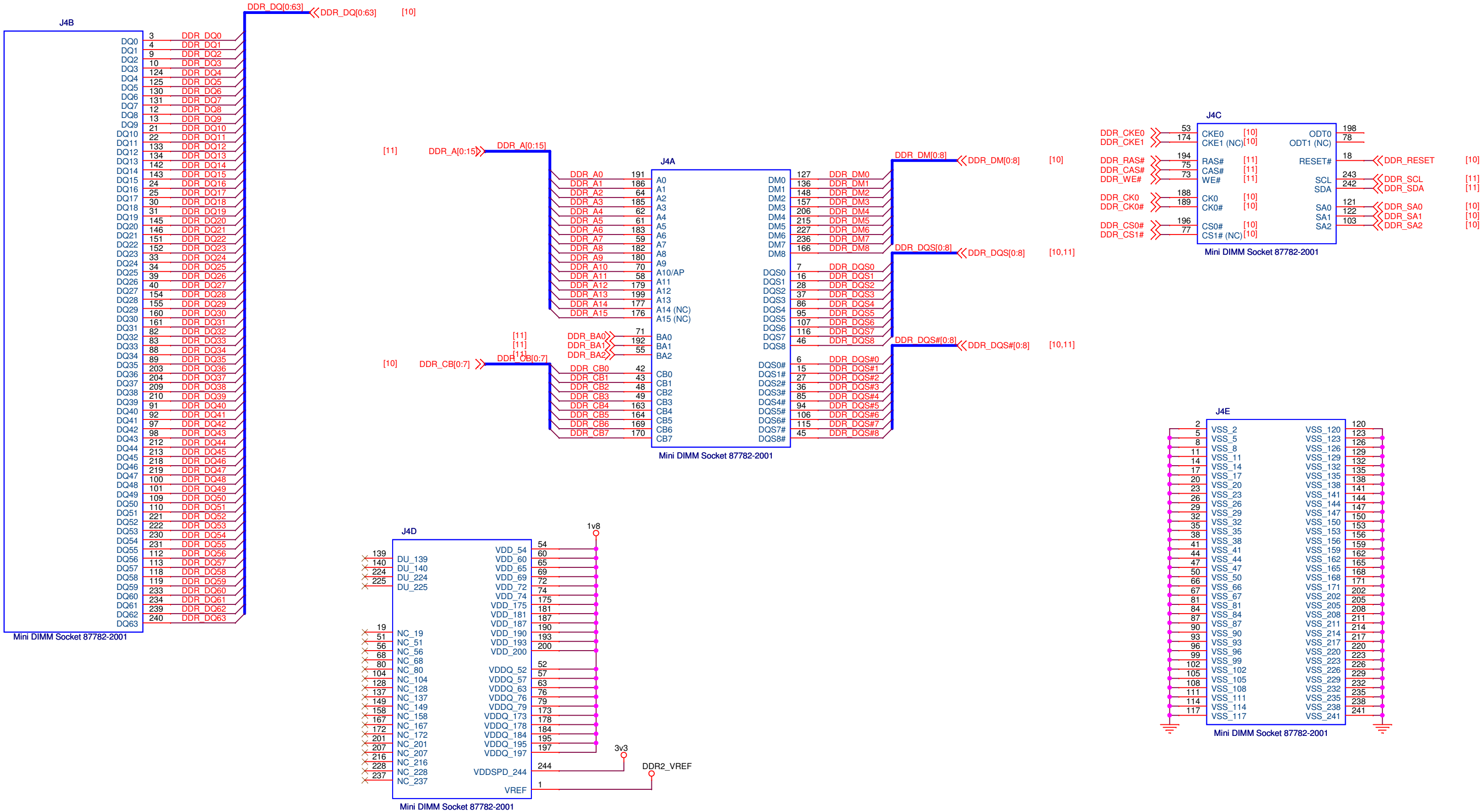


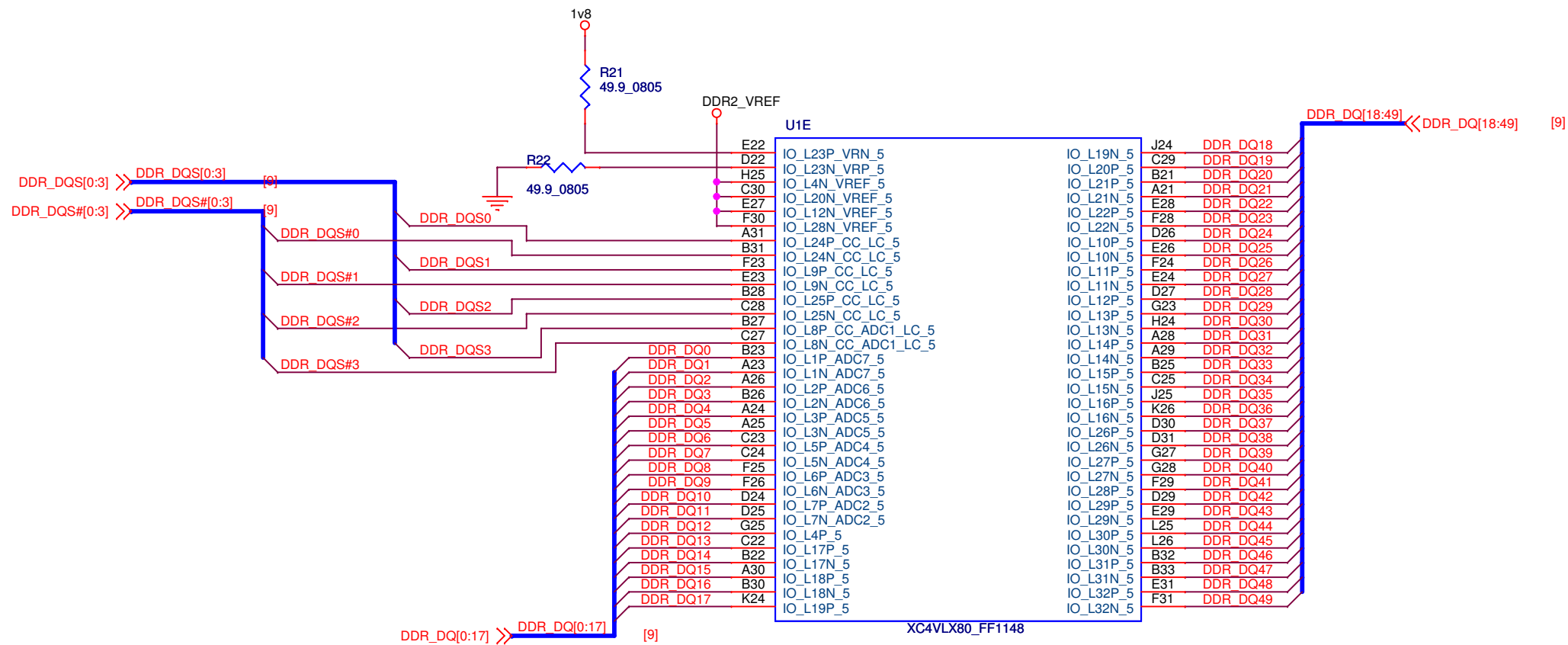
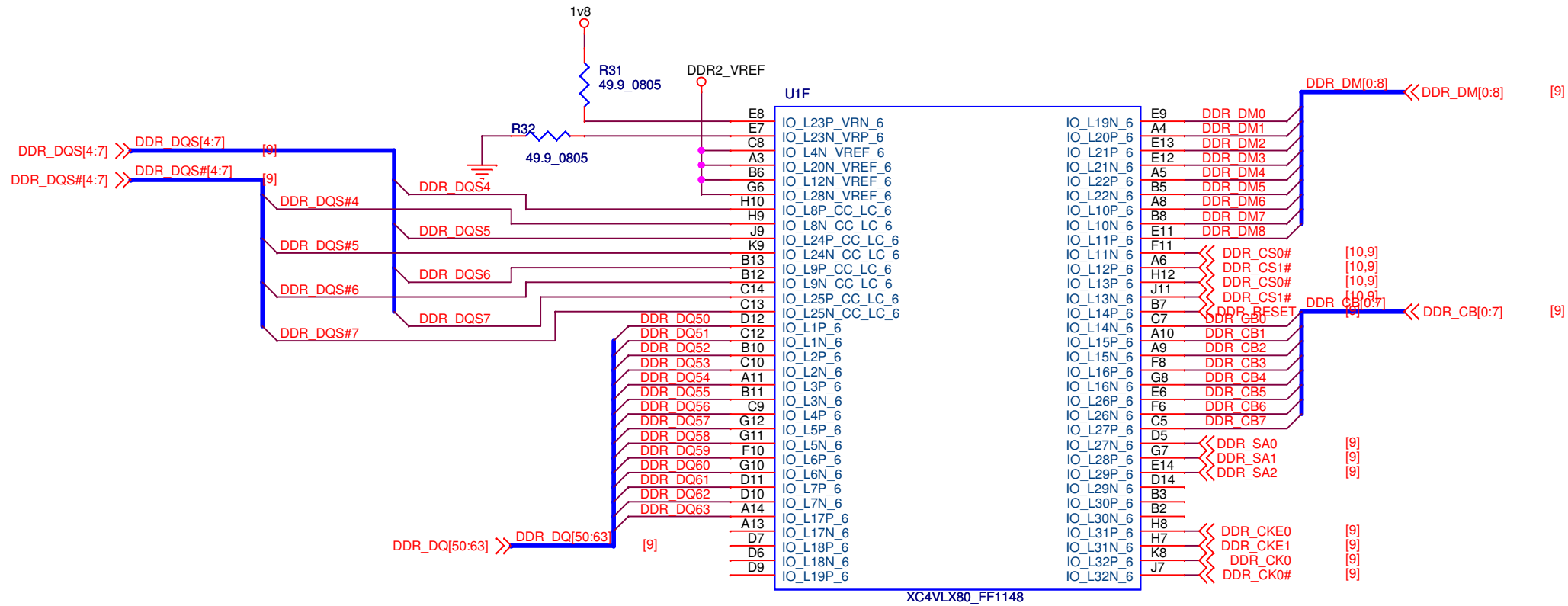


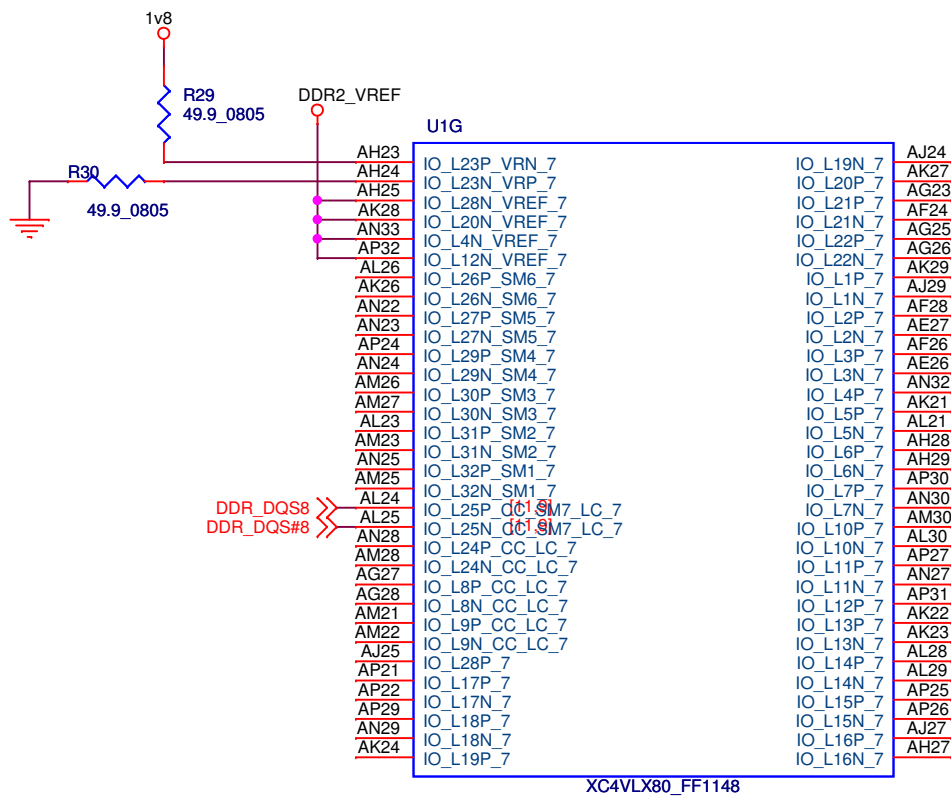
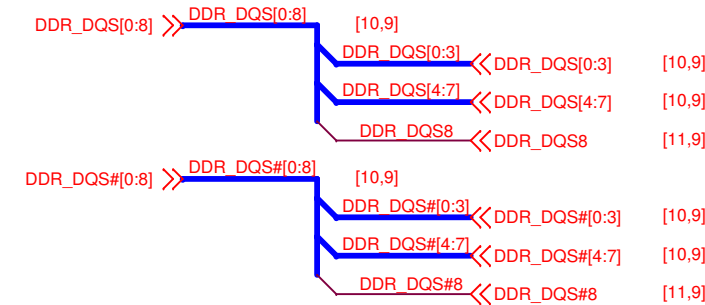
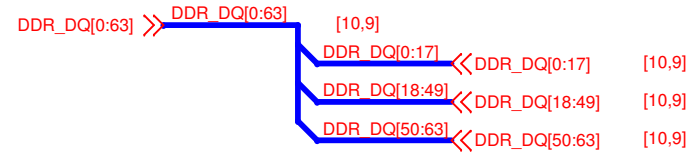
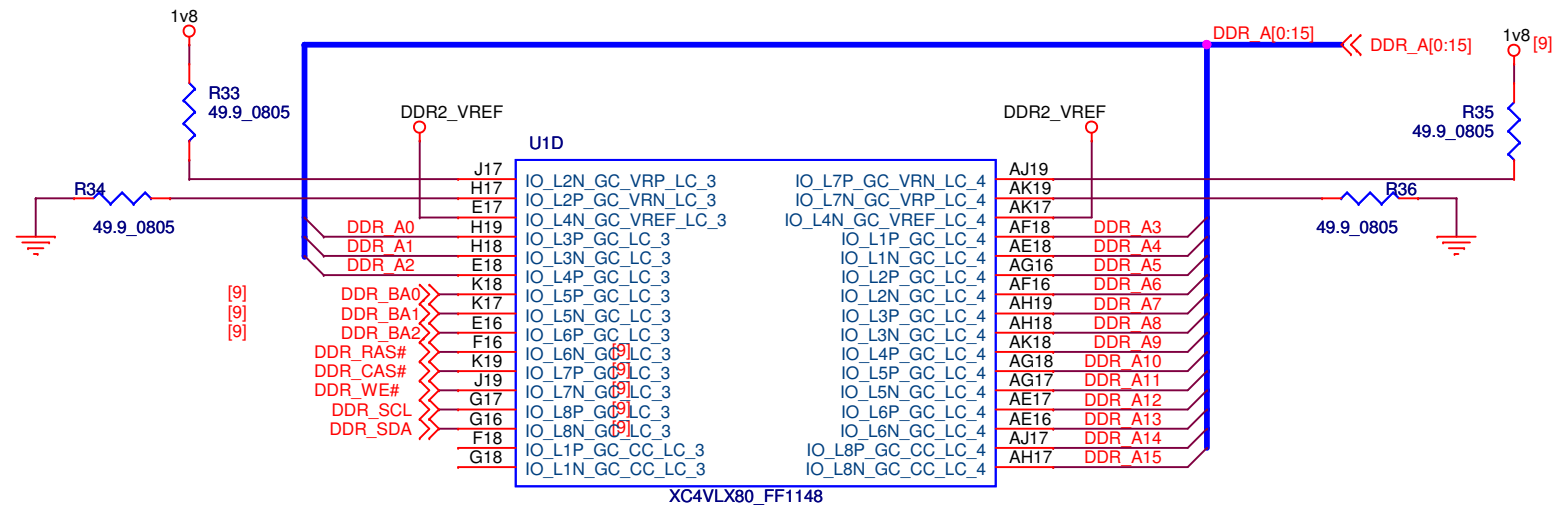
JTAG Path

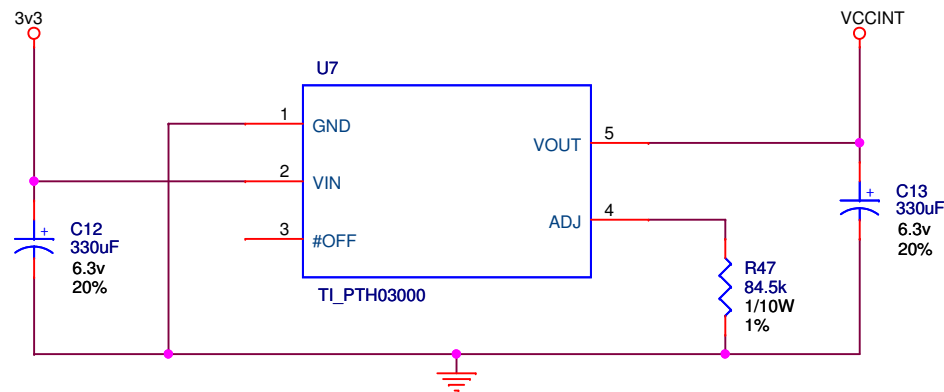
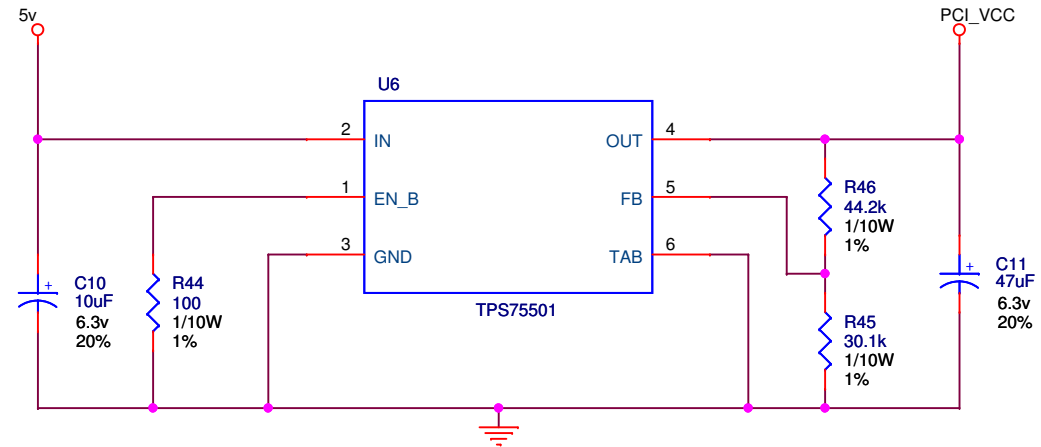
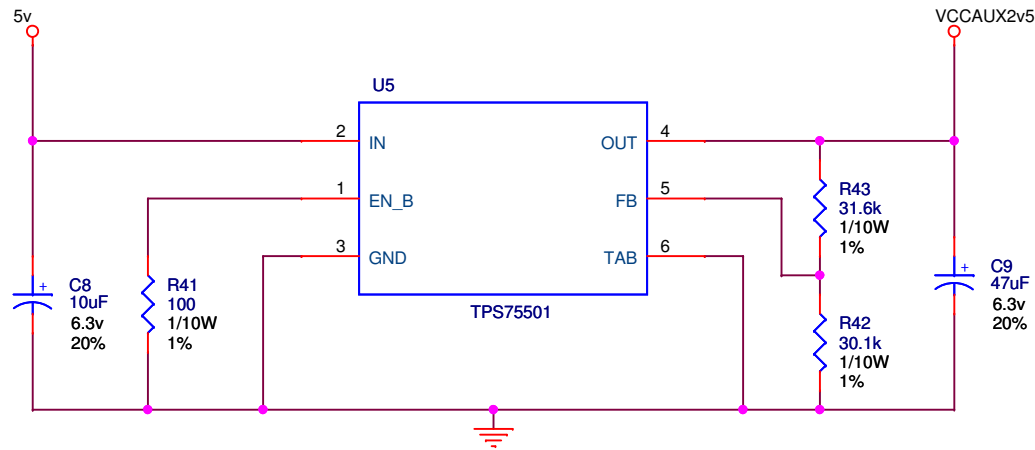
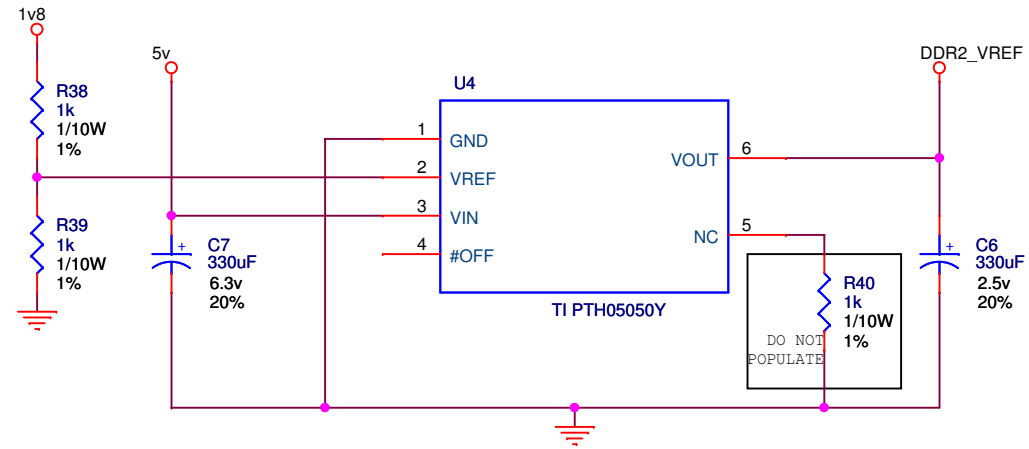
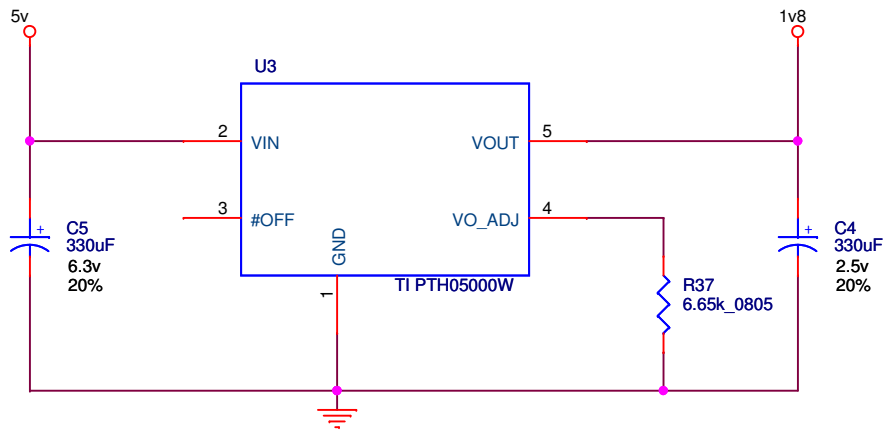












PCI FPGA Card			
Title			
Power Regulation			
Size	Document Number		Rev
			1.0
Date:	Sunday, October 03, 2010	Sheet	12 of 1

