

THIS DESIGN IS BASED ON DESIGN OBJECTIVES AND IS STRICTLY TENTATIVE. IT MAY CHANGE BASED ON RESULTS OF ADDITIONAL DESIGN REVIEWS & VERIFICATIONS.

- NOTES:
1. MATERIAL:  
HOUSING - POLYAMIDE (PA) GLASS FILLED, UL 94V-0, COLOR - BLACK  
LATCH - POLYAMIDE (PA) GLASS FILLED, UL 94V-0, COLOR - BLACK  
TERMINAL - COPPER ALLOY
  2. FINISHES: TERMINAL PLATING SEE TABLE ON SHEET 5
  3. PRODUCT SPECIFICATION: PS-78315-001
  4. PACKAGING SPECIFICATION: PRODUCT SHALL BE PACKED IN TRAYS
  5. CARD SLOT ACCEPTS 1.27 +/- 0.10MM MODULE THICKNESS (MEASURED FROM PC PADS)
  6. MOLEX LOGO, DATE & PART NUMBERS INDICATED ON HOUSING
  7. MODULE SEATING PLANE FROM TOP OF PCB
  8. KEEP OUT ZONE RESERVED FOR LATCH

ADDED OPTION EC NO: S2009-0332 DRWN:CTEH 2009/05/25 CHKD:CGTAN 2009/05/25 APPR:SHLENI 2009/05/27	DESCRIPTION	QUALITY SYMBOLS  ▽=0 ◻=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
				mm	INCH	DRAWN BY CMTEO	DATE 2008/07/17	TITLE DDR3 DIMM (LSP, VLP) 1.00MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR			
			4 PLACES	± ---	± ---	CHECKED BY CCTEH	DATE 2008/07/30				
			3 PLACES	± ---	± ---	APPROVED BY SHLENI	DATE 2008/08/01	MOLEX INCORPORATED			
			2 PLACES	± 0.20	± ---	MATERIAL NO.					
			1 PLACE	± ---	± ---	SEE TABLE		SD-78315-001		1 OF 6	
			ANGULAR ± 1 °								
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				

10 9 8 7 6 5 4 3 2 1

F

E

D

C

B

A

F

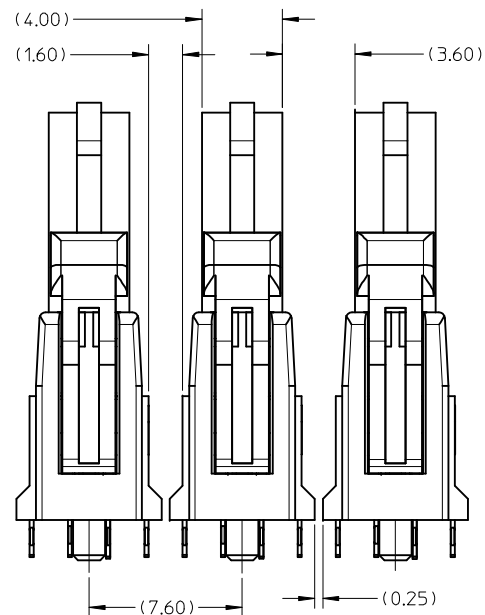
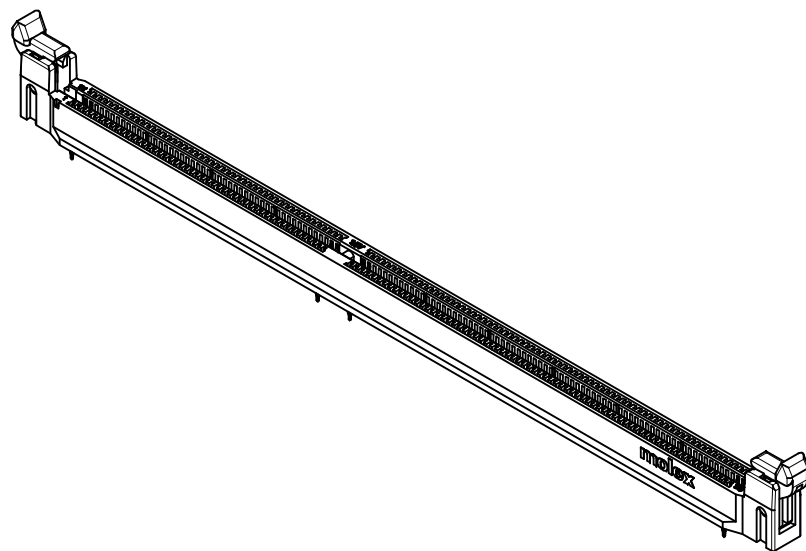
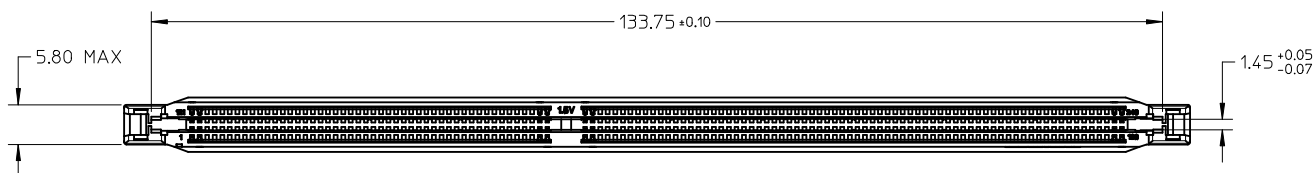
E

D

C

B

A

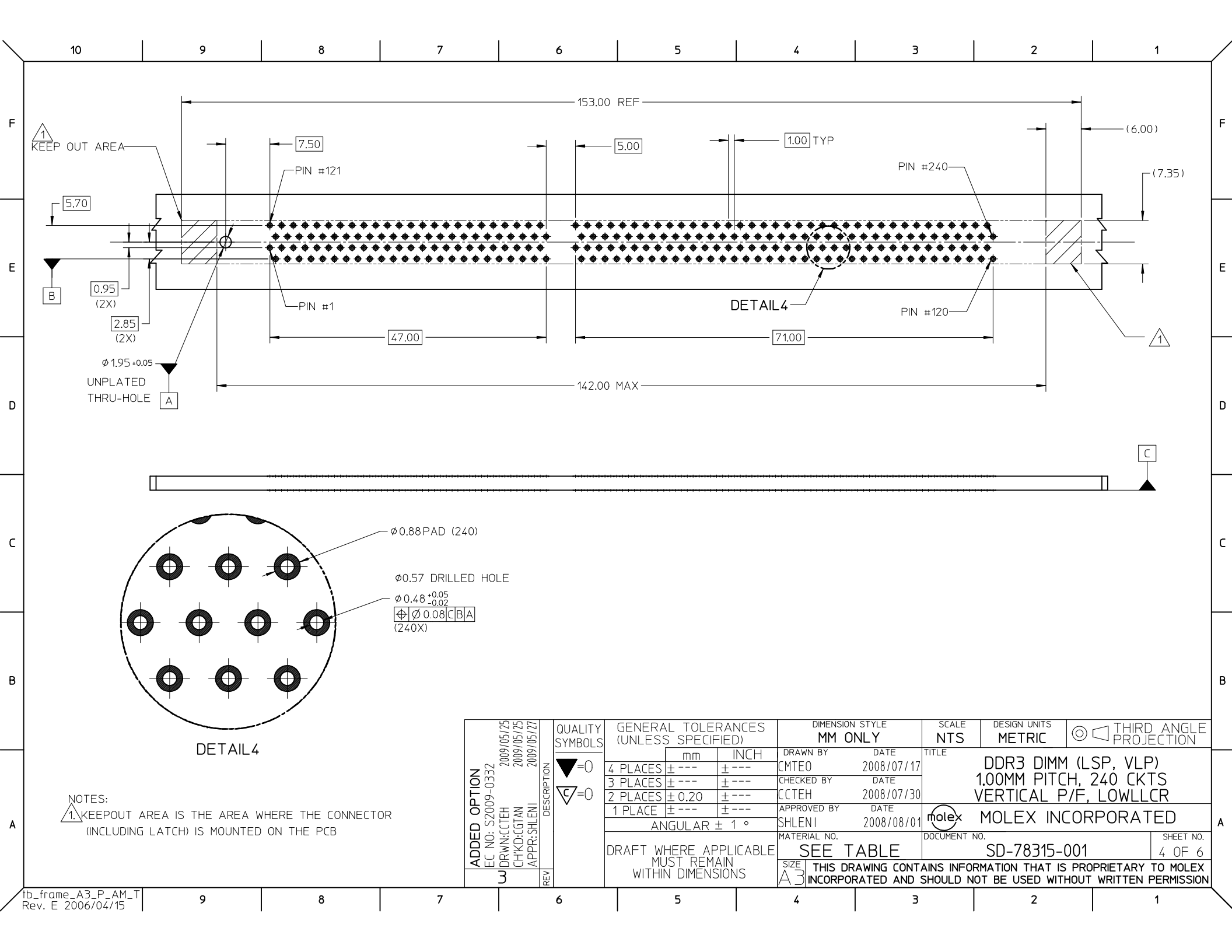


MINIMUM ROW TO ROW SPACING RECOMMENDATION  
(USING 4.00MM MODULE CARD)

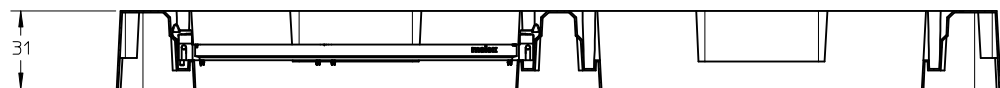
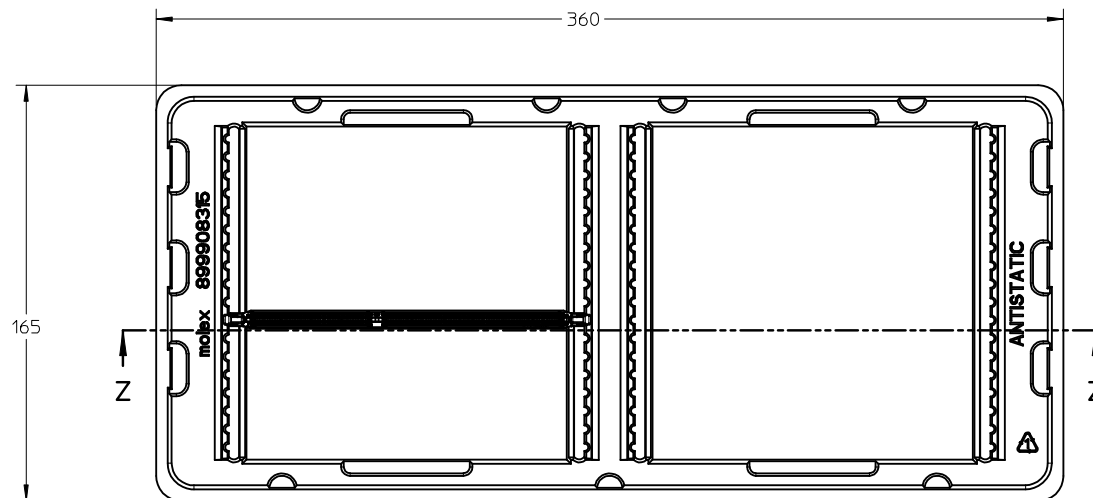
ADDED OPTION EC NO: S2009-0332 2009/05/25 DRWN:CTEH CHKD:CGTAN 2009/05/25 APPR:SHLENI 2009/05/27			DESCRIPTION	QUALITY SYMBOLS  ▽=0 ◻=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
REV 3	DESCRIPTION	4 PLACES		mm	INCH	DRAWN BY CMTEO		DATE 2008/07/17		TITLE DDR3 DIMM (LSP, VLP) 1.00MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR  MOLEX INCORPORATED SD-78315-001				
		3 PLACES		± ---	± ---	CHECKED BY CCTEH		DATE 2008/07/30						
		2 PLACES		± 0.20	± ---	APPROVED BY SHLENI		DATE 2008/08/01						
		1 PLACE		± ---	± ---	MATERIAL NO.		DOCUMENT NO.						
				ANGULAR ± 1 °		SEE TABLE				SHEET NO. 2 OF 6				
				DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						

9 8 7 6 5 4 3 2 1





# VIEW FOR PACKAGING TRAY



SECTION Z-Z

NOTE:

1. NO. OF CAVITY - 18 X 2 =36

ADDED OPTION EC NO: S2009-0332 DRWN:CTEH 2009/05/25 CHKD:CGTAN 2009/05/25 APPR:SHLENI 2009/05/27	DESCRIPTION REV	QUALITY SYMBOLS <div>▽=0</div> <div>◻=0</div>	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION				
				mm	INCH	DRAWN BY CMTED	DATE 2008/07/17	TITLE DDR3 DIMM (LSP, VLP) 1.00MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR					
			4 PLACES	± ---	± ---	CHECKED BY CCTEH	DATE 2008/07/30						
			3 PLACES	± ---	± ---	APPROVED BY SHLENI	DATE 2008/08/01						molex MOLEX INCORPORATED
			2 PLACES	± 0.20	± ---	ANGULAR ± 1 °		DOCUMENT NO. SD-78315-001		SHEET NO. 5 OF 6			
			1 PLACE	± ---	± ---	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE TABLE					
							MATERIAL NO.						
							SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				

