



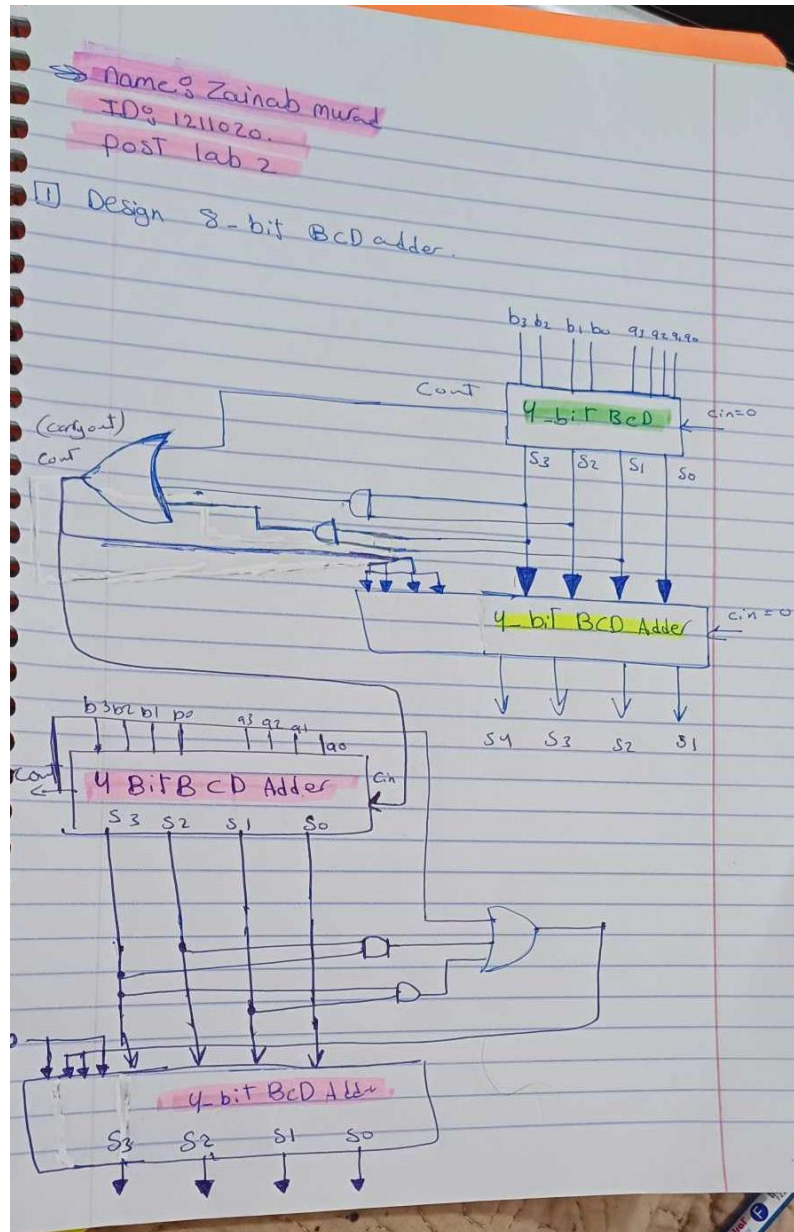
Faculty of Engineering and Technology Department
of Electrical and Computer Engineering

ENCS 2110

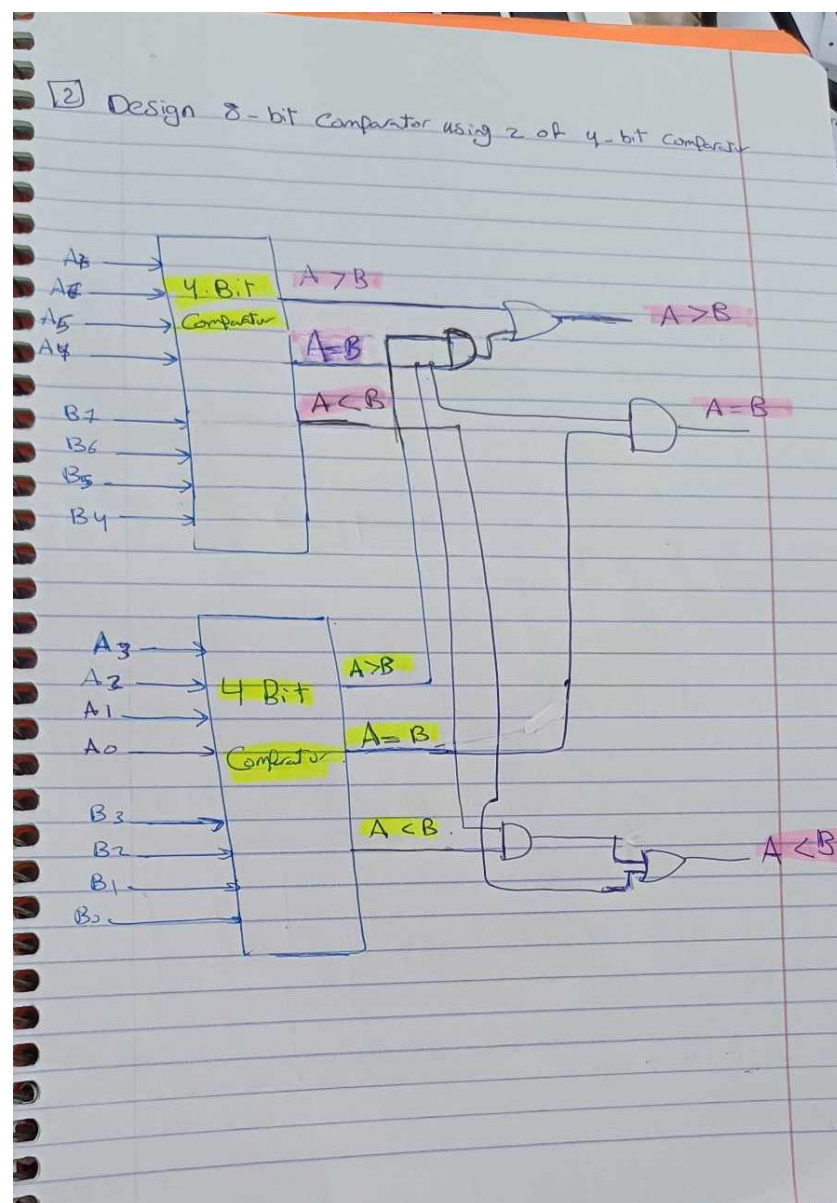
Digital Electronics and Computer Organization
Lab Experiment No. 2 - Comparators, Adders, and
Subtractors

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QUESTION1



QUESTION2:



QUESTION3

③ A 4-input, 3 output circuit that compares 2 bit unsigned numbers and outputs a 1 on one of three output lines according to whether the first number is greater than, equal to, or less than the other number. You can only use two 4x1 multiplexers.

