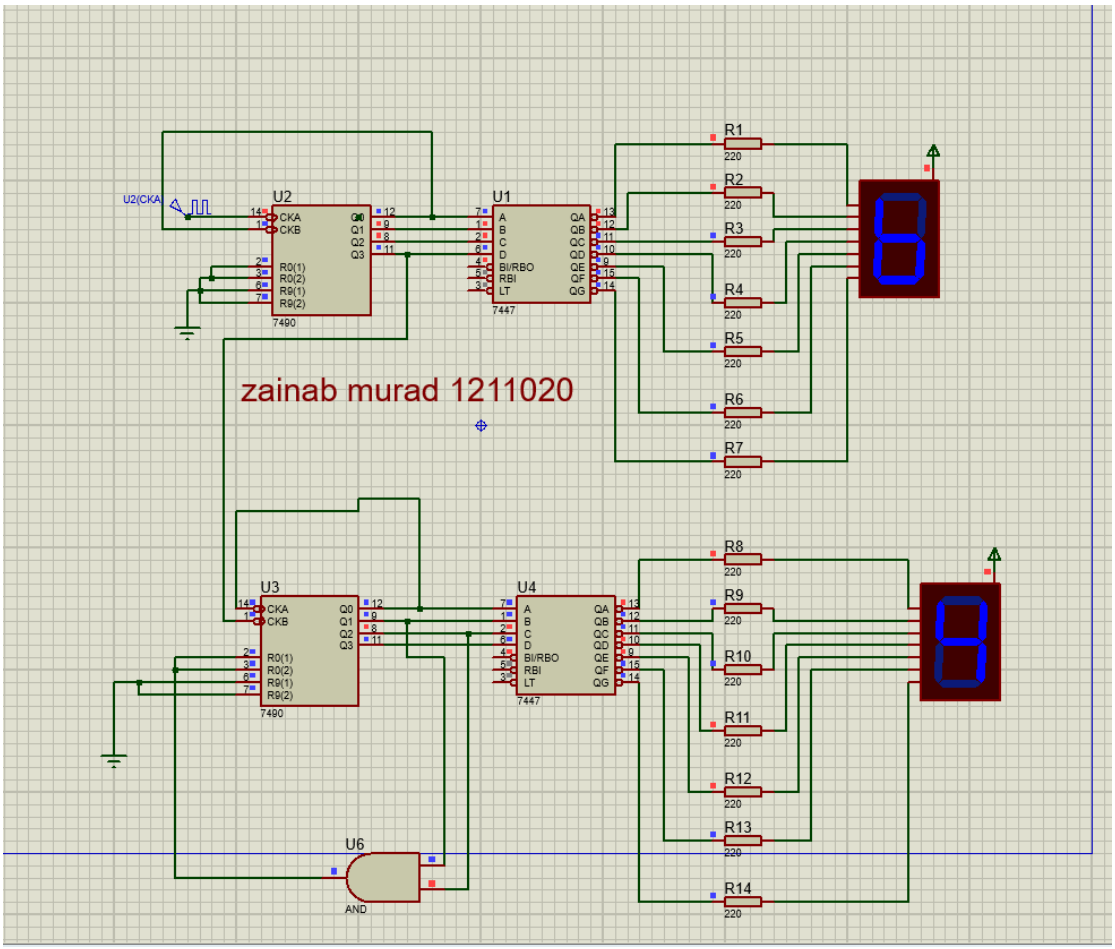
Faculty of Engineering and Technology Department
of Electrical and Computer Engineering

ENCS 2110

Digital Electronics and Computer Organization Lab Experiment No. 6&7- - Sequential Logic Circuits using Breadboard and IC's &Constructing Memory Circuits Using Flip–Flops (POST Lab)

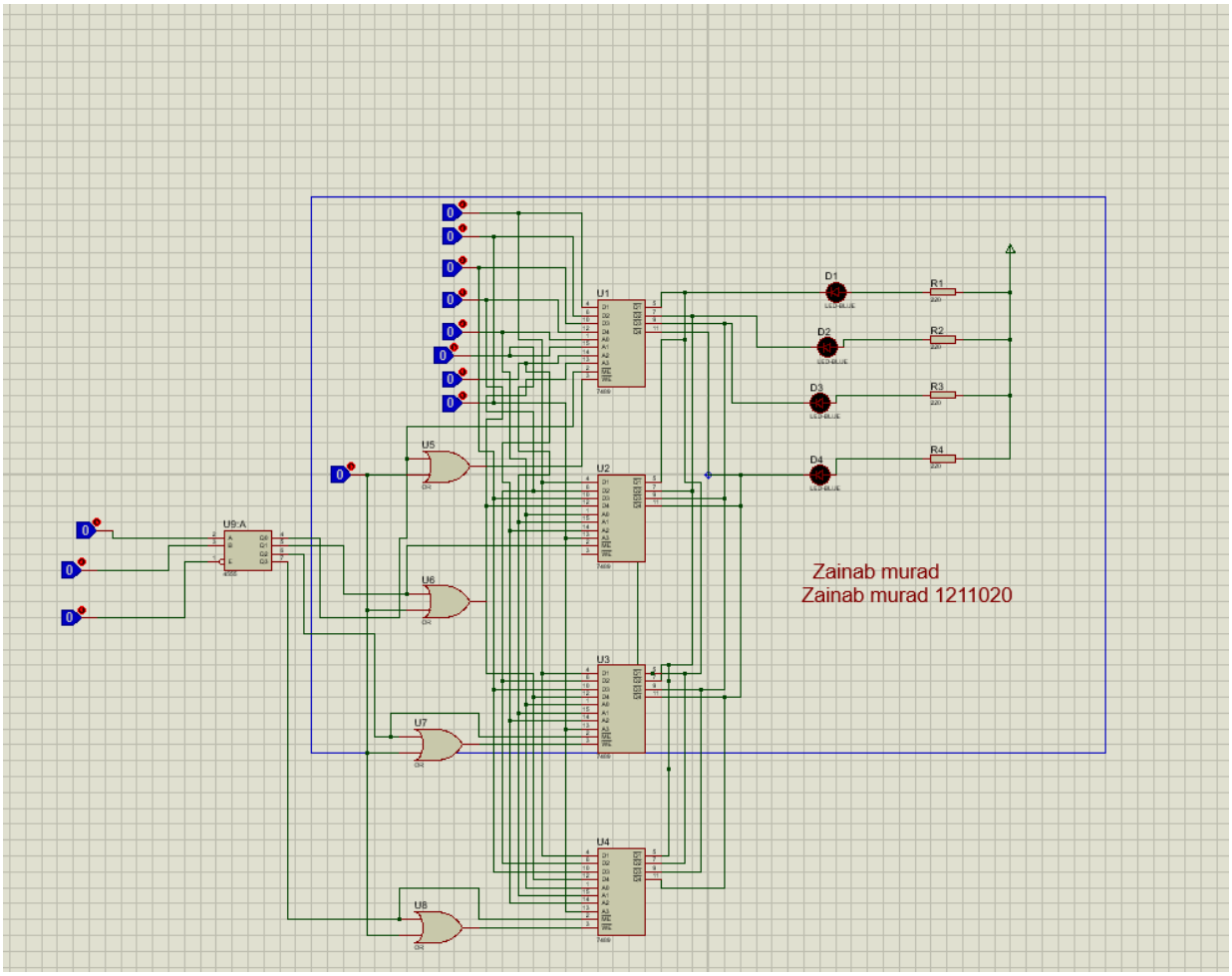
Name:	**Zainab murad ismail**
ID:	**1211020**
Instructor:	**Ashraf Al-Rimawi**
Section:	**4**

3. Modify the counter to count to 59 (without Reset).6.3



7.1

1. Design a 4x16 RAM using four 4x4 RAMS.



Although D latches are useful for storing binary information they are not used in RAM circuit design, why?

They don't allow the quick access time that's required of the RAM

D latches are not clocked which means they are level triggered

which means we have high instability, a lot of power consumption time delays , and could even cause it to lose information. Also, its

harder to integrate these circuits in comparison to Flip-flops