No	Subject	Description	reference
1	the same channel of I2C function is assigned to another pins	For controlling ICM-20649, 92 pin is assigned I2C clock and 93 pin is assigned I2C data. For controlling MCP9805-BE/ST, 95 pin is assinged I2C clock and 96 pin is assigned I2C data. Although 92/93/95/96 pins can be I2C function, but these pins have only "I2C1" function. I don't think it can be used this way. So I suggest the diffrent channel of I2C is used for each device. If the same channel of I2C is needed to use, I think multi-slave connection should be used.	"No1" Sheet
2	About the pins for decide the I2C slave address	The pins below are used for decide the I2C slave address. AD0 : 9 pin of ICM-20649 A0 : 1 pin of MCP9805-BE/ST A1 : 2 pin of MCP9805-BE/ST A2 : 3 pin of MCP9805-BE/ST In my experience, these pins are tied HIGH or LOW usually. Because I2C slave address is not needed to change dynamically. These pins are connected to STM32F407VG, but I can't understand what is intended. Is there any reason NOT to tie them HIGH or LOW?	"No2" Sheet
3	About the Solder Bridges between SPI1 bus and SPI2 bus	Are SB1 to SB4 intended for SPI multi-slave connection that means that SPI1 of STM32F407VG is used for controlling both TMC5160A and AEAT-8800-Q24? To short the SB1 to SB4 means connect SPI1 bus to SPI2 bus on current schematics. If SPI2 is incorrectly enabled by the firmware while SPI1 is enabled and SB1 to SB4 are shorted, SPI1's outputs and SPI2's outputs are connected and in the worst case the pins can be destroyed. If you are preparing SB1 to SB4, I think you should also in include a mechanism to open the SPI2 bus for safety.	"No3" Sheet
4	About the BOOT1 pin	BOOT0 pin and BOOT1 pin, 94 pin and 37 pin of STM32F407VG are used to decide the boot modes. BOOT0 pin can be varied HIGH or LOW using SW2 on the Top Board, but BOOT1 pin is fixed to LOW. STM32F407VG have 3 boot modes. If BOOT1 pin is fixed to LOW, only 2 modes are available. Although I'm not sure if I want to use all three modes for now, is it possible to be able to change the state of the BOOT1 pin?	"No4" Sheet
5	About the two USB connectors	There are two USB connectors, one is on the Bottom Board with VBUS 5V connection and another is on the Top Board without VBUS 5V connection. USB port of STM32F407VG is same. How do you expect each of these connectors to be used?	
6	About the CAN	What kind of information is exchanged by CAN communication?	