MUFFAKHAM JAH COLLEGE OF ENGINEERING AND TECHNOLOGY

BANJARA HILLS, ROAD NO.3

ELECTRICAL AND ELECTRONICS ENGINEERING DEPARTMENT

Subject Name: Analog Electronics Subject code: PC223EC

CLASS TEST-1 EEE

(2022-23)

NOTE:

1. ANSWER ALL QUESTIONS FROM PART-A

2. ANSWER ANY TWO QUESTIONS FROM PART-B

3. ANY MISSED DATA CAN ASSUMED APPROPRIATELY

PART A(3x2M=6M)

1. Draw the Forward and Reverse Bias PN Junction diode and its V-I Characteristics.
2. Classify the types of BJT configurations with their diagrams.
3. Write brief notes on the following.
    a. Rectifier
    b. Clamping circuits

PART B (2x7M=14M)

4. Explain various transistor biasing techniques with their neat circuit diagrams.
5. Explain the Bipolar Junction Transistor V-I characteristics for its various configurations and write the current gains of each configuration.
6. Explain Full-wave rectifier circuit operation with its neat circuit diagram. And derive all relevant parameters associated with the circuit.

Course Instructor: MD ZAKIR HUSSAIN, ASST.PROF. ECED