Subject code: PC234EC Semester: 2nd

Subject name: Computer Organisation & Architecture ACY: 2020-2021

CLASS TEST-2

1. Answer all questions from part-A
2. Answer ANY 2 questions from part-B

PART-A

1. Explain general register organization. [CO3]
2. Explain the differences between Asynchronous serial transfer and Strobe control in asynchronous data transfer. [CO4]
3. Brief about cache memory. [CO5]

PART-B

1. Explain with an example vector Processing, Parallel Processing, [CO3]
2. Compare the functionalities of I/O Bus and Memory Bus, discussing how they serve distinct purposes in a computer architecture. [CO4]
3. With an example mapping functions in cache memory. [CO5]