Subject code: PC404EC Semester: 2nd

Subject name: Computer Organisation & Architecture ACY: 2021-2022

CLASS TEST-2

1. Answer all questions from part-A
2. Answer all questions from part-B

PART-A

1. Describe the role of DMA controller in Direct Memory Access and its impact on data transfer.
2. Explain the concept of Input Output Processor and discuss its communication with the CPU through I/O channels.
3. Discuss the advantages and disadvantages of parallel processing in computer systems.

PART-B

1. Discuss the various modes of data transfer, including Programmed I/O, Interrupt-driven I/O, and Priority interrupt modes, and elaborate on their applications.
2. Explore the concept of Daisy chaining in the context of parallel priority interrupt, highlighting its advantages and potential challenges.
3. Provide an in-depth overview of Direct Memory Access (DMA) and explain the role of DMA controllers in facilitating efficient data transfer.