

- MNEMONIC

OP-CODE . OPERAND

Diagram illustrating a memory layout with two rows and two columns. The top row is labeled "Data" and the bottom row is labeled "Nothing". The left column is labeled "Address" and the right column is labeled "Data".

1. All the instructions, which are used to bring in (Load) data from main memory or to take data back (write) to main memory.

50	8	
51	3	
52	54	
53	9	Defect Bus
54	6	
55	2	

→ 90 | LDD 53 |

Clock	PC	MAR	MDR	CIR	ACC.	IX
1	90	90	LDD 53	LDD 53		
2	91	53	9	9		

LDI <address>

Indirect This add. holds the add. of data.

50	8							
51	3							
52	54							
53	9							
54	6							
55	2							

90	LDI 52							
91								
92								
⋮								

Cycle#	PC	MAR	MDR	CIR	ACC.	IX
1	90	90	LDI 52	LDI 52		
2	91	52	54			
3		54	6			

LDM #n

Load Immediately Constant

50	8	
51	3	
52	54	
53	9	
54	6	
55	2	

90	LDM#8
91	
92	
⋮	

Cycle#	PC	MAR	MDR	CIR	ACC	IX
1	90	90	LDM#8	LDM#8	8	