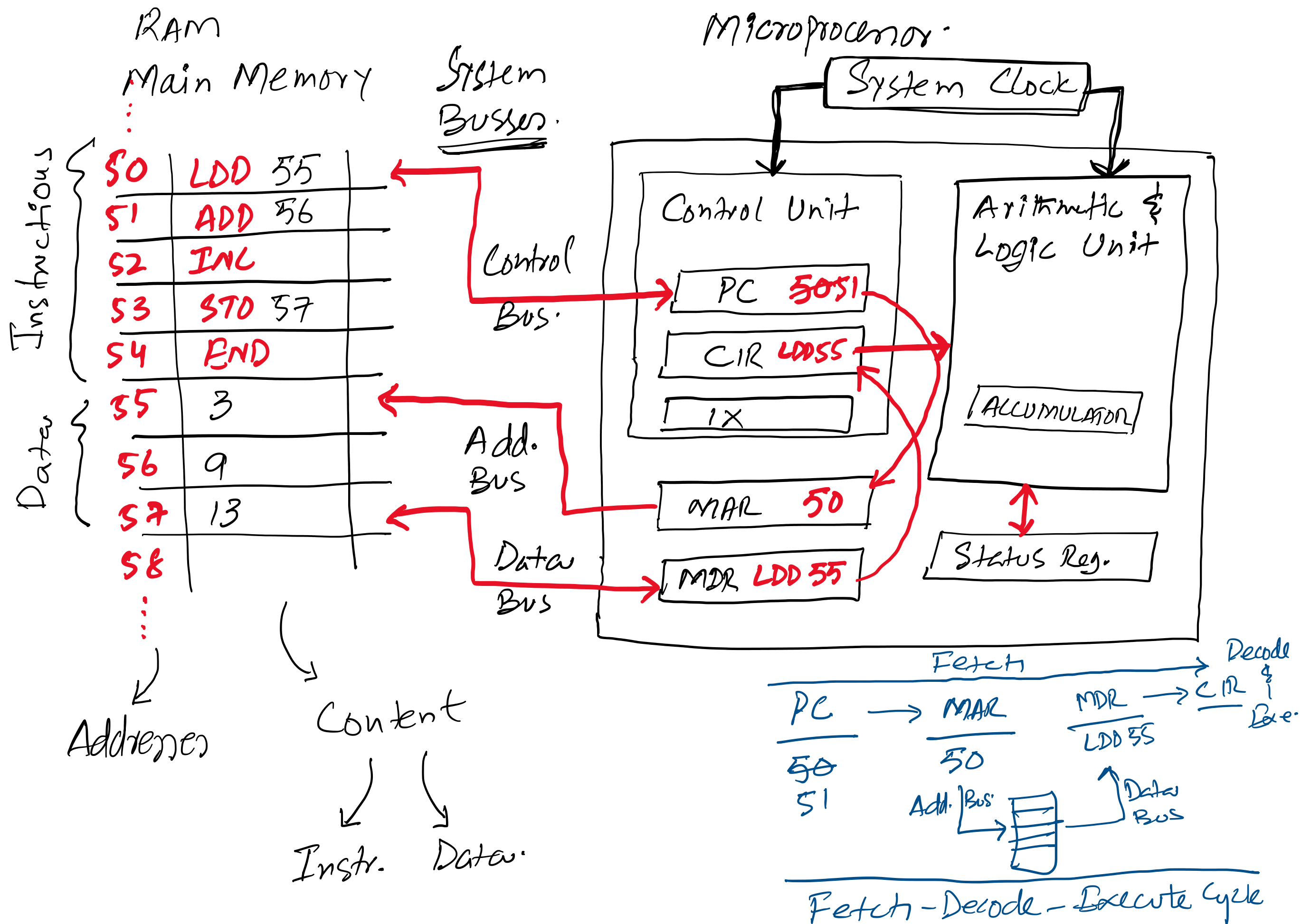


Processor Fundamentals:

Program Counter (PC): It holds the address of next inst.

Memory Address Register (MAR): It holds the address of current inst.

Memory Data Register (MDR): It holds the inst. whose add. is saved in MAR.

Current Inst. Register (CIR): It decodes, and executes the current inst.