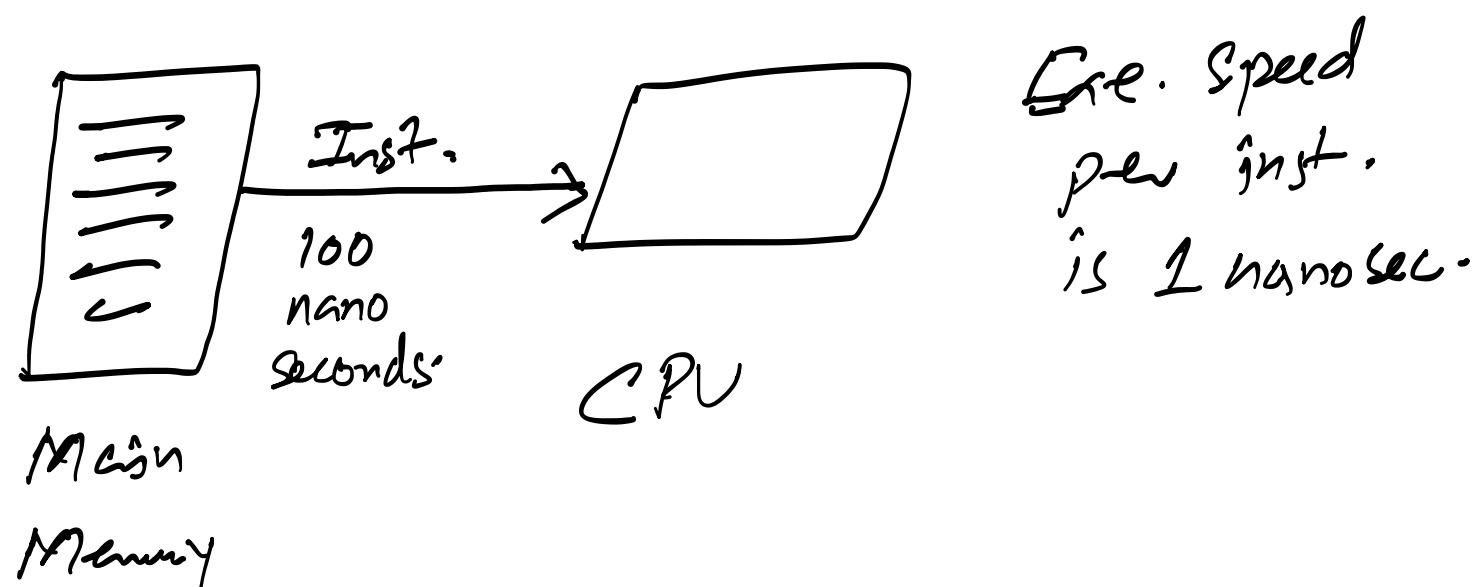


Von-Neumann Bottleneck.

Cache:



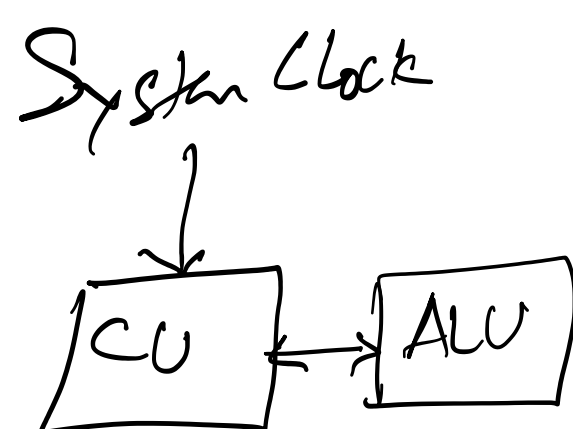
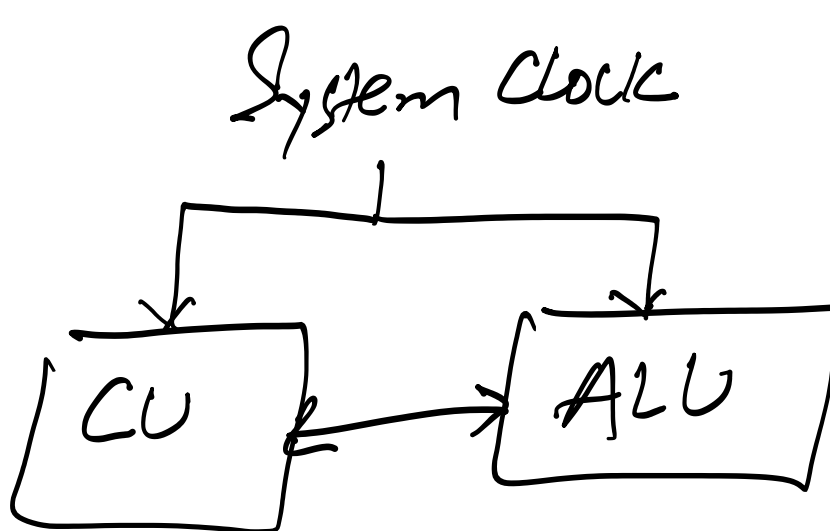
- To resolve this issue computer cache memory is installed in CPU.

99% times is sitting idle; as it is waiting for the instruction.

- Cache is a buffer of inst.

Clock Speed: It is responsible for the FDEC execution speed.

Overclocking???



Bus Width:

Data Bus

Bring in or Read
1000 bits
into MP.

Data Bus:
64 bits | 32 bit
16 turns | 32 turns.

Address Bus:

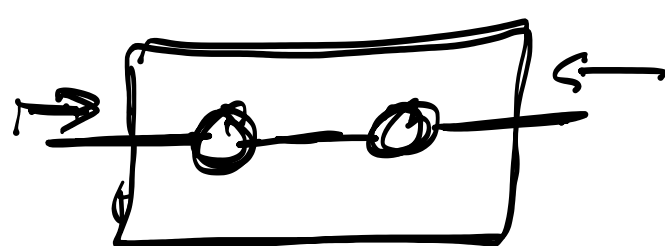
$2^8 = 256$ 0 255

Add. Bus width \propto Mem. Amount.

$2^{16} = 65k$
 $2^{32} = 4GB$

Processor Type & Cores:

Dual Core



Quad Core

