Data Integrity Thursday, 18 February 2021 7:02 PM Keykyms: - parity byte - Data Integrity - Validation - ARQs - Verification - Acknowledgement - Check Digit - Timeout. - Modulo-11 - Checksum - Parity Check - parity bit - Rrenlodd Parity Validations: - existence -format - Type - Consistency -length -Range - Un's queren . - Presente - Dimit Verification: Double Endry Manual Data Yisual Check Verification Parity Receiver ARQ Text uses ASCII. 0-127 - Time set la reply. 2'= 128 - Timeout occurs when Second computer does not acknowledge during set time. - 9f timeout occurs then first machine veserds the data. かけ -91 acknowledgement aggings: 2) dates received and it correct; send rove 2) dater is received ad B=66= it is corrupted; send C=67= again BLOCK parity O -> 1 gain. 1->0 drop. Lingle byk paristy trad Weaknesses? 1. 17 two bits are swapped-2. It two bits are gained. 3. IF two bits are drapped. Checksum; 01000001 101000010 B = 66 01000011 C 267 11000110 198 <=255 = 2⁸ 2=90 7=89 X=267 1=1.043 X = 88 Z=7*256=256 Sum '15 Within diff(x-2) = 267-256 28 range Mir 17-11 the checksum. IF sum is >255 00 28=256 |Len: 1. We divide som by 256/X-2. Round down the ans to the reasonst whole himble. 3. Then we multiply by 256 4. Subtract that from X 5. Result is checkson.