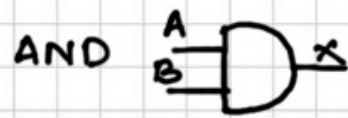
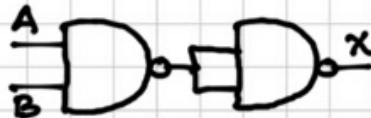
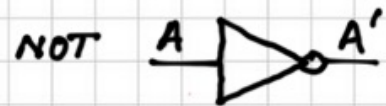
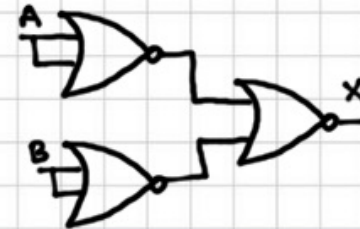


**BUILDING  
BLOCKS.****NAND LOGIC****NOR LOGIC**

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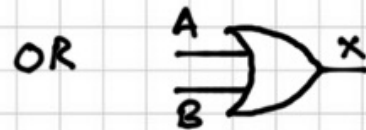
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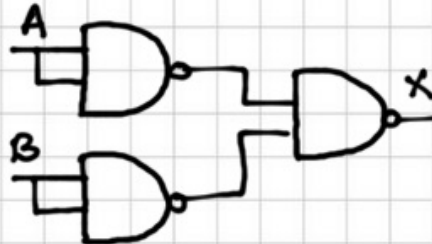
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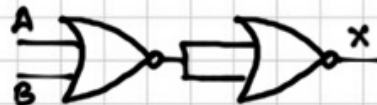
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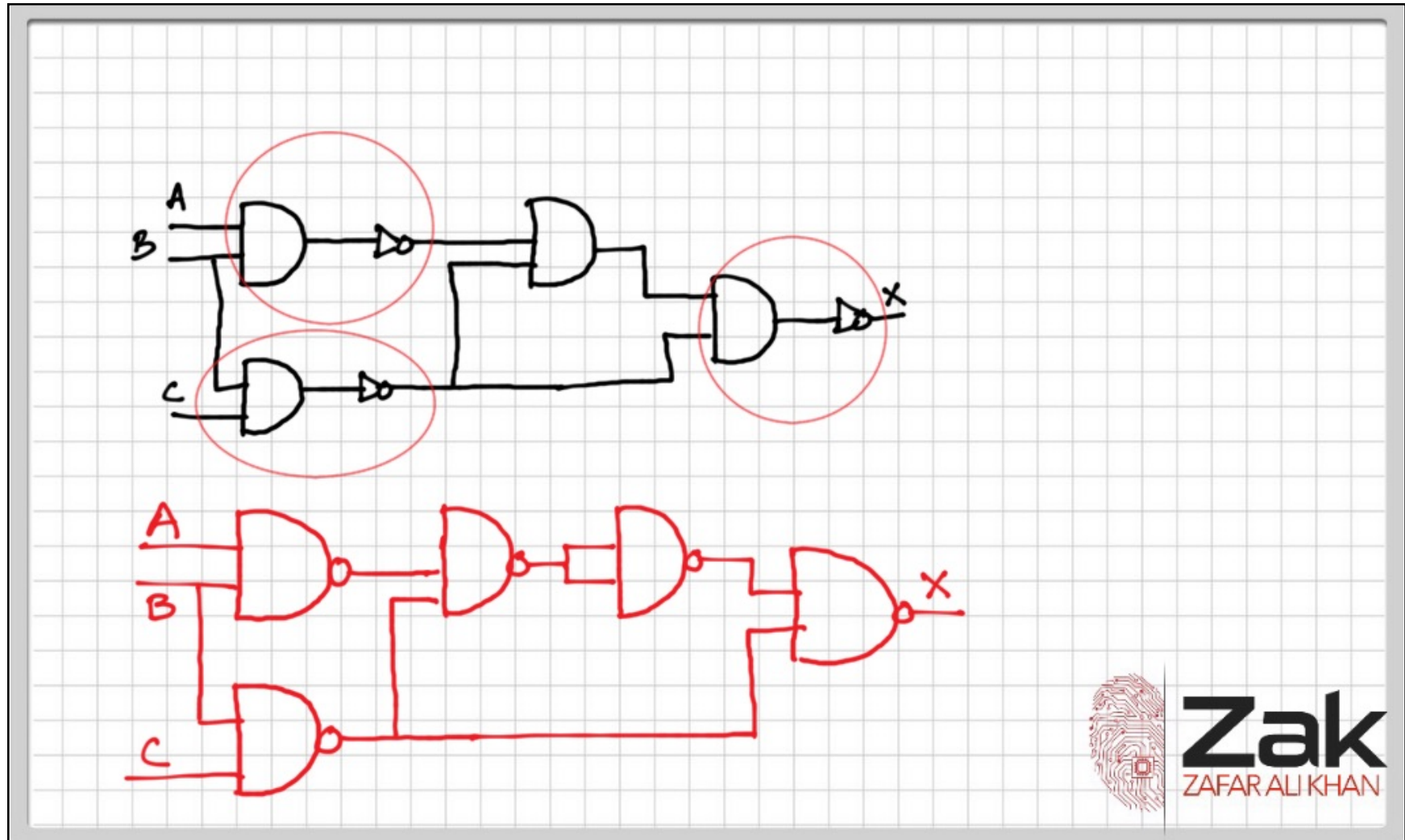
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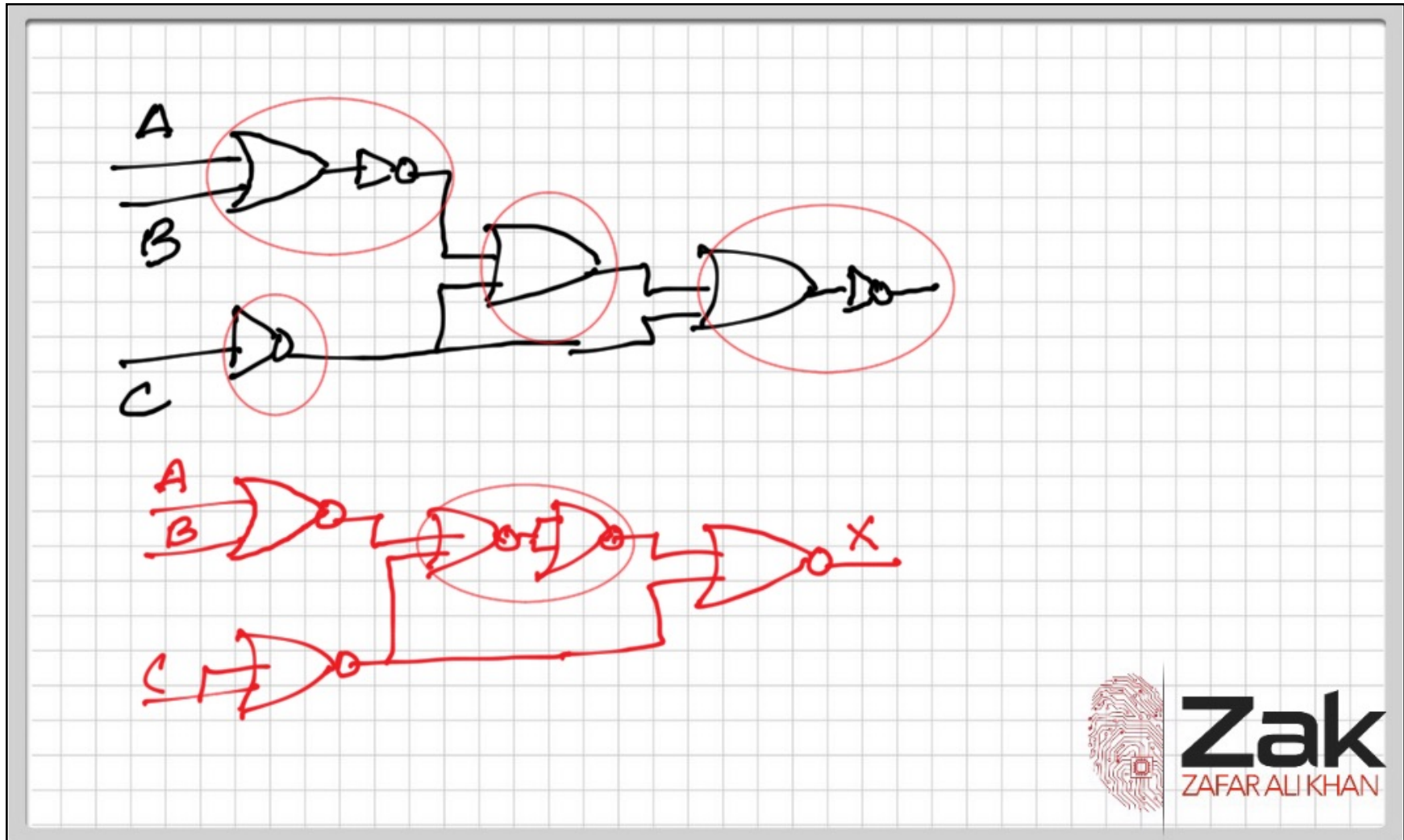


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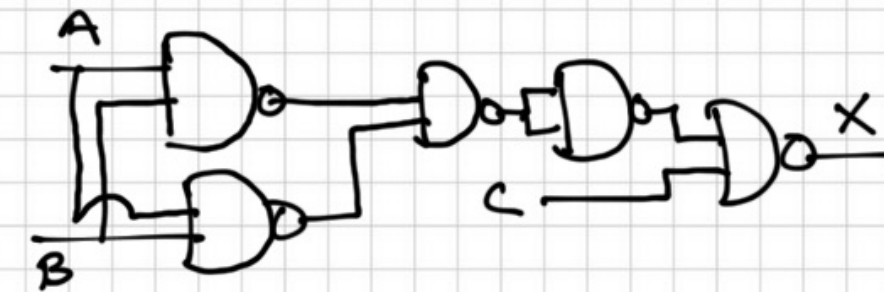
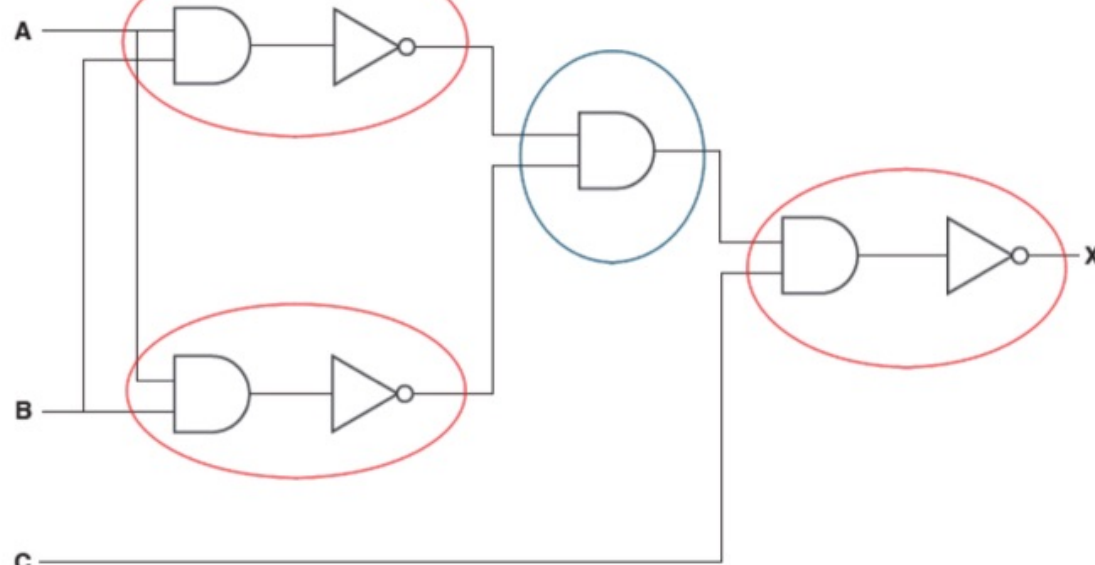


**Zak**  
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(c) Re-draw the following logic circuit using NAND gates only.



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