- * Flip flop circuits are used to hold dater In computeil memory
- 7 There are sequential concuits. Means their betaviour depends over their output.
- * Confusers' cache memory (RAM inside microprocessor) is also made up of LUP Hops.
- · Flip flops vsc either NOR or NAMD gates.

- Single Atipflop represents one bit c7 storage

- Only one input (S,R)
out of two can be
changed at a time.

occurs, both Q and Q' becomes same. Klywoods:

Set: Saving 1 Reset: Saving 0 Latch: Hold a bit Race: An error occurred

NAND LOGIC:

SR Fup Flop

TRUTH TABLE

	S	R	Q	$\overline{\Diamond}$	
One	0	ţ	•	\bigcirc	Set
Change	1	1	1	0	Catch
In Input	?	\Diamond	\(\(\)	1	Reset
aly.	1	1		1	Lateh
l	0	0	4	1	Race/from