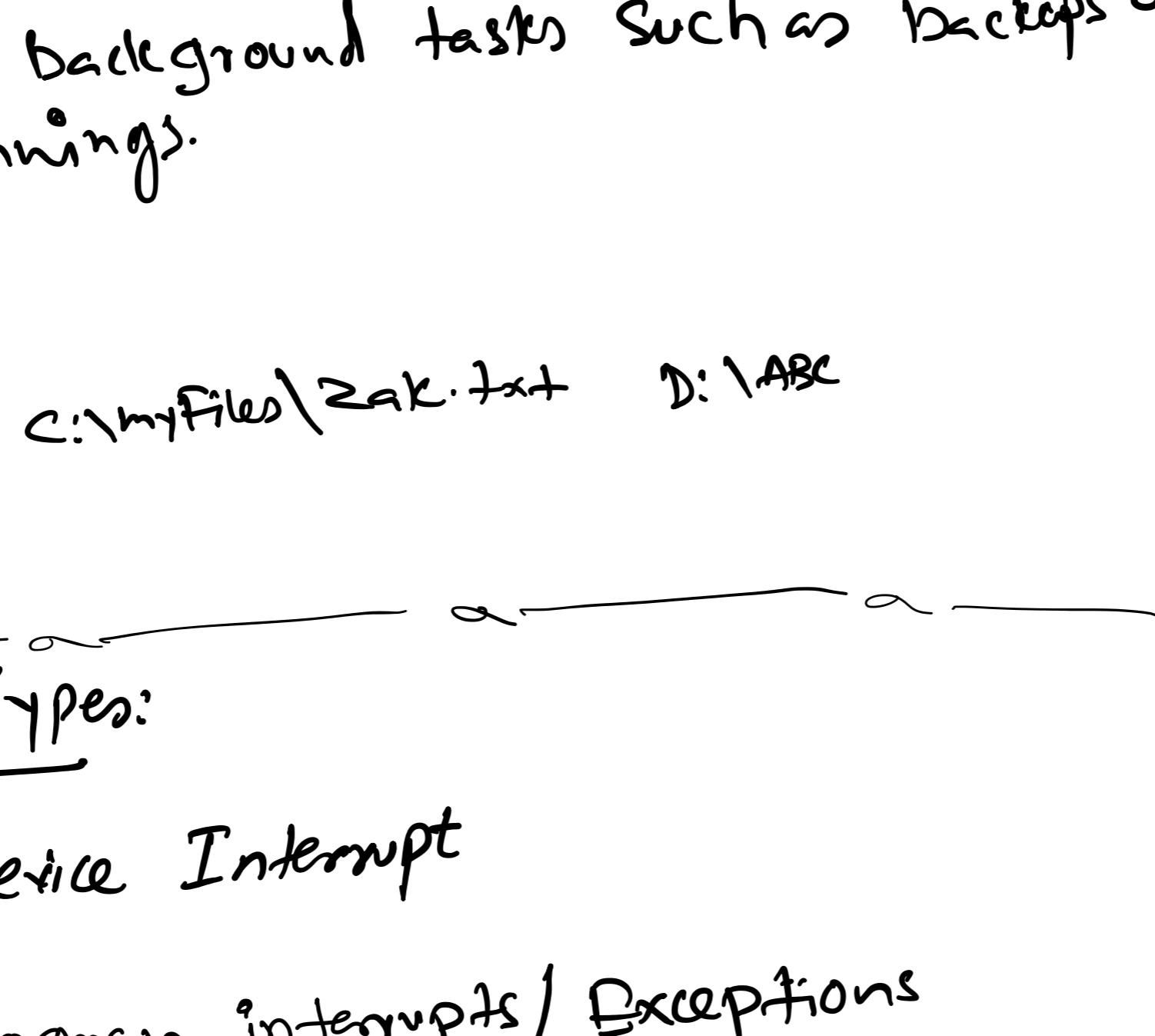


The kernel



Hiding complexities of H/W from the user:

- Using GUI instead of CLI
- Using device drivers.
- Simplifying access to mem. and storages.
- Automating background tasks such as Backups and virus scanning.

DOS S:
COPY C:\myfiles\ZAK.txt D:\ABC

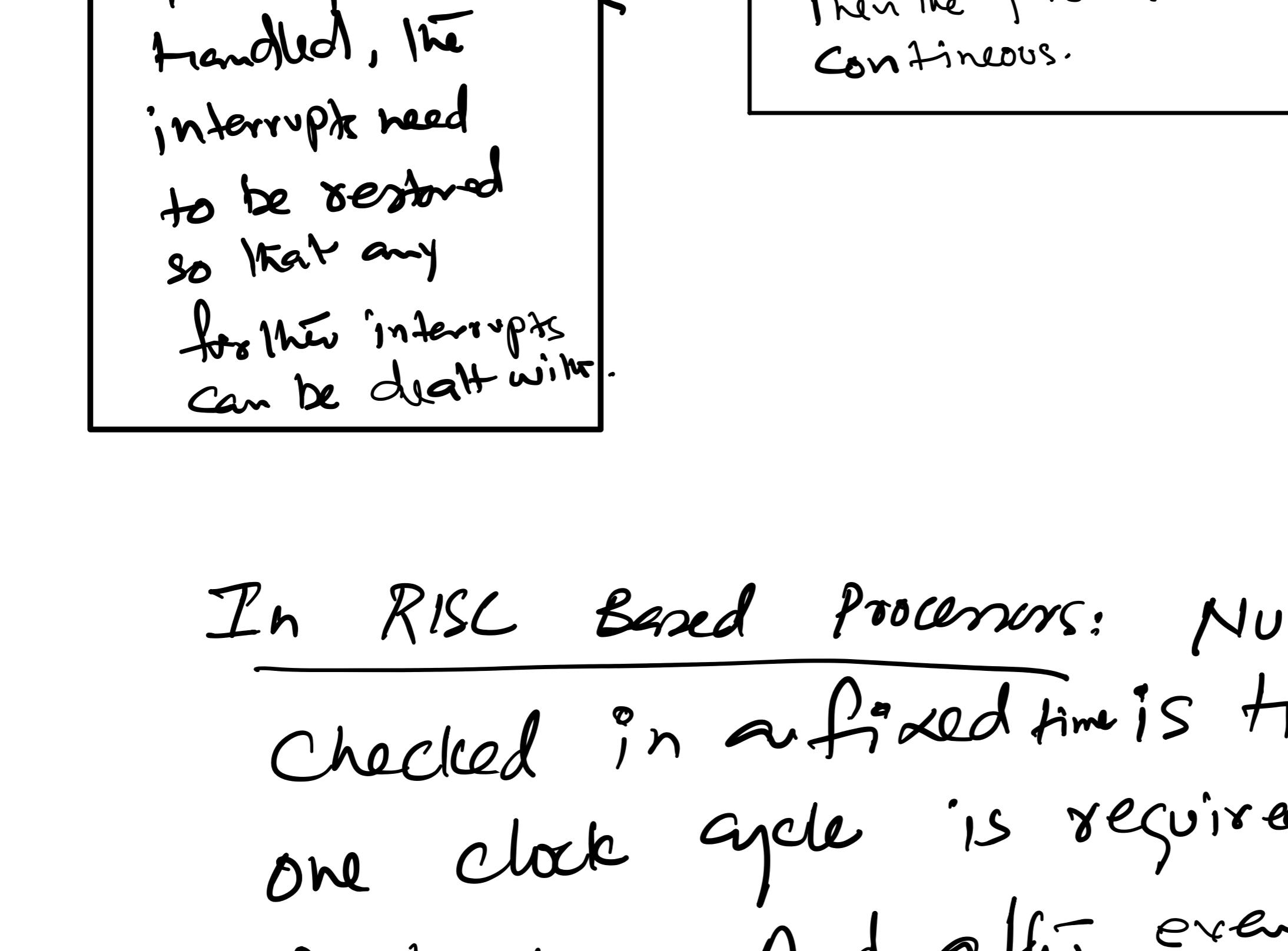
Interrupt Types:

- Device Interrupt
- program interrupts / Exceptions
- faults.

- When an interrupt is received the kernel consults the IDT (Interrupt dispatch table).

- Kernel will save the state of the current program on kernel stack.

- Interrupts when raised are prioritised. This priority is maintained according to levels called IPL (Interrupt priority levels). (0 - 31)



In RISC Based Processors: Number of interrupts checked in a fixed time is higher as only one clock cycle is required to execute one instruction. And after every inst. execution interrupts are checked.

In CISC Based Processors: Number of interrupts checked in a fixed time is lower than RISC as many clock cycles are required to execute one instruction. And after every inst. execution interrupt are checked and dealt with.