Candidate name

Mark for Review

Cambridge International AS & A Level

Candidate name		
Centre number	Candidate number	

COMPUTER SCIENCE 9608/23

Paper 2 Written Paper

October/November 2023

2 hours

You must answer on the question paper.

No additional materials are needed.

INSTRUCTIONS

- Answer all questions.
- Use black or dark blue pen.
- Write your name, centre number and candidate number in the boxes above.
- Write your answers in the space provided in this question paper.
- Do not use staples, paper clips, glue or correction fluid.
- DO NOT WRITE IN THIS MARGIN.

INFORMATION

- The total mark for each question is given in brackets [ ].
- Calculators may be used.
- You may use an HB pencil for any diagrams or graphs.

This document consists of 28 printed pages.

© UCLES 2023 [Turn over]

1 (a) Tick (✓) the statements that are true about a computer.

- The Arithmetic Logic Unit (ALU) is part of the Control Unit (CU).
- The Control Unit (CU) controls the operation of the other components in the CPU.
- The Control Unit (CU) stores the results of calculations.
- The Control Unit (CU) stores the address of the next instruction to be fetched.

[1]

(b) Tick (✓) the statements that are true about a RISC processor.

- The instruction set contains a large number of instructions.
- The instruction set contains a small number of instructions.
- The instruction set contains a large number of addressing modes.
- The instruction set contains a small number of addressing modes.

[1]

(c) The operation of the instruction set for a CISC processor is controlled by a microprogram.

Explain why this is not necessary for a RISC processor.

[2]

(d) Tick (✓) the statements that are true about pipelining.

- Pipelining allows more than one instruction to be executed at the same time.
- Pipelining allows more than one instruction to be fetched at the same time.
- Pipelining allows more than one instruction to be decoded at the same time.
- Pipelining allows more than one instruction to be stored at the same time.

[1]

(e) Tick (✓) the statements that are true about a Harvard architecture.

- The processor will have a large number of registers.
- The processor will have a large cache memory.
- The processor will have separate data and instruction buses.
- The processor will have separate data and instruction caches.

[1]

(f) Tick (✓) the statements that are true about a multicore processor.

- Each core has its own cache.
- Each core has its own ALU.
- Each core has its own Control Unit.
- Each core has its own Fetch-Execute cycle.

[1]

(g) Tick (✓) the statements that are true about a GPU (Graphics Processing Unit).

- A GPU is a processor that is optimised for floating-point calculations.
- A GPU is a processor that is optimised for integer calculations.
- A GPU is a processor that is optimised for Boolean calculations.
- A GPU is a processor that is optimised for string manipulation.

[1]

(h) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC is a processor that is optimised for floating-point calculations.
- A SoC is a processor that is optimised for integer calculations.
- A SoC is a processor that is optimised for Boolean calculations.
- A SoC is a processor that is optimised for string manipulation.

[1]

(i) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(j) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(k) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(l) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(m) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(n) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(o) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(p) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(q) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(r) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(s) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(t) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(u) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(v) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(w) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(x) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(y) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(z) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(aa) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ab) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ac) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ad) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ae) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(af) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ag) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ah) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ai) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(aj) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ak) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(al) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(am) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(an) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ao) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ap) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(aq) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ar) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(as) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(at) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(au) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(av) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(aw) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ax) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ay) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(az) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ba) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bb) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bc) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bd) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(be) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bf) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bg) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bh) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bi) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bj) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bk) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bl) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bm) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bn) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bo) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bp) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bq) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(br) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bs) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bt) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bu) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bv) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bw) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bx) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(by) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(bz) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ca) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cb) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cc) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cd) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ce) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cf) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cg) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ch) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ci) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cj) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ck) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cl) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cm) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cn) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(co) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cp) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cq) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cr) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cs) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ct) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cu) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cv) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cw) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cx) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cy) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(cz) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(da) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(db) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dc) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dd) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(de) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(df) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dg) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dh) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(di) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dj) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dk) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dl) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dm) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dn) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(do) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dp) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dq) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dr) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ds) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dt) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(du) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dv) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dw) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dx) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dy) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(dz) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ea) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(eb) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ec) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ed) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ee) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ef) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(eg) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(eh) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ei) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ej) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ek) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(el) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(em) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(en) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(eo) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ep) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(eq) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(er) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(es) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(et) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(eu) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ev) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ew) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ex) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ey) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ez) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fa) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fb) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fc) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fd) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fe) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ff) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fg) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fh) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fi) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fj) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fk) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fl) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fm) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fn) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fo) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fp) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fq) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fr) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fs) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(ft) Tick (✓) the statements that are true about a SoC (System on a Chip).

- A SoC contains a GPU.
- A SoC contains a CPU.
- A SoC contains a cache.
- A SoC contains a Control Unit.

[1]

(fu) Tick (✓) the statements that are true about a SoC (System on a Chip).