

Progress

- A comparison between Modelica and Simulink models. Four different test cases regarding ENTSE_131127_Controller_Test_Report. The tests are summarised only with the relevant results.
- A VSC model is implemented and tested. The issues encountered with Dynawo are summarized.

Next steps

- Solve the VSC issue.
- Test case 1: SM terminal disconnection initialization. Must connect somewhere as it is an external variable.
- Test case 2: is there the possibility to close a switch?
- Test case 3: execute the simulation with required fault resistance value.
- By now, only a *dyd* file is used to define the systems. Support to build systems with more than one synchronous machine, including the *iidm* file (implement the C++ network model).
- Preassemble the synchronous machine models with our controls to execute faster simulations. Same with the VSC.
- Next test cases:
 - i) VSC + synchronous machine.
 - ii) Grids with more than one synchronous machine.