

High Speed Optocoupler, 1 MBd, Photodiode with Transistor **Output**

Features

• Isolation Test Voltage: 5300 V_{RMS}

TTL Compatible

• High Bit Rates: 1.0 Mbit/s

• High Common-Mode Interference Immunity

• Bandwidth 2.0 MHz

Open-Collector Output

· External Base Wiring Possible

Agency Approvals

• UL - File No. E52744 System Code H or J

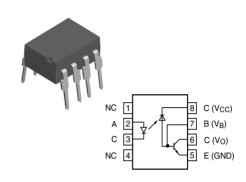
• DIN EN 60747-5-2(VDE0884) DIN EN 60747-5-5 pending Available with Option 1

CSA 93751

Description

The 6N135 and 6N136 are optocouplers with a GaA-IAs infrared emitting diode, optically coupled with an integrated photo detector which consists of a photo diode and a high-speed transistor in a DIP-8 plastic package.

Signals can be transmitted between two electrically separated circuits up to frequencies of 2.0 MHz. The potential difference between the circuits to be coupled should not exceed the maximum permissible reference voltages



Order Information

Part	Remarks
6N135	CTR ≥ 7 %, DIP-8
6N136	CTR ≥ 19 %, DIP-8
6N135-X007	CTR ≥ 7 %, SMD-8 (option 7)
6N136-X006	CTR ≥ 19 %, DIP-8 400 mil (option 6)
6N136-X007	CTR ≥ 19 %, SMD-8 (option 7)
6N136-X009	CTR ≥ 19 %, SMD-8 (option 9)

For additional information on the available options refer to Option Information.

Absolute Maximum Ratings

T_{amb} = 25 °C, unless otherwise specified

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Rating for extended periods of the time can adversely affect reliability.

Input

Parameter	Test condition	Symbol	Value	Unit
Reverse voltages		V_{R}	5.0	V
Forward current		l _F	25	mA
Peak forward current	t = 1.0 ms, duty cycle 50 %	I _{FSM}	50	mA
Maximum surge forward current	t ≤ 1.0 μs, 300 pulses/s		1.0	Α
Thermal resistance		R _{th}	700	K/W
Power dissipation	T _{amb} = 70 °C	P _{diss}	45	mW

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Output

Parameter	Test condition	Symbol	Value	Unit
Supply voltage		V _S	- 0.5 to 15	V
Output voltage		V _O	- 0.5 to 15	V
Emitter-base voltage		V _{EBO}	5.0	V
Output current		Io	8.0	mA
Maximum output current			16	mA
Base current		I _B	5.0	mA
Thermal resistance			300	K/W
Power dissipation	T _{amb} = 70 °C	P _{diss}	100	mW

Coupler

Parameter	Test condition	Symbol	Value	Unit
Isolation test voltage (between emitter and detector climate per DIN 50014 part 2, NOV 74	t = 1.0 s	V _{ISO}	5300	V _{RMS}
Pollution degree (DIN VDE 0109)			2.0	
Creepage			≥ 7.0	mm
Clearance			≥ 7.0	mm
Comparative tracking index per DIN IEC112/VDE 0303 part 1, group IIIa per DIN VDE 6110			175	
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Storage temperature range		T _{stg}	- 55 to + 125	°C
Ambient temperature range		T _{amb}	- 55 to + 100	°C
Soldering temperature	max. ≤ 10 s, dip soldering ≥ 0.5 mm from case bottom	T _{sld}	260	°C

Electrical Characteristics

 T_{amb} = 25 °C, unless otherwise specified Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

Input

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Forward voltage	I _F = 16 mA	V _F		1.6	1.9	V
Breakdown voltage	I _R = 10 μA	V_{BR}	5.0			V
Reverse current	V _R = 5.0 V	I _R		0.5	10	μΑ
Capacitance	$V_R = 0 \text{ V, } f = 1.0 \text{ MHz}$	Co		125		pF
Temperature coefficient, forward voltage	I _F = 16 mA	$\Delta V_F / \Delta T_A$		-1.7		mV/°C



Output

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Logic low supply current	$I_F = 16 \text{ mA}, V_O \text{ open}, V_{CC} = 15 \text{ V}$		I _{CCL}		150		μΑ
Supply current, logic high	$I_F = 0 \text{ mA}, V_O \text{ open}, V_{CC} = 15 \text{ V}$		I _{CCH}		0.01	1	μΑ
Output voltage, output low	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V},$ $I_O = 1.1 \text{ mA}$	6N135	V _{OL}		0.1	0.4	V
	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V},$ $I_O = 2.4 \text{ mA}$	6N136	V _{OL}		0.1	0.4	V
Output current, output high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		I _{OH}		3.0	500	nA
	$I_F = 0 \text{ mA}, V_O = V_{CC} = 15 \text{ V}$		I _{OH}		0.01	1	μΑ

Coupler

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Capacitance (input-output)	f = 1.0 MHz	C _{IO}		0.6		pF

Current Transfer Ratio

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Current Transfer Ratio	$I_F = 16 \text{ mA}, V_O = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	6N135	CTR	7	16		%
		6N136	CTR	19	35		%
	$I_F = 16 \text{ mA}, V_O = 0.5 \text{ V}, V_{CC} = 4.5 \text{ V}$	6N135	CTR	5			%
		6N136	CTR	15			%

Switching Characteristics

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
High-low	$I_F = 16 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 4.1 \text{ k}\Omega$	6N135	t _{PHL}		0.3	1.5	μs
	$I_F = 16 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1.9 \text{ k}\Omega$	6N136	t _{PHL}		0.2	0.8	μs
Low-high	I_F = 16 mA, V_{CC} = 5.0 V, R_L = 4.1 k Ω	6N135	t _{PLH}		0.3	1.5	μs
	$I_F = 16 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1.9 \text{ k}\Omega$	6N136	t _{PLH}		0.2	0.8	μs

Common Mode Transient Immunity

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
High	$I_F = 0 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5.0 \text{ V}, R_L = 4.1 \text{ k}\Omega$	6N135	I CM _H I		1000		V/μs
	$I_F = 0 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5.0 \text{ V}, R_L = 1.9 \text{ k}\Omega$	6N136	I CM _H I		1000		V/μs
Low	$I_F = 16 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5.0 \text{ V}, R_L = 4.1 \text{ k}\Omega$	6N135	ICMLI		1000		V/μs
	$I_F = 16 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5.0 \text{ V}, R_L = 1.9 \text{ k}\Omega$	6N136	I CM _L I		1000		V/μs

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Typical Characteristics ($T_{amb} = 25$ °C unless otherwise specified)

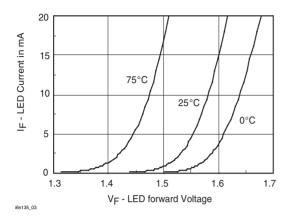


Fig. 1 LED Forward Current vs.Forward Voltage

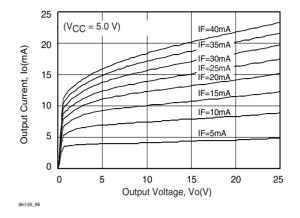


Fig. 4 Output Current vs. Output Voltage

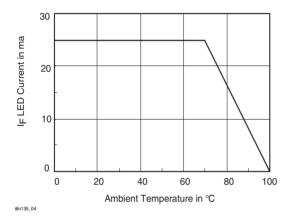


Fig. 2 Permissible Forward LED Current vs. Temperature

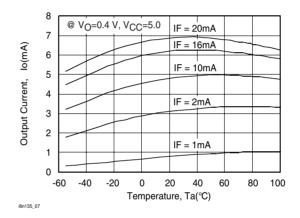


Fig. 5 Output Current vs. Temperature

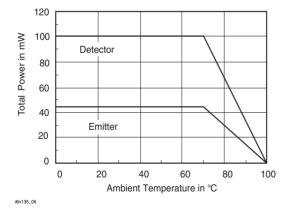


Fig. 3 Permissible Power Dissipation vs. Temperature

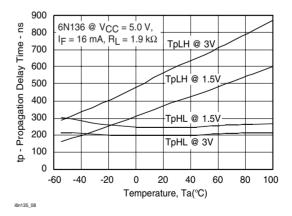


Fig. 6 Propagation Delay vs. Temperature



 $(V_{CC} = 5.0 \text{ V}, R_L = 100 \Omega)$

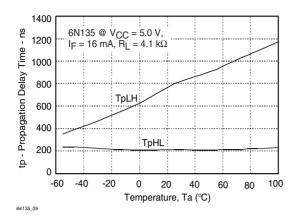
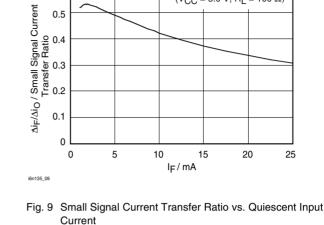


Fig. 7 Propagation Delay vs. Temperature



0.6

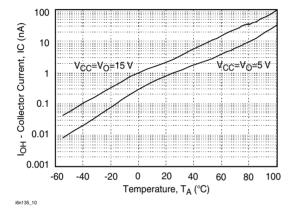


Fig. 8 Logic High Output Current vs.Temperature

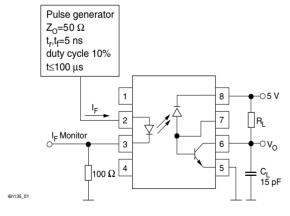
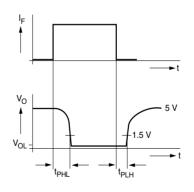
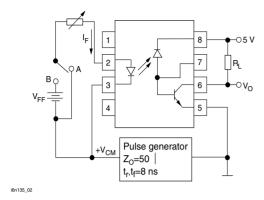


Fig. 10 Switching Times







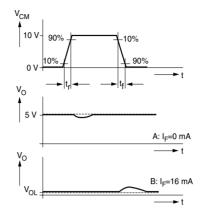
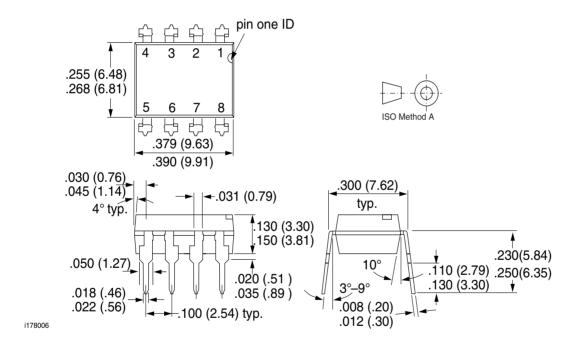


Fig. 11 Common-Mode Interference Immunity

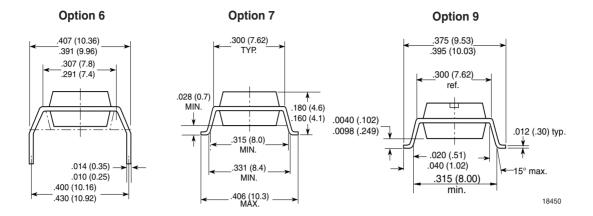
Package Dimensions in Inches (mm)



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Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423

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Document Number 83604

Rev. 1.3, 26-Apr-04

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