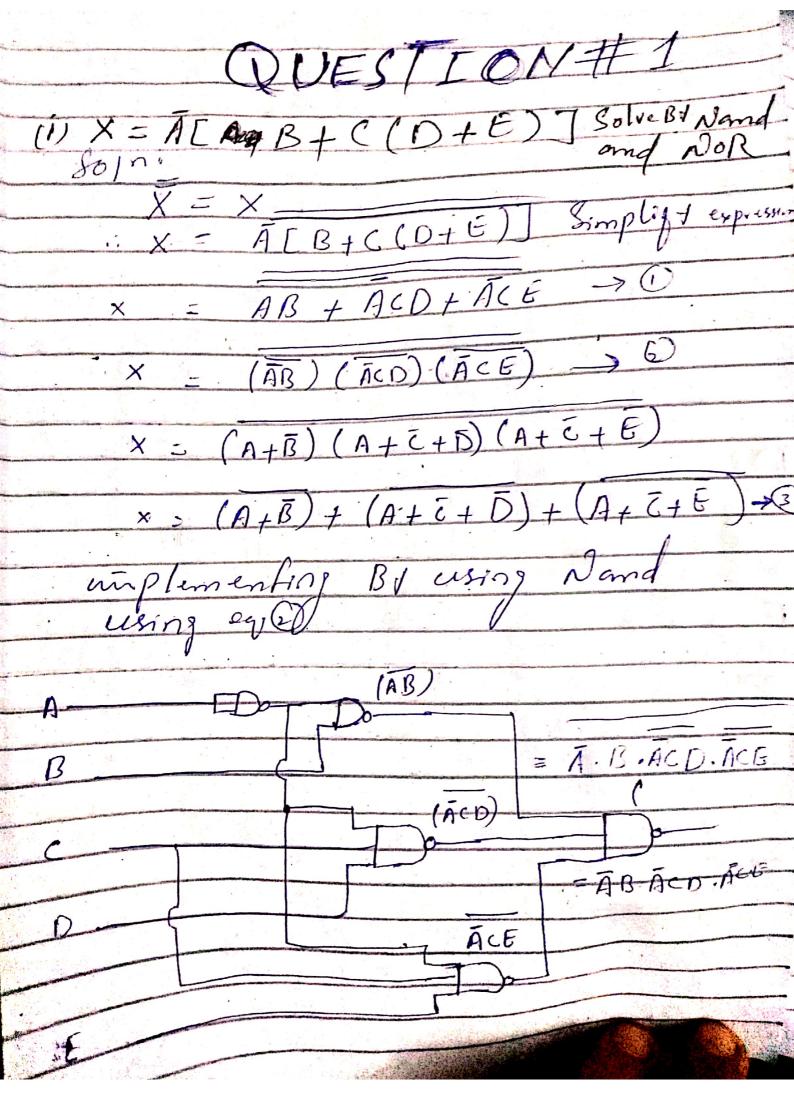
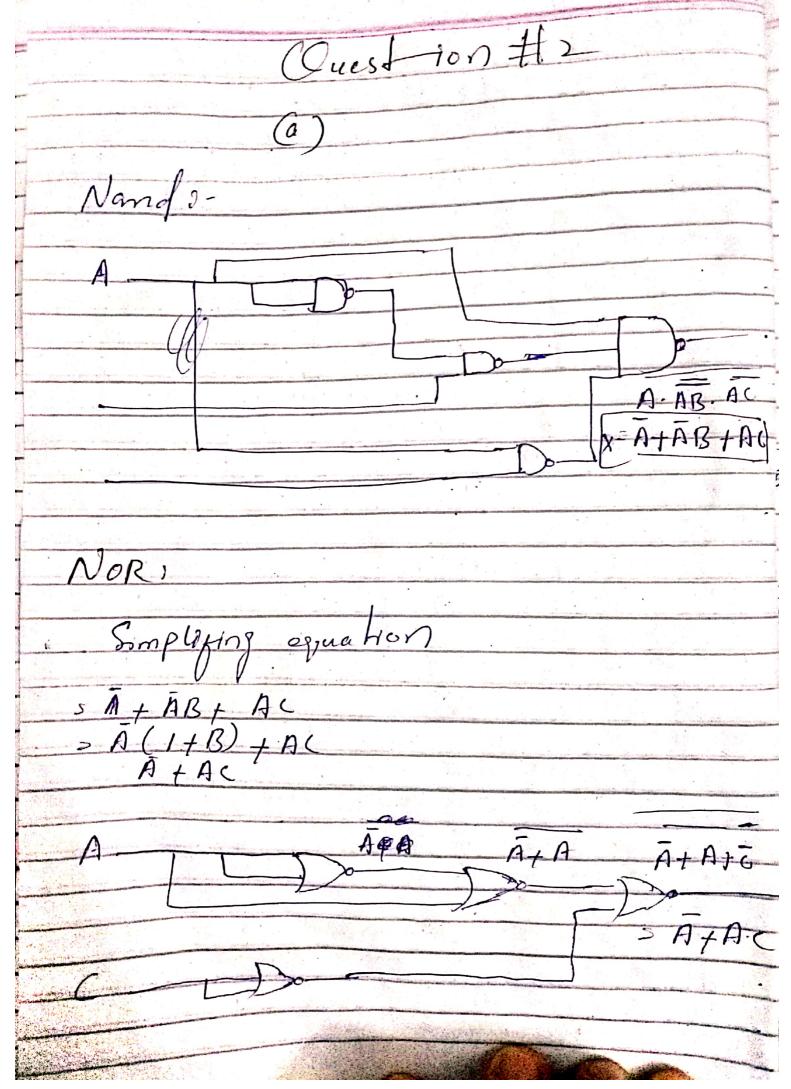
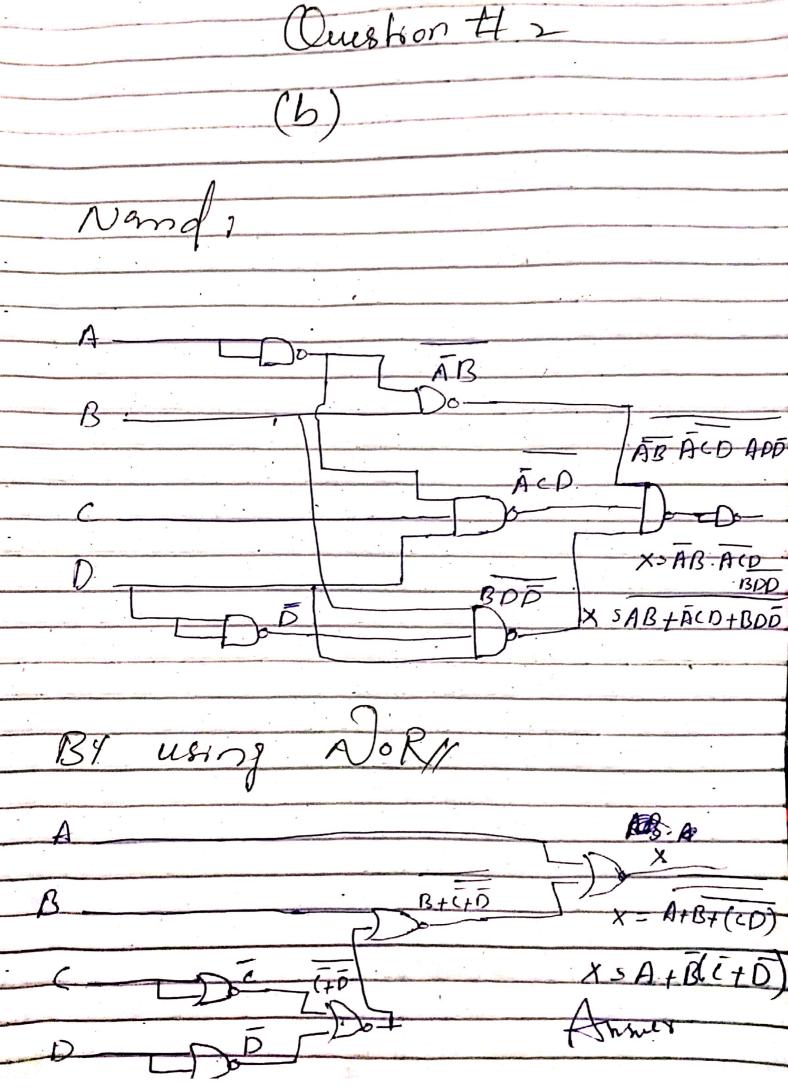
Name é MuzaMIL Coursé à DLD Assignment Nov 5



using NOR. x= A+B+c+D+6 Ex A[B+c(D+E) C+(D+6) D+5

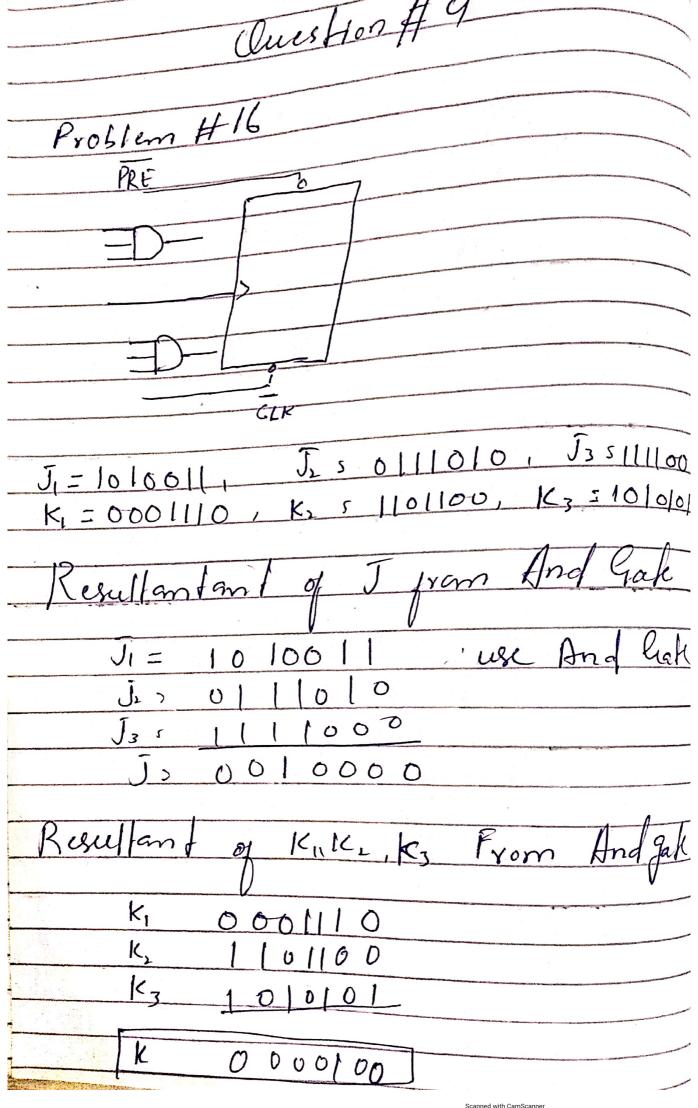




Question # 3

03# How J.K Flipflop have Solved invalid Condition?

Answerd- J.K Flip Flop is with addition of Clock input Circuity when Bath J emal K are I f the invalid Condition takes place. Thus to prevent invalid for Condition Clock is introduced.



Applying tyuth table
(Positive triggered e

