



#### Software

- NASM Assembler (<a href="http://www.nasm.us/">http://www.nasm.us/</a>)
- QEMU Emulator (<a href="https://www.qemu.org/">https://www.qemu.org/</a>)
- = No destroyed PC hardware;)
   brew install nasm qemu



## Turning on the Computer

- Press the Power Button
- BIOS Hardware Test



- Loads the first 512 bytes off the floppy disk/hard drive
- Does byte 510-512 equal 0x55AA? YES jump to 0x7C00!

#### Real Mode

- http://wiki.osdev.org/Real\_Mode
- BIOS Interrupts <a href="https://en.wikipedia.org/wiki/BIOS\_interrupt\_call">https://en.wikipedia.org/wiki/BIOS\_interrupt\_call</a>
- 16 bit instructions
- Only 1 MB of memory can be accessed (ignoring segments)

Register	Accumulator			Counter			Data			Base			Stack F	ointer	Stack Base Pointer		Source		Destination				
64-bit	RAX			RCX				RDX			RBX			RSP		RBP		RSI		RDI			
32-bit		EAX		ECX				EDX		EBX			ESP		EBP		ESI		EDI				
16-bit		A	Х			СХ	<b>K</b>			D	K			B)	<		SP		BP		SI		DI
8-bit		АН	AL		- 1	СН	CL			DH	DL			вн	BL								

```
output 16 bit instructions
    bits 16
    org 0x7c00
 3
 4
    boot:
 5
         mov si, hello
                           ah=0x0e int 0x10 means
 6
                           'Write Character in TTY mode'
         mov ah, 0x0e
    .loop:
 8
                      loads byte at address `ds:si` into `al`.
         lodsb
 9
         or al,al
10
         jz halt
11
         int 0x10
12
         jmp .loop
13
    halt:
14
         cli
               clear interrupts
               halt cpu
15
         hlt
16
    hello: db "Hello world!",0
17
                                 pad remaining 510 bytes with 0
18
    times 510 - (\$-\$\$) db 0
19
    dw 0xaa55
                magic!
20
```

### nasm -f bin boot1.asm -o boot.bin qemu-system-i386 -fda boot.bin

```
QEMU
SeaBIOS (version rel-1.7.4-0-g96917a8-20140203_153353-nilsson.home.kraxel.org)
iPXE (http://ipxe.org) 00:03.0 C900 PCI2.10 PnP PMM+07FC6110+07F26110 C900
Booting from Hard Disk...
Boot failed: could not read the boot disk
Booting from Floppy...
Hello world!
```

#### 32 bit Mode

Enable A20 Line (<a href="http://wiki.osdev.org/A20\_Line">http://wiki.osdev.org/A20\_Line</a>)

```
mov ax, 0x2401
int 0x15
```

- Setup a Global Descriptor Table (<a href="http://wiki.osdev.org/GDT">http://wiki.osdev.org/GDT</a>)
- Set Protected Mode Bit on cr0
- Jump to 32 bit Code!

### Global Descriptor Table

- Tells the CPU what memory ranges mean
- Useful for memory protection
- Tells CPU about 64/32/16 bit modes
- Can you execute this? Can you read/write this?
- Load with special lgdt instruction!

```
gdt start:
    da 0x0
gdt_code:
   dw 0xFFFF
                code segment from 0-0xFFFF with
   dw 0x0
                read/write/execute and
   db 0x0
   db 10011010b 32 bits flags
   db 11001111b
   db 0x0
gdt_data:
   dw 0xFFFF
              data segment from 0-0xFFFF with
   dw 0x0
              read/write and 32 bits flags
   db 0x0
   db 10010010b
   db 11001111b
   db 0x0
gdt_end:
gdt_pointer:
   dw gdt_end - gdt_start pointer structure telling CPU how big the
    dd gdt_start
                          GDT is
   0
                                       16
                                                20
                     Ω
                              12
                                                                  20
```

U		8	12	16	20	24	28	32			
limit_low		base_low		base_middle	access	flags	base_high	]			
								_			
access layout											
0											
present	ring level		1	executable	direction	read/write	accessed	]			
	•			•	•		•				
flags layout											
0				4				8			
granularity	size	0	0	limit high				]			

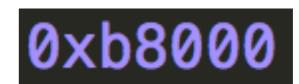
32

```
CODE_SEG equ gdt_code - gdt_start
DATA_SEG equ gdt_data - gdt_start
```

#### Protected Mode VGA

mov *ax*, 0x3 int 0x10

Text Mode 3 80x50 characters with 16 colours memory mapped to 0xb8000



0		8	16
background_color	foreground_color	ascii character	

```
bits 32
boot2:
    mov esi, hello
    mov ebx, 0xb8000
.loop:
            loads byte at address `ds:esi` into `al`.
    lodsb
    or al,al
    jz halt
                     set foreground colour to blue (1)
    or eax, 0x0100
    mov word [ebx], ax
    add ebx,2
    jmp .loop
halt:
    cli
    hlt
hello: db "Hello world!",0
```



## Beyond 512 Bytes

Disk Interrupts (https://en.wikipedia.org/wiki/INT 13H)

```
mov [disk], dl bios magic dl register value
mov ah, 0x2 ; read sectors
mov al, 6 ;sectors to read
mov ch, 0 ;cylinder idx mov dh, 0 ;head idx
mov cl, 2 ;sector idx
mov dl, [disk] ;disk idx
mov bx, copy_target; target pointer
int 0x13 ah=0x2 int 0x13 means
           'Read Sectors From Drive'
```

```
times 510 - ($-$$) db 0 our boot sector zero padding
dw 0xaa55
           bootsector magic value
copy_target:
bits 32
    hello: db "Hello more than 512 bytes world!!",0
boot2:
    mov esi, hello
    mov ebx, 0xb8000
.loop:
    lodsb
    or al,al
    jz halt
                    0x0F00 = white text (15)
    or eax, 0x0F00
    mov word [ebx], ax
    add ebx,2
    jmp .loop
```



# Getting to C++!

## Cross Compiler

- A compiler specifically targeted to your platform
- A Complete Nightmare to compile!
- Removes weird OS hacks, optimisations and function call conventions

brew tap zanders3/homebrew-gcc\_cross\_compilers
brew install i386-elf-gcc

### Call C++ from Assembly

```
bits 32
                                esp = stack pointer
    mov esp,kernel_stack_top
                                it grows down!
    extern kmain
    call kmain
    cli
    hlt
section .bss
align 4
kernel_stack_bottom: equ $
    resb 16384 ; 16 KB reserve 16KB of stack
kernel_stack_top:
```

# Link it all together!

```
ENTRY(boot)
                             output asm directly in binary
   OUTPUT_FORMAT("binary")
                             not ELF or EXE, etc.
 3
   SECTIONS {
 4
        = 0x7c00; start at 0x7c00
 5
        .text:
6
7
8
9
            *(.boot) put the boot loader first
            *(.text) all the C++ stuff after
11
        .rodata:
12
13
            *(.rodata)
14
15
16
        .data:
17
18
            *(.data)
19
20
21
        .bss :
22
23
            *(.bss)
24
```

```
nasm -f elf32 boot4.asm -o boot4.o
i386-elf-_g++ kmain.cpp boot4.o -o kernel.bin -nostdlib -ffreestanding
-mno-red-zone -fno-exceptions -fno-rtti -Wall -Wextra -Werror -T
linker.ld
qemu-system-i386 -fda kernel.bin
```





#### Resources

- http://wiki.osdev.org/Main\_Page
- http://3zanders.co.uk/2017/10/13/writing-a-bootloader/
- https://os.phil-opp.com/multiboot-kernel/
- http://www.jamesmolloy.co.uk/tutorial\_html/

