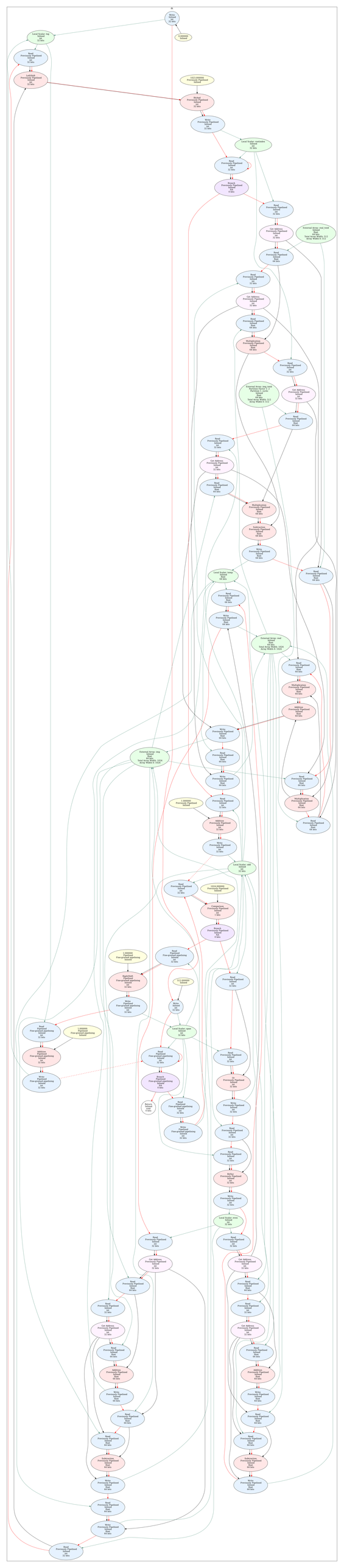
AI
Write
Vectorized
int
32 bits
0.000000
Solved
Load Scalar int
Solved
int
32 bits
Read
Previously Pipelined
Vectorized
int
32 bits
LutStash
Previously Pipelined
Vectorized
int
32 bits
1023.000000
Previously Pipelined
Solved
Ricked
Previously Pipelined
Vectorized
int
32 bits
Write
Previously Pipelined
Vectorized
int
32 bits
Load Scalar opcodes
Solved
32 bits
Read
Previously Pipelined
Vectorized
int
32 bits
Branch
Previously Pipelined
Vectorized
N/A
0 bits
Read
Previously Pipelined
Solved
int
32 bits
Get Address
Previously Pipelined
Solved
32 bits
External Array: mul
Used
64 bits
Total Array Width: 512
Array Width 0: 512
Read
Previously Pipelined
Solved
64 bits
Read
Previously Pipelined
Solved
32 bits
Get Address
Previously Pipelined
Solved
32 bits
Read
Previously Pipelined
Vectorized
float
64 bits
Multiplication
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits
External Array: neg_mul
Used
64 bits
Total Array Width: 512
Array Width 0: 512
Get Address
Previously Pipelined
Solved
32 bits
Read
Previously Pipelined
Solved
64 bits
Read
Previously Pipelined
int
32 bits
Get Address
Previously Pipelined
Solved
32 bits
Read
Previously Pipelined
Solved
64 bits
Multiplication
Previously Pipelined
Vectorized
float
64 bits
Subtraction
Previously Pipelined
Vectorized
float
64 bits
Write
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits
Load Scalar temp
Solved
64 bits
Read
Previously Pipelined
Solved
64 bits
Write
Previously Pipelined
Vectorized
float
64 bits
External Array: mul
Used
64 bits
Total Array Width: 1024
Array Width 0: 1024
Read
Previously Pipelined
Solved
64 bits
Multiplication
Previously Pipelined
Vectorized
float
64 bits
Addition
Previously Pipelined
Vectorized
float
64 bits
Write
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits
Write
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits
Multiplication
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits
External Array: neg
Used
64 bits
Total Array Width: 1024
Array Width 0: 1024
1.000000
Previously Pipelined
Solved
Read
Previously Pipelined
Solved
32 bits
Addition
Previously Pipelined
Vectorized
int
32 bits
Write
Previously Pipelined
Vectorized
int
32 bits
Load Scalar int
Solved
32 bits
Read
Previously Pipelined
Solved
32 bits
1024.000000
Previously Pipelined
Solved
Comparison
Previously Pipelined
Vectorized
int
1 bits
Branch
Previously Pipelined
Vectorized
0 bits
Read
Pipelined
Fine-grained pipelining
Solved
32 bits
1.000000
Pipelined
Fine-grained pipelining
Solved
RightShift
Pipelined
Fine-grained pipelining
Solved
32 bits
Write
Pipelined
Fine-grained pipelining
Solved
32 bits
Read
Pipelined
Fine-grained pipelining
Solved
32 bits
1.000000
Pipelined
Fine-grained pipelining
Solved
Addition
Pipelined
Fine-grained pipelining
Solved
32 bits
Write
Pipelined
Fine-grained pipelining
Solved
32 bits
Read
Pipelined
Fine-grained pipelining
Solved
32 bits
32.000000
Solved
Write
Solved
int
32 bits
Load Scalar size
Solved
32 bits
Read
Pipelined
Fine-grained pipelining
Solved
32 bits
Branch
Pipelined
Fine-grained pipelining
Solved
0 bits
Return
Solved
void
0 bits
Read
Pipelined
Fine-grained pipelining
Solved
32 bits
Write
Pipelined
Fine-grained pipelining
Solved
32 bits
Read
Pipelined
Fine-grained pipelining
Solved
32 bits
Or
Pipelined
Fine-grained pipelining
bool
32 bits
Write
Pipelined
Fine-grained pipelining
bool
32 bits
Read
Pipelined
Fine-grained pipelining
bool
32 bits
Read
Previously Pipelined
Solved
32 bits
Write
Previously Pipelined
Vectorized
int
32 bits
Write
Previously Pipelined
Vectorized
int
32 bits
Load Scalar size
Solved
32 bits
Read
Previously Pipelined
Solved
32 bits
Get Address
Previously Pipelined
Solved
32 bits
Read
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
32 bits
Get Address
Previously Pipelined
Solved
32 bits
Read
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits
Address
Previously Pipelined
Solved
64 bits
Write
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits
Subtraction
Previously Pipelined
Vectorized
float
64 bits
Write
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
32 bits
Read
Previously Pipelined
Solved
64 bits
Address
Previously Pipelined
Solved
64 bits
Write
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits
Subtraction
Previously Pipelined
Vectorized
float
64 bits
Write
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
32 bits
Write
Previously Pipelined
Vectorized
float
64 bits
Read
Previously Pipelined
Solved
64 bits