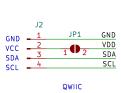
NOTE: This schematic is provided for documentation only. The actual layout was done manually as a single footprint to simplify panelizing. Importing the schematic into the layout will erase the existing layout.



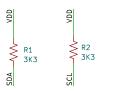
U1								
SLG46826G								
1014	1	1014	VDD	20	VDD			
1013	2	1013	100	19	100			
1012	3	1013	101	18	101			
1011	4	1012	101	17	102			
1010	5	1011	102	16	103			
109	6	1010	103	15	104			
VDD2	7	VDD2	105	14	105			
108	8	108	SCL	13	SCL			
107	9	107	SCL	12	SDA			
GND	10	GND	106	11	106			
		שאט	100					

		J1		
1014	1	_	_ 20	VD
1013	2		19	10
1012	3		18	10
1011	4		17	10:
1010	5		16	10:
109	6		15	10
VDD2	7		14	10
108	8		_ 13	SC
107	9		12	SD.
GND	10		11	10
		•	_	

GreenPak SPLD

DIP Header

This design is compatible with: * SLG46827G * SLG46826G * SLG46827G



I2C Pullups

