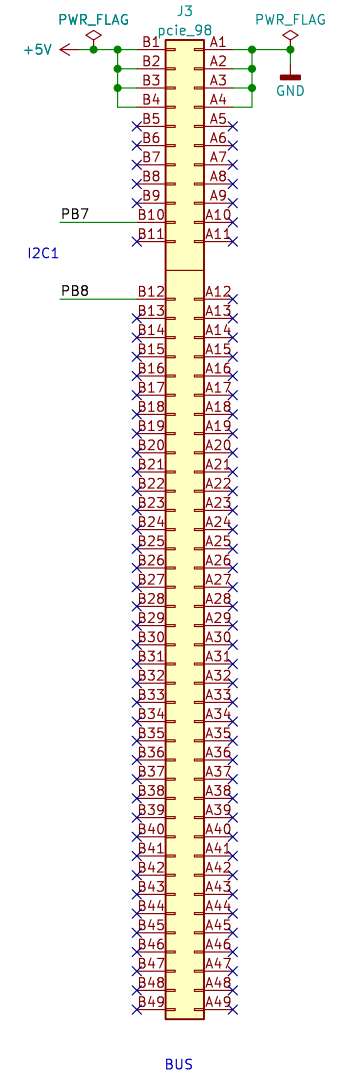
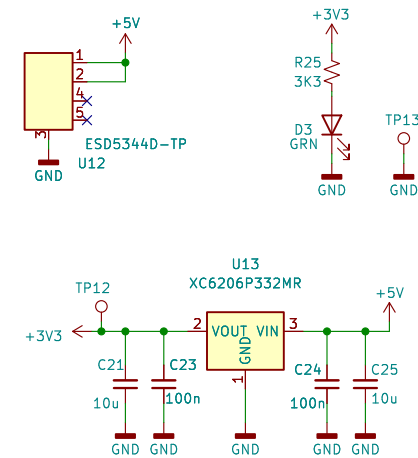
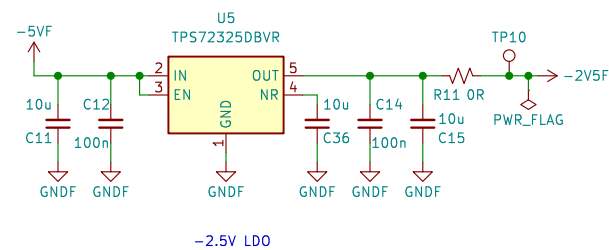
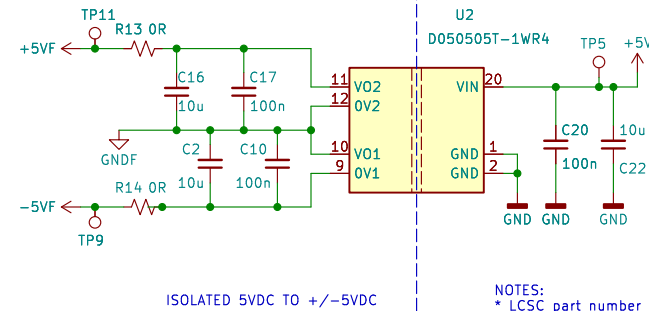
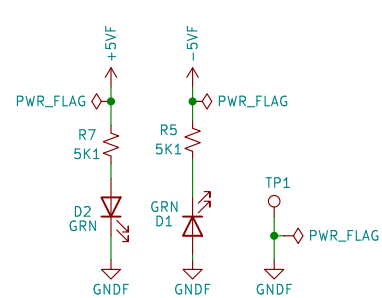
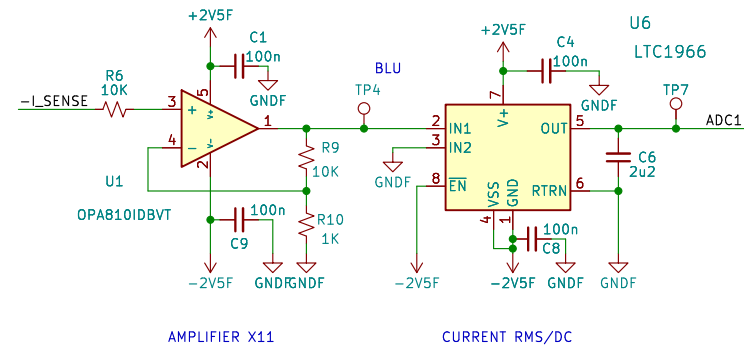
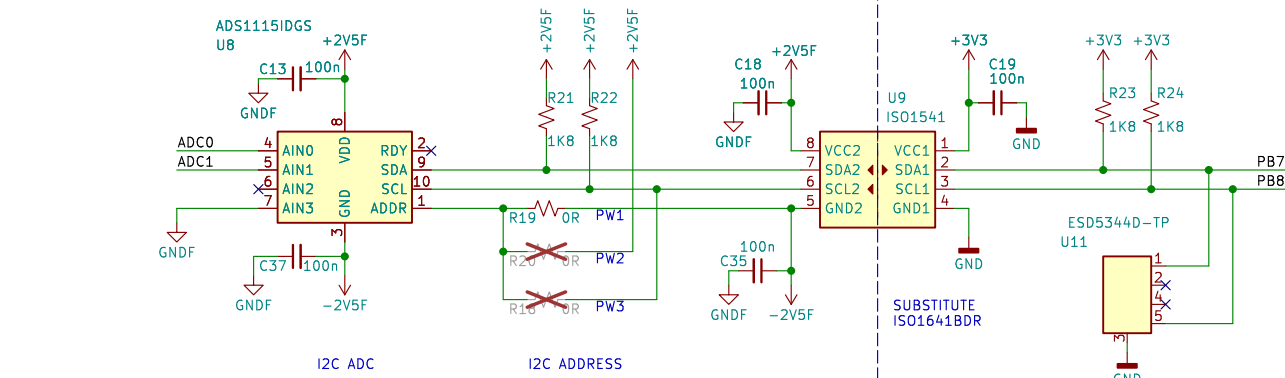
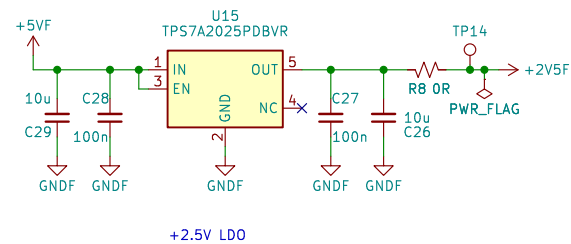
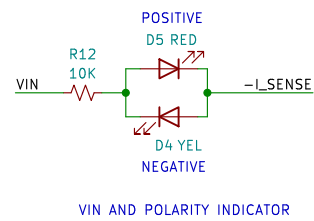
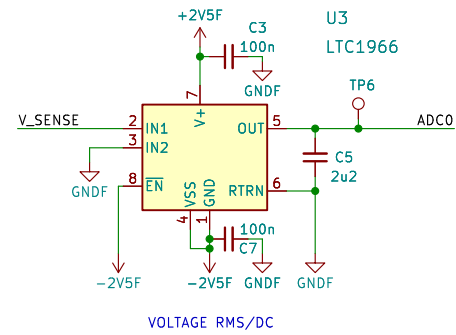
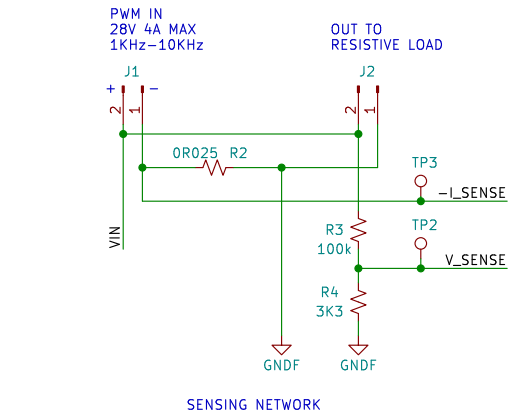


BUS GROUND



- NOTES:
- * LCSC part number are of approximated values to maximize usage of JLCPCB 'basic' parts.
 - * Accuracy can be improved by using low ppm resistors where applicable and a film capacitor for the RMS converter integrator.
 - * JLCPCB fabrication files should be generated using the Kicad's Fabrication Toolkit plugin.
 - * OP4810DBVT was selected for high slew rate, low current offset, and low voltage offset.
 - * Substitute ISO1541 with ISO1641BDR.
 - * Substitute LTC1966 with LTC1967, LTC1968
 - * Several OR resistors were added to allow to disconnect for diagnostics.
 - * The ground plane under the shunt resistor is isolated thermally from the rest of the ground plane.
 - * I sense and V sense are calibrated for positive polarity input. Negative polarity may introduce a small offset but should be in range.

PWM POWER METER V0.2