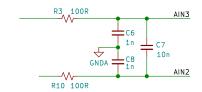ADD INPUTS for two thermistors.

TODO: Add jumpers for two SPI channels.

R3 100R
C6 1n
C7 10n
GNDA
C8 1n
AIN3
AIN2
R10 100R

+5VA
4
3
2
1
LOAD CELL
J2
GNDA
R14 100R
C10 1n
C11 10n
GNDA
C13 1n
AIN1
AIN0
R19 100R
ESD5344-TP
U2
GNDA

NOTE: AINCOM users its own voltage divider to simplify the PCB layout.

+5VA
+3V3
C1 1u
C3 100n
C2 1u
C4 100n
GNDA
GNDD
AVDD
DVDD
4
17
NC 19
NC 20
NC 21
NC 22
+3V3
RESET_N 8
PWDN_N 7
DRDY_N 13
START 9
CLKIN 18
GNDD

+5VA
R1 51K
R2 51K
C5 1u
GNDA
GNDA
AINCOM 1
AIN9 23
AIN8 24
AIN7 25
AIN6 26
AIN5 27
AIN4 28
AIN3 29
AIN2 30
AIN1 31
AIN0 32

+5VA
R4 51K
R7 51K
C9 1u
GNDA
GNDA

AIN3
AIN2
AIN1
AIN0

C12 4n7
CAPP 2
CAPN 3

ADS1261B
U3

AVSS 5
AVSS 33
DGND 16

GNDA
GNDD

+3V3 +3V3
R5 51K
R6 51K

MOSI 12
MISO 14
SCLK 11
CS_N 10

R15 47R
R16 47R
R17 47R
R18 47R

PA7
PA6
PA5
PB14

REFOUT 6
BYPASS 15

C14 1u
C15 10u
R22 51K
R21 51K

GNDA GNDA
GNDD GNDD

1 2 4 5
U4
ESD5344-TP
3
GNDD

+5VD
U6
TPS61252DSG
VIN 8
SW 7
EN 6
GND 1
VOUT 2
FB 3
PG 5
ILIM 4

10u C17
100n C20

L1 1uH

510K R23
C16 100pf

120K R25
22K R28
R26 20K

GNDD GNDD
GNDD GNDD GNDD

5.77V NOMINAL

10u C18
10u C19
R24 5K1
D1 RED

GNDD GNDD GNDD

FB1
PWR_FLAG

R27 0R
PWR_FLAG
GNDA

U5
TPS7A2050PDBVR
IN 1
EN 4
GND 2
OUT 5
NC 3

10u C21
GNDA

100n C22
10u C23
10u C24

GNDA GNDA GNDA

+5VA

+5VD
U7
XC6206P332MR
C5446
VIN 3
GND 1
VOUT 2
+3V3

10u C25
100n C26
100n C27
10u C28

GNDD GNDD GNDD GNDD GNDD

PWR_FLAG
J5
pcie_98
PWR_FLAG
+5VD
GNDD

B1 A1
B2 A2
B3 A3
B4 A4
B5 A5
B6 A6
B7 A7
B8 A8
B9 A9
B10 A10
B11 A11
B12 A12
B13 A13
PB14 B14 A14
B15 A15
B16 A16
B17 A17
B18 A18
B19 A19
B20 A20
PA6 B21 A21
B22 A22
B23 A23
B24 A24
B25 A25
B26 A26
B27 A27 PA7
PA5 B28 A28
B29 A29
B30 A30
B31 A31
B32 A32
B33 A33
B34 A34
B35 A35
B36 A36
B37 A37
B38 A38
B39 A39
B40 A40
B41 A41
B42 A42
B43 A43
B44 A44
B45 A45
B46 A46
B47 A47
B48 A48
B49 A49

TODO: Should MOSI be pulled up or down?