

10W isolated DC-DC converter in DIP package
Ultra-wide input and regulated dual/single output



CA Report

BS EN62368-1

CE Report

UL60950-1

EN62368-1

#### **FEATURES**

- Ultra-wide 4:1 input voltage range
- High efficiency up to 88%
- No-load power consumption as low as 0.12W
- I/O isolation test voltage 1.5k VDC
- Input under-voltage protection, output
   short-circuit, over-current, over-voltage protection
- Operating ambient temperature range: -40°C to +85°C
- Meets CISPR32/EN55032 CLASS A, without extra components
- Input reverse polarity protection available with Chassis (A2S) or 35mm DIN-Rail mounting (A4S) version
- Meets EN50155 railway standard
- Industry standard pin-out

URA\_YMD-10WR3 & URB\_YMD-10WR3 series are isolated 10W DC-DC converter products feature an ultra-wide with 4:1 input voltage with efficiencies of up to 88%, 1500VDC input to output isolation, operating ambient temperature range of -40°C to +85°C, input under-voltage protection, output short-circuit, over-current, over-voltage protection. They meet CLASS A of CISPR32/EN55032 EMI standards without external components, optional packages are offered for chassis or DIN-rail mounting (A2S, A4S), adding additional input reverse polarity protection and they are widely used in applications such as industrial control, electric power, instruments, communication and railway applications.

IEC60950-1

Selection Gu	uide						
		Input Volta	age (VDC)	Out	put	Full Load	Capacitive
Certification	Part No. <sup>11</sup>	Nominal <sup>®</sup> (Range)	Max. <sup>®</sup>	Voltage(VDC)	Current (mA) Max./Min.	Efficiency <sup>®</sup> (%)Min./Typ.	Load <sup>©</sup> (µF)Max.
	*URA2405YMD-10WR3	_		±5	±1000/0	81/83	1000
	URA2409YMD-10WR3			±9	±555/0	84/86	680
	*URA2412YMD-10WR3			±12	±416/0	85/87	470
	URA2415YMD-10WR3			±15	±333/0	85/87	330
	*URA2424YMD-10WR3			±24	±208/0	85/87	100
	URB2403YMD-10WR3	24 (9-36)	40	3.3	2400/0	75/77	2200
	URB2405YMD-10WR3	(9-30)		5	2000/0	80/82	2200
	URB2409YMD-10WR3			9	1111/0	83/85	680
	URB2412YMD-10WR3			12	833/0	84/86	470
LIL /ENL/DC ENL/IEC	URB2415YMD-10WR3			15	667/0	84/86	330
UL/EN/BS EN/IEC	URB2424YMD-10WR3			24	416/0	86/88	100
	*URA4805YMD-10WR3			±5	±1000/0	81/83	1000
	*URA4812YMD-10WR3			±12	±416/0	85/87	470
	*URA4815YMD-10WR3			±15	±333/0	85/87	330
	*URA4824YMD-10WR3	40		±24	±208/0	85/87	100
	*URB4803YMD-10WR3	48 (18-75)	80	3.3	2400/0	77/79	2200
	*URB4805YMD-10WR3	(10-70)		5	2000/0	81/83	2200
	*URB4812YMD-10WR3			12	833/0	85/87	470
	*URB4815YMD-10WR3			15	667/0	85/87	330
	*URB4824YMD-10WR3			24	416/0	86/88	100

**MORNSUN®** 

MORNSUN Guangzhou Science & Technology Co., Ltd.

# DC/DC Converter URA\_YMD-10WR3 & URB\_YMD-10WR3 Series



#### Notes:

- Use "H" suffix for heat sink mounting, "A2S" suffix for chassis mounting and "A4S" suffix for DIN-Rail mounting;
- The A2S and A4S Model's start-up and minimum input voltages are increased by 1VDC due to the input reverse polarity protection circuit;
- 3 Exceeding the maximum input voltage may cause permanent damage;
- 4 Efficiency is measured at nominal input voltage and rated output load; efficiencies for A2S and A4S Model's is decreased by 2% due to the input reverse polarity protection circuit;
- 5) The specified maximum capacitive load value for positive and negative output is identical;
- 6 Products marked with "\*" need an input capacitor in order to meet conducted specifications of CISPR32/EN55032 CLASS A.

Item	Operating Conditions		Min.	Тур.	Max.	Unit
	24VDC nominal input series,	3.3VDC output	-	429/5	440/12	
Input Current	nominal input voltage	Others	-	502/5	521/12	
(full load / no-load)	48VDC nominal input series,	3.3VDC output		190/4	215/8	A
	nominal input voltage	Others	-	251/4	258/8	mA
Deflected Dipple Current	24VDC nominal input series, no	ominal input voltage	-	40	-	-
Reflected Ripple Current	48VDC nominal input series, no	ominal input voltage	-	30	-	
Curas Voltago (Issa may)	24VDC nominal input series		-0.7		50	
Surge Voltage (1sec. max.)	48VDC nominal input series		-0.7	-	100	VDC
Start-up Voltage	24VDC nominal input series			-	9	VDC
sidit-up vollage	48VDC nominal input series				18	
Input Under-voltage Protection	24VDC nominal input series		5.5	6.5	-	VDC
input under-voltage Protection	48VDC nominal input series		12	15.5	_	VDC
Start-up Time	Nominal input voltage & const	ant resistance load		10		ms
Input Filter				Pi f	ilter	
Hot Plug				Unav	ailable	
	Module on		Ctrl pi	n open or pull	ed high (3.5-1:	2VDC)
Ctrl *	Module off		Ctrl p	oin pulled low	to GND (0-1.2)	VDC)
	Input current when off			6	10	mA

Output Specification	S					
Item	Operating Conditions		Min.	Тур.	Max.	Unit
Voltage Accuracy <sup>®</sup>	0%-100% load		-	±1	±3	
Lineary Dear destina	Input voltage variation from low to	Vo1	-	±0.2	±0.5	
Linear Regulation	high at full load	Vo2	-	±0.5	±1	9/
Lord Domination 2	59/ 1009/ In and	Vo1	-	±0.5	±1	%
Load Regulation®	5%-100% load	Vo2		±0.5	±1.5	
Cross Regulation	Dual output, Vo1 load at 50%, Vo2 10%-100%	load at range of	-		±5	
Transient Recovery Time	OF9/ In end at an absence on a main all in-			300	500	μs
Transient Response Deviation	25% load step change, nominal in	out voltage		±3	±5	%
Temperature Coefficient	Full load				±0.03	%/℃
Ripple & Noise®	20MHz bandwidth, 5%-100% load			40	80	mVp-p
Over-voltage Protection			110		160	%Vo
Over-current Protection	Input voltage range		110	140	190	%lo
Short-circuit Protection				Continuous,	self-recovery	

#### Note:

- $\bigcirc$  Output voltage accuracy of  $\pm 5$ VDC/ $\pm 9$ VDC output converter for 0%-5% load is  $\pm 5$ % max;
- 2Load regulation for 0%-100% load is ±5%;
- ③Under 0% -5% load conditions, ripple & noise does not exceed 5%Vo. The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information.



General Specificat			_		
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation	Input-output Electric Strength Test for 1 minute with a leakage current of 1mA max.	1500			VDC
Insulation Resistance	Input-output resistance at 500VDC	1000			MΩ
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V		1000		рF
Operating Temperature	See Fig. 1	-40		+85	°C
Storage Temperature		-55	-	+125	
Storage Humidity	Non-condensing	5		95	%RH
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			+300	$^{\circ}$
Vibration		IEC	/EN61373 - C	ategory 1, Gro	ade B
Switching Frequency *	PWM mode		350		kHz
MTBF	MIL-HDBK-217F@25℃	1000			k hours

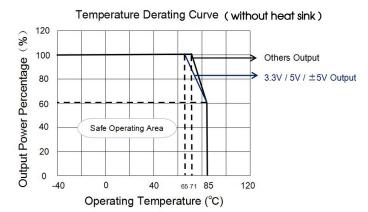
Mechanical Spec	ifications		
Case Material	Aluminum alloy		
	Horizontal package	(without heat sink)	25.40 x 25.40 x 11.70 mm
Blooming	Horizontal package	(with heat sink)	25.40 x 25.40 x 16.20 mm
Dimensions	A2S chassis mountin	ng	76.00 x 31.50 x 21.20 mm
	A4S DIN-rail mountin	ng	76.00 x 31.50 x 25.80 mm
Weight	without heat sink	Horizontal package/A2S chassis mounting/A4S DIN-Rail mounting	12.5g/36.0g/56.0g (Typ.)
	with heat sink	Horizontal package	17g
Cooling method	Free air convection		

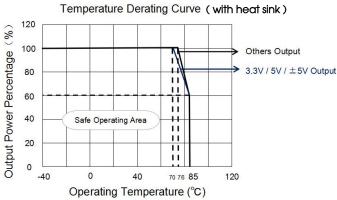
Electror	magnetic Com	npatibility (EMC)		
Emissions	CE	CISPR32/EN55032	CLASS A (Without extra components)/ CLASS B (see Fig.3-② for recommended circuit)	
ETTISSIOTIS	RE	CISPR32/EN55032	CLASS A (Without extra components)/ CLASS B (see Fig.3-② for recommended circuit)	
	ESD	IEC/EN61000-4-2	Contact ±4kV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2kV (see Fig.3-① for recommended circuit)	perf. Criteria B
Immunity	Surge	IEC/EN61000-4-5	line to line ±2kV (see Fig.3-①for recommended circuit)	perf. Criteria B
ITITICITITY	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A
	Voltage dips, short interruptions and voltage variations immunity	IEC/EN61000-4-29	0%, 70%	perf. Criteria B

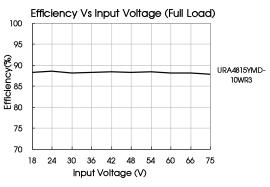
Electror	magnetic Con	npatibility (	(EMC) (EN50155)	
	CE	EN50121-3-2	150kHz-500kHz 99dBuV (see Fig.3-2) for recommended circuit)	
Emissions	<u> </u>	EN55016-2-1	500kHz-30MHz 93dBuV (see Fig.3-2) for recommended circuit)	
LITIOSIONS	RE	EN50121-3-2	30MHz-230MHz 40dBuV/m at 10m (see Fig.3-2) for recommended	ed circuit)
	KL	EN55016-2-1	230MHz-1GHz 47dBuV/m at 10m (see Fig.3-2) for recommended	ed circuit)
	ESD	EN50121-3-2	Contact ±6kV/Air ±8kV	perf. Criteria A
	RS	EN50121-3-2	20V/m	perf. Criteria A
Immunity	EFT	EN50121-3-2	±2kV 5/50ns 5kHz (see Fig.3-1) for recommended circuit)	perf. Criteria A
	Surge	EN50121-3-2	line to line ±1kV (42 $\Omega$ , 0.5 $\mu$ F) (see Fig.3-1) for recommended circuit)	perf. Criteria A
	CS	EN50121-3-2	0.15MHz-80MHz 10V r.m.s	perf. Criteria A

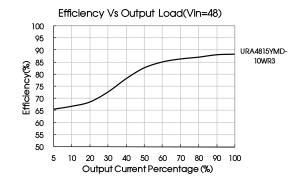


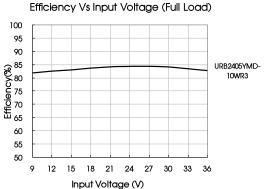
#### Typical Characteristic Curves

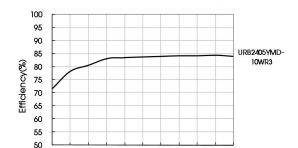












20 30 40 50 60 70 80

Output Current Percentage (%)

90

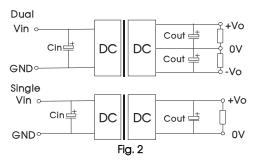
Efficiency Vs Output Load(Vin=24)

## Design Reference

#### 1. Typical application

All the DC/DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 2. Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.

Fig. 1



Vin(VDC)	Vout(VDC)	Cin	Cout
	3.3/5/±5		10µF/16V
24	9/12/15/±9/± 12/±15	100µF/50V	10µF/25V
	24/±24		10µF/50V
	3.3/5/±5		10µF/16V
48	9/12/15/±9/± 12/±15	10μF - 47μF/100V	10µF/25V
	24/±24		10µF/50V



Vin: 48VDC

#### 2. EMC compliance circuit

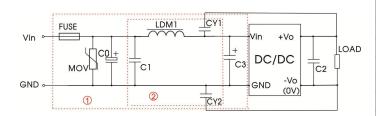


Fig. 3

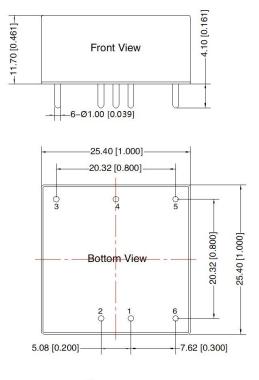
Notes: For EMC tests we use Part ① in Fig. 3 for immunity and part ② for emissions test. Selecting based on needs.

Parameter description	n:	
Model	Vin: 24VDC	
FUSE	Choose according to	ac
MOV	S20K30	
00.00	000 -F /FO\ /	

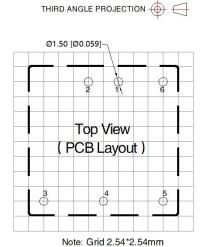
FUSE	Choose according to	actual input current
MOV	S20K30	S14K60
C0, C3	330µF/50V	330µF/100V
C1	1µF/50V	1µF/100V
C2	Refer to the	Cout in Fig.2
LDM1	4.7	μH
CY1, CY2	1nF,	/2kV

- 3. The products do not support parallel connection of their output
- 4. For additional information please refer to DC-DC converter application notes on www.mornsun-power.com

## Horizontal Package (without heat sink) Dimensions and Recommended Layout





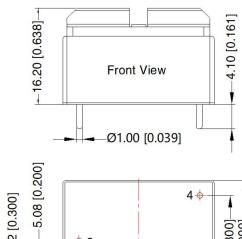


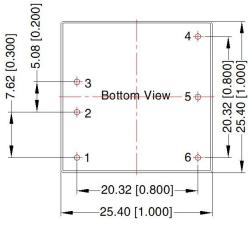
	Pin-Out	
Pin	Single	Dual
1	GND	GND
2	Vin	Vin
3	+Vo	+Vo
4	No Pin	OV
5	OV	-Vo
6	Ctrl	Ctrl



## Horizontal Package (with heat sink) Dimensions and Recommended Layout



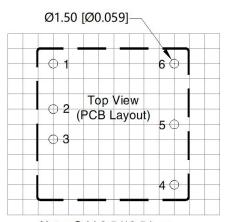




Note:

Unit: mm[inch]

Pin diameter tolerances:  $\pm 0.10[\pm 0.004]$ General tolerances:  $\pm 0.50[\pm 0.020]$ 

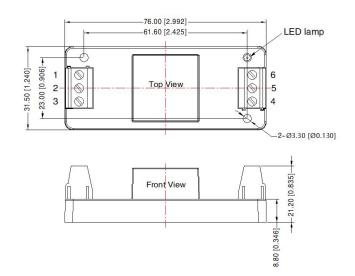


Note: Grid 2.54\*2.54mm

	Pin-Out	İ
Pin	Single	Double
1	Ctrl	Ctrl
2	GND	GND
3	Vin	Vin
4	+Vo	+Vo
5	No Pin	OV
6	OV	-V0

### URA\_YMD-10WR3A2S & URB\_YMD-10WR3A2S Dimensions





Pin-Out									
Pin	1	2	3	4	5	6			
Single	Ctrl	GND	Vin	+Vo	NC	0V			
Dual	Ctrl	GND	Vin	+Vo	OV	-Vo			

Note:

Unit: mm[inch]

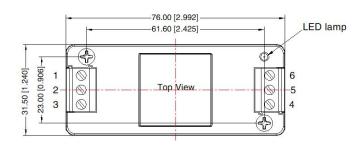
Wire range: 24-12 AWG

Tightening torque: Max 0.4 N ⋅ m General tolerances: ±1.00[±0.039]

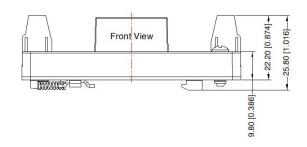


## URA\_YMD-10WR3A4S & URB\_YMD-10WR3A4S Dimensions





Pin-Out									
Pin	1	2	3	4	5	6			
Single	Ctrl	GND	Vin	+Vo	NC	OV			
Dual	Ctrl	GND	Vin	+Vo	oV	-Vo			



Note:

Unit: mm[inch]
Mounting rail: TS35
Wire range: 24–12 AWG

Tightening torque: Max 0.4 N ⋅ m General tolerances: ±1.00[±0.039]

#### Note:

- For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>. Packaging bag number: 58210003 (DIP), 58220022(A2S/A4S package);
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on our company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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