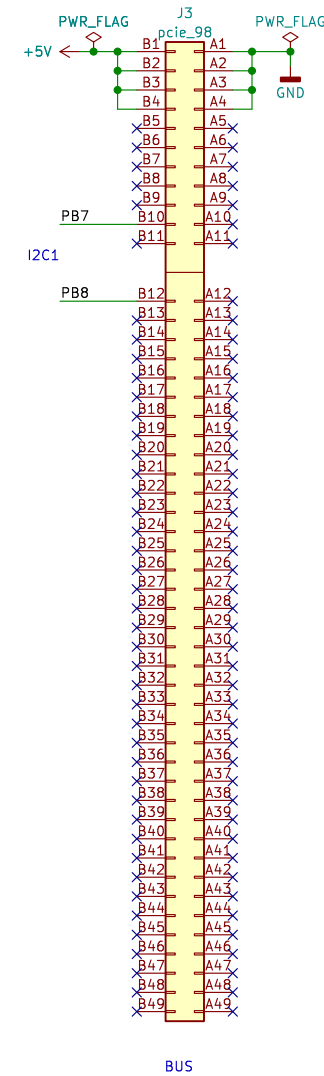
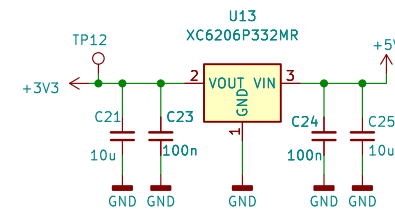
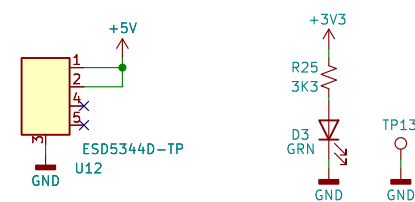
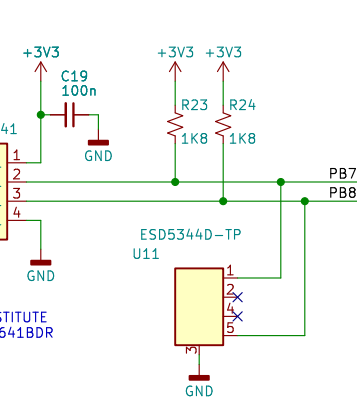
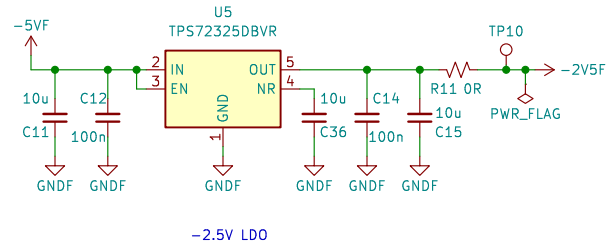
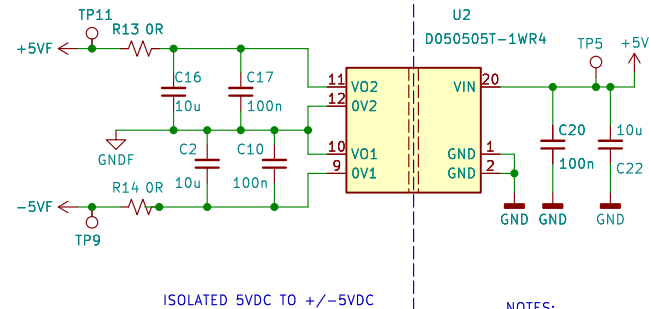
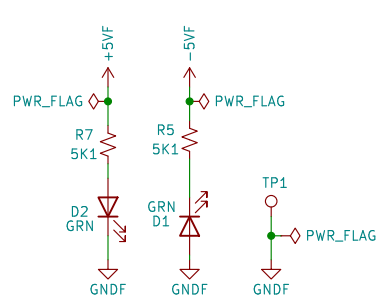
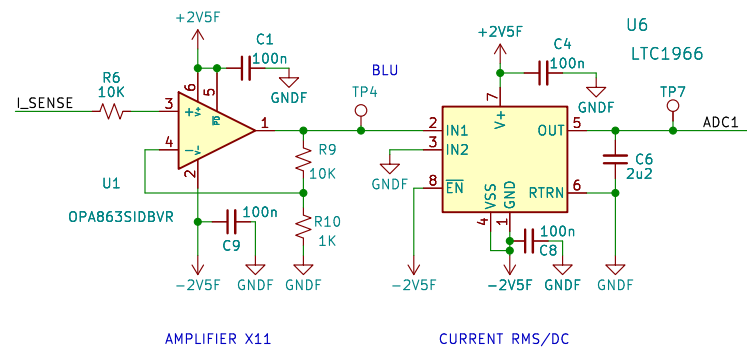
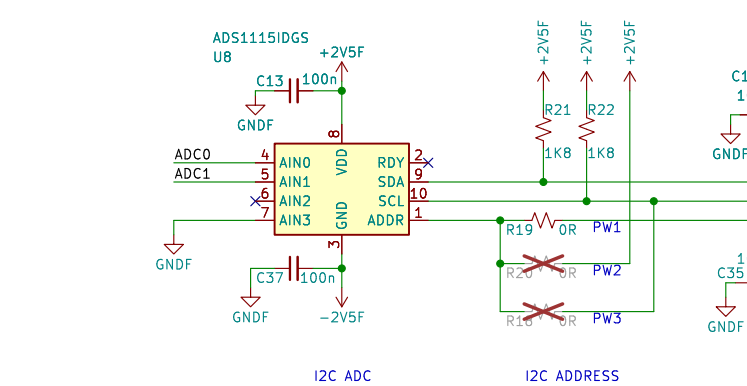
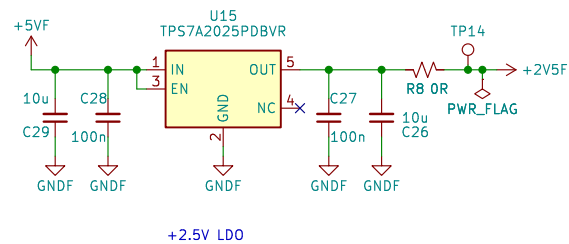
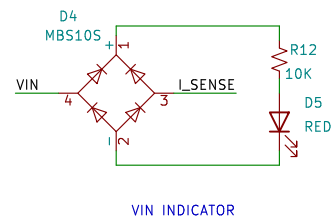
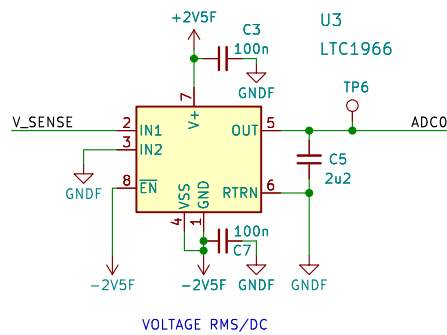
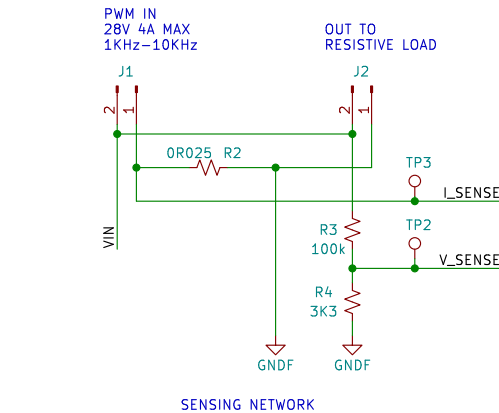


TODO: Verify pinout

TODO: Verify footprints

FLOATING GROUND

BUS GROUND



NOTES:  
\* LCSC part number are of approximated values to maximize usage of JLCPCB 'basic' parts.  
\* Accuracy can be improved by suing low ppm resistors where applicable and a film capacitor for the RMS converter integrator.  
\* JLCPCB fabrication files should be generated using the Kicad's Fabrication Toolkit plugin.  
\* Substitute OPA863SIDBVR with OPA863DBVR.  
\* Substitute ISO1541 with ISO1641BDR.  
\* Substitute LTC1966 with LTC1967, LTC1968  
\* Several 0R resistors where added to allow to disconnect for diagnostics.  
\* The ground plane under the shunt resistor is isolated thermally from the rest of the ground plane.

PWM POWER METER V0.2