TODO: verify pin numbers against datasheets. 1K 0.1% 10ppm 1K 0.1% 10ppm 1K 0.1% 10ppm TODO: verify footprints against datasheets. <sup>+</sup>C3 100n 100n AIN8 PWR\_FLAG pcie\_98 470R R12 🗧 R2 1K 0.1% 10ppm 1K 0.1% 10ppm 1K 0.1% 10ppm GNDA C11 1u C12 100n THERMISTOR 1 THERMISTOR 3 THERMISTOR 2 C13 100n (AIN4-AIN5)/(AVDD-AVSS) (AIN8-AIN9)/(AVDD-AVSS) (AIN6-AIN7)/(AVDD-AVSS) GNDA 1K 0.1% 10ppm 1K 0.1% 10ppm R17 R13 AIN9 AIN8 AIN7 AIN6 AIN5 AIN4 24 AIN8 25 AIN7 PWDN\_N 7
DRDY\_N 13 1K 0.1% 10ppm 26 AIN6 R14 > START 27 AIN5 R15 R16 C46 GNDA 1K 0.1% 10ppm AIN3 AIN2 AIN1 AIN0 29 AIN3 1K 0.1% 10ppm 30 AIN2 GNDA 31 AIN1 ----√\^ R35 R23 5K1 R24 5K1 R25 5K1 R26 5K1 R36 1K 0.1% 10ppm R18 \ 47R R19 \ 47R R21 \ 47R R22 \ 47R PA6 PA5 PA7 REF\_P = 6 SIGNL\_P = 5 SHLD = 4 ±C14 22n SIGNL\_N -REFOUT 6 BYPASS 15 REF\_N = 2 EXCIT\_N = 1 C21 GNDA ±C20 22n GNDA JP2 ADS1261B J5 GNDA Tu GNDA 51K 51K U1 ESD5344D-TP C35 1 1u GNDDGNDD U2 LOAD CELL GNDAGNDA (AIN2-AIN3)/(AIN0-AIN1) U4 TPS7A2025PDBVR U9 XC6206P332MR 10u C26 100n C27 100n C25 10u C28 C24 U6 100n C23 100n C5446 TPS65135 +5VD 360K 0.1% GNDA GNDA GNDA GNDA GNDA C32 C29 BUS OUTP 100 10u 100n C33 GNDD GNDD GNDA GNDD GNDD GNDD R32 GNDA 100K 0.1% GNDA GNDA GNDA GNDA GNDA GNDA DIGITAL 3.3V 仝 100n C42 12 PGND R33 100n C36 100n 100n OUTN 2 OUTN 3 C41 C37 10u C43 C39 17 PAD C38 464K 0.1% GNDA GNDA GNDA

> U8 ADP7182AUJZ-2.5

U7 ADP7182AUJZ-5.0