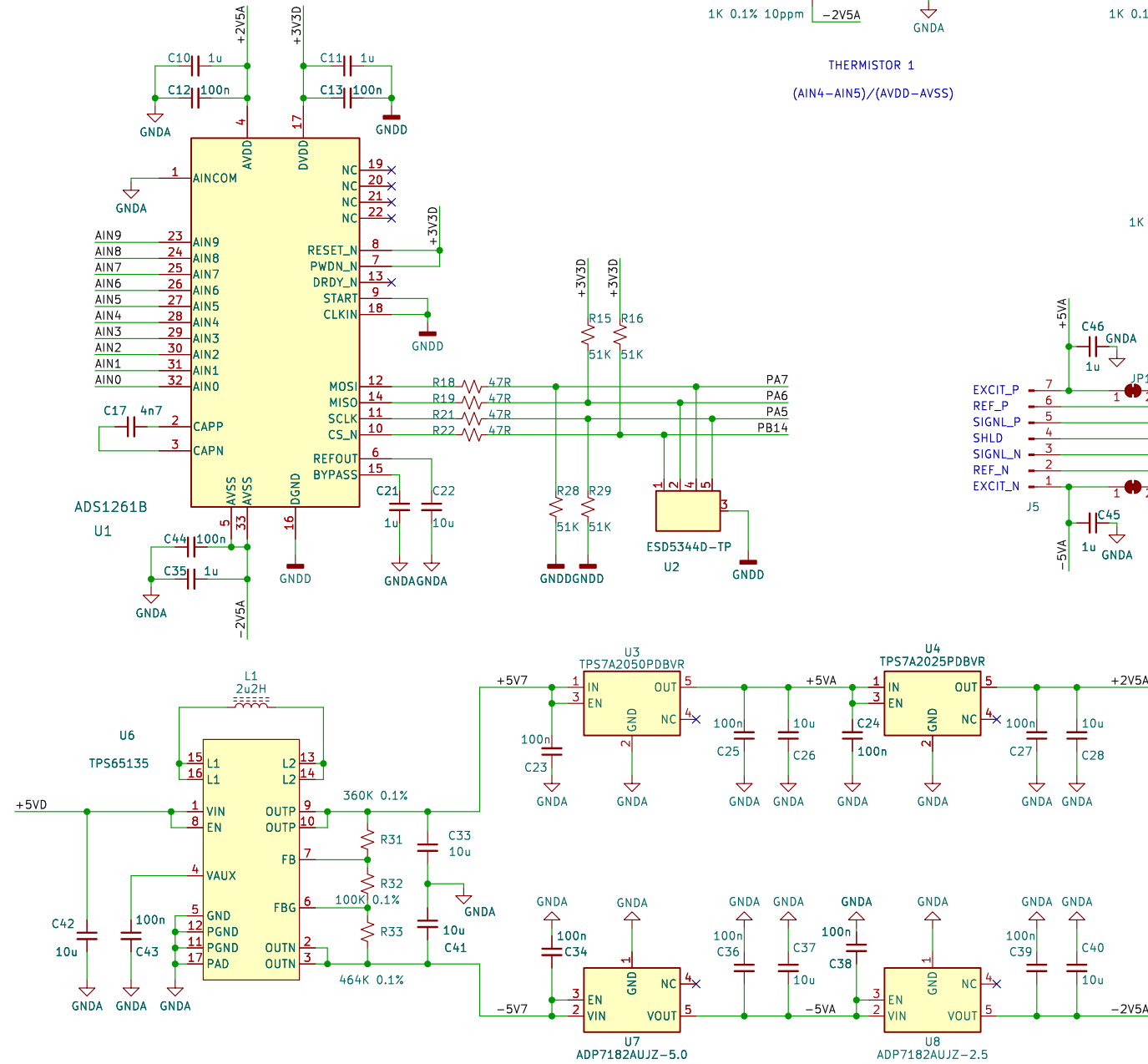
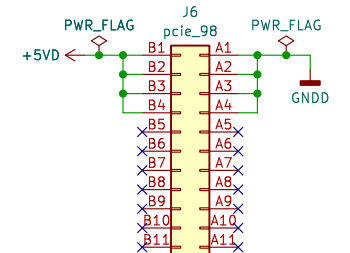


TODO: verify footprints against datasheets.

THERMISTOR 1
(AIN4-AIN5)/(AVDD-AVSS)

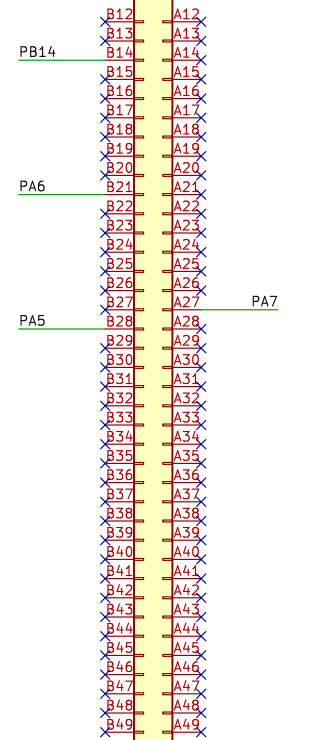
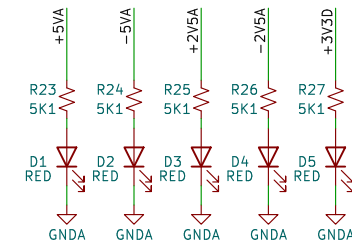
THERMISTOR 2
(AIN6-AIN7)/(AVDD-AVSS)

THERMISTOR 3
(AIN8-AIN9)/(AVDD-AVSS)



LOAD CELL

$(AIN2-AIN3)/(AIN0-AIN1)$



BUS

DIGITAL 3.3V

