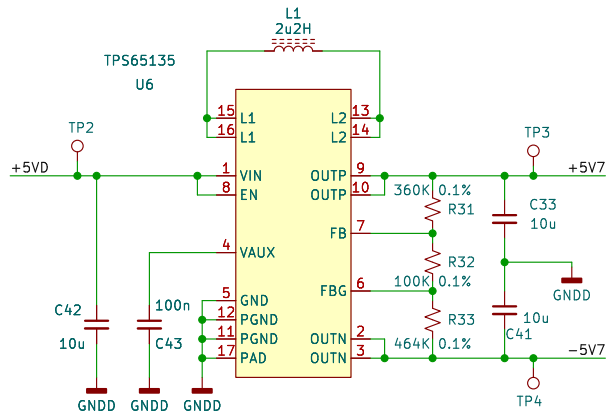
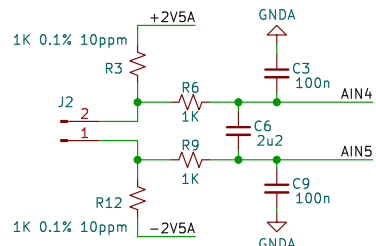


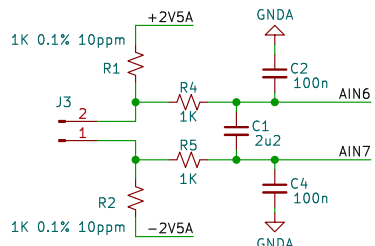
5 CHANNELS DIFFERENTIAL 24BIT ADC



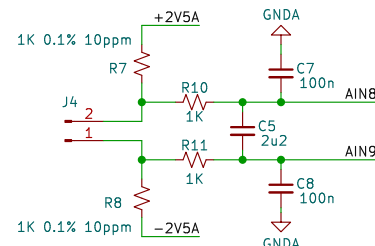
DC/DC +/-5.7V



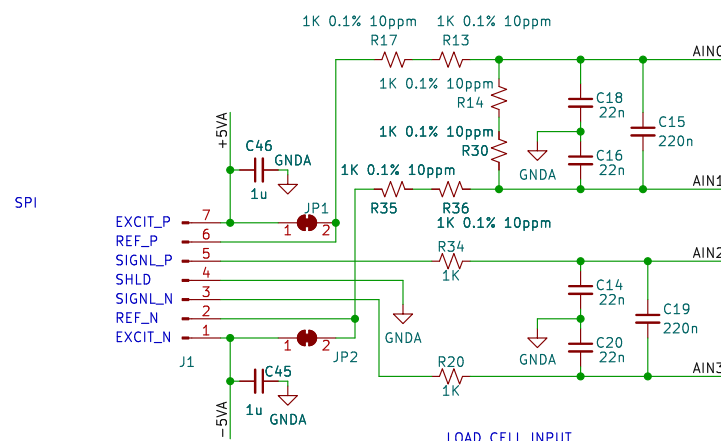
THERMISTOR 1
MEASURED (AIN4-AIN5)/(AVDD-AVSS)



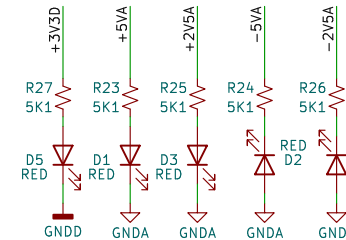
THERMISTOR 2
MEASURED (AIN6-AIN7)/(AVDD-AVSS)



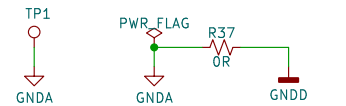
THERMISTOR 3
MEASURED (AIN8-AIN9)/(AVDD-AVSS)



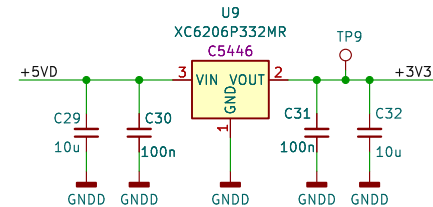
LOAD CELL INPUT
MEASURED (AIN2-AIN3)/(AIN0-AIN1)



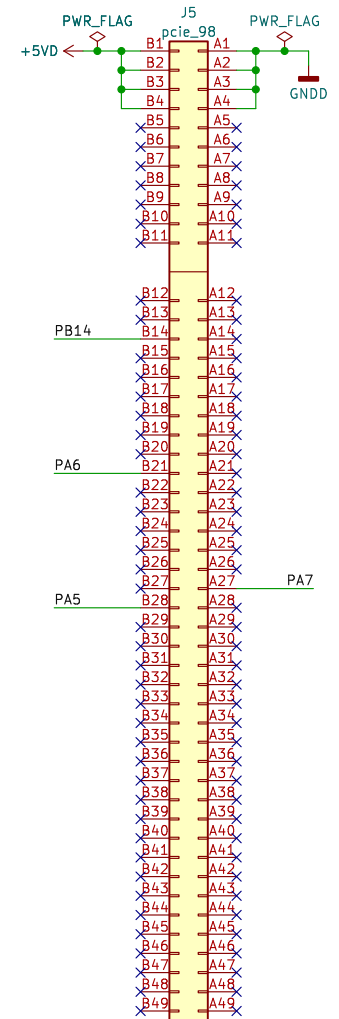
INDICATIONS



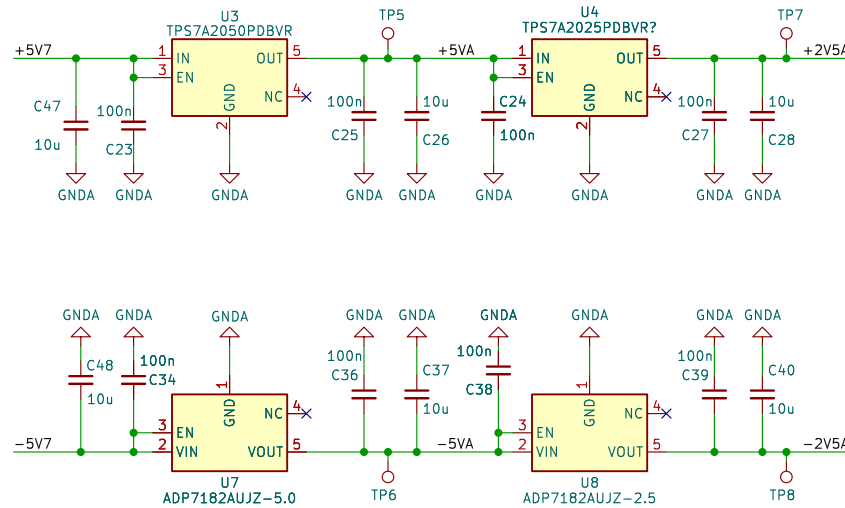
GROUND



DIGITAL 3.3V



BUS



LOW NOISE LDO +/-5V, +/-2.5V