

Microoperations flow diagram	Control signals flow diagram	Control signals sequence
		step ₀₀ br (if not ADDR step ₀₀)
		step ₀₁ br(case (immed,memdir,regdir,regind,brpom,xrpom,bxpom) then (immed, step ₀₂) (memdir, step ₀₃) (regdir,step ₀₄) (regind,step ₀₅) (brpom, step ₀₆) (xrpom, step ₀₆) (bxpom,step ₀₆))
<p>IMMED</p>	<p>IMMED</p>	step ₀₂ ldB, mxB0 , br step ₀₉
<p>MEMDIR</p>	<p>MEMDIR</p>	step ₀₃ ldMAR, mxMAR0 , br step ₀₇
<p>REGDIR</p>	<p>REGDIR</p>	step ₀₄ ldB, mxB1 , br step ₀₉
<p>REGIND</p>	<p>REGIND</p>	step ₀₅ ldMAR, mxMAR1 , br step ₀₇
<p>BRPOM</p>	<p>BRPOM</p>	step ₀₆ ldMAR, mxMAR0, mxMAR1 , br step ₀₇

