Minimizing Page Faults of Bloom Filters

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Abstract

Bloom filters [1] are used ubiquitously due to their speed and memory efficiency in theory and in practice. However, the standard implementation of sufficiently large bloom filters suffers from page faults. In this paper we propose a bloom filter implementation that guarantees one page access per operation. This minimizes page faults, thereby drastically improving efficiency. We will show theoretically and empirically that our hierarchical implementation is expected to be faster than the standard implementation without alterating the false positive rate.

Keywords: probabilistic data structures, bloom filters, memory hierarchy, implementation, page fault analysis

1 Introduction

Bloom filters are space efficient probabilistic data structures that implement set operations developed by Bloom in 1970 [1]. The trade-off of the afformentioned space efficiency is a probabilistic response. When an element is queried for membership, a "false" response means the element is definitely not a member, but a "true" response only means the element may be a member. The rate at which the data structure incorrectly reports that an element has been inserted is called the False Positive Rate (FPR). This probabilistic feature allows Bloom filters to be extraordinarly space efficient, only needing a constant number of bits per stored element irrespective of the elements size.

Due to Bloom filter's space efficiency they have been adopted in many domains from network security [2] to bioinformatics [6]. Any improvement in performance (without negatively alterating the false positive rate) could lead to reduce latency in network calls or speed-ups in sequenceing DNA. This motivates our proposed solution which exploits computer systems' memory hierarchy to provide better performance without increasing the false positive rate of bloom filters.

2 Implementations

2.1 Standard Implementation

Before we discuss the proposed solution, let us review the standard implementation. The standard implementation allocates a bit vector of size m bits. Typically, this is set to be $\times 10$ the expected number of elements it will hold.

Standard Bloom Filter													
Bit Vector:													

Per the standard implementation, to insert an element we hash the element k times and set the corresponding bit in the vector. To query, we simply read instead of set the bit. Notice, that if m is sufficiently large, it will span across multiple pages of memory. Thus, when we read and write to the underlying bit vector, we may page fault for every bit set (k times), slowing down our insertions or queries.

2.2 Proposed Hierarchical Implementation

Our proposal is to allocate w bit vectors of size P bits, where P is computer system's page size in bits and w is an integer such that m = wP. Notice, our proposed implementation uses the same amount of memory, but splits the bit vector into page size chunks.

	Hierarch	ical Bloom Filte	r			
Bit Vector 1:	Bit Vector 2:]	Bit Vector w :		

To insert, hash the element l times mod w and insert the element per the standard bloom filter operations to the corresponding bit vector. Each bit vector (bloom filter) has k hash functions associated with it. Here l is a pre-determined parameter of the datastructure; we will dicuss what the optimal setting is in a following section. To query, we simply query the corresponding bit vector instead of inserting. In essence, our proposal to create a bloom filter of bloom filters, hence the name.

Notice, for any given insertion or query, we have to perform l more hash operations than the standard implementation. This is not a concern if we select sufficiently cheap hash functions. More importantly, since each bit vector is on it's own page of memory, we expect to page fault at most l times. Thus, if we minimize l without sacrificing effectiveness, we will reduce the expected number of page faults and increase the data structures efficiency.

3 Theoretical Work

In this section we will justify why the hierarchical implementation will outperform the standard implementation in theory while preserving effectiveness. First, we will **calculate expected number of page faults** for each implementation. Second, we will **find the theoretical false positive rate**, which will guide us in our parameter selection for the implementation.

3.1 Expected Page Faults

Page faults occur when the operating system is forced to fetch data from a source lower in the memory hierarchy to be used by the process. Whenever a page fault occurs, the program must be halted to resolve the page fault, which requires relatively slow I/O operations such as checking the TLB or loading the page from disk. This leads to slower performance.

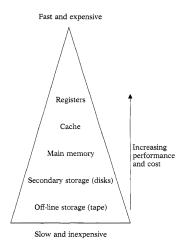


Table 1. Properties of the memory hierarchy.

Memory type	Access time	Cost/MB	Typical amount used	Typical cost
Registers	1 ns	High	1 KB	-
Cache	5–20 ns	\$100	1 MB	\$100
Main memory	60-80 ns	\$1.10	64 MB	\$70
Disk memory	10 ms	\$0.05	4 GB	\$200

[4, Murdocca et al.]

As demonstrated in the figures above from Miles J. Murdocca et al. [4], we see that we get radically better performance the closer we access memory to the CPU. If we design our algorithm to be more cache-efficient, then we expect massive speed-ups in practie.

Note, when discussing page faults in this section, we will only discuss the page faults caused due to accessing the underlying bit vector of the bloom filter. Natuarlly, page faults can occur while running the underlying code of the bloom filter, but this should be rare and would realistically cause at most

one page fault. We will now compute the expected number of page acceses for both the standard and hierarchical implementation.

The problem with doing an analysis on page faults is that it impossible to know when accessing a page will cause a page fault as this is dependent on the operating system and hardware. Generally, the more memory you are using, the higher the likelyhood a page fault will occur. In this analysis we will assume more page accesses is coorelated with a higher liklihood of page faulting. In otherwords, we can approximate the liklihood of page page fault to the expected number of different pages that will be accessed during an operation.

First, we will discuss the standard implementation. Let A be a random variable representing the number of pages accessed. Let P be the number of bits in a page and let m be the number of bits in the underlying bitvector. Suppose there are k hash functions. We now define the indictor variable A_i which is 1 if bit i is set, 0 otherwise.

$$A = A_1 + A_2 + \ldots + A_{m/P}$$

Thus, the expected number of distinct pages accessed can be found by computing the expected value that any given page is accessed and applying linearity of expectation:

$$E(A) = \sum_{i=1}^{m/P} E(A_i) = \frac{m}{P} \cdot E(A_i) \text{ for arbitrary } i$$

The probability that A_i is accessed at least once is the inverse of it being never accessed. Assuming that our hash function is uniform, we expect it to pick any particular page with probability $\frac{1}{m/P} = \frac{P}{m}$. Thus, the probability A_i is accessed at least once is:

$$E(A_i) = 1 - (1 - \frac{P}{m})^k$$

Ergo, we have a closed form equation for the expected number of page faults for a given operation:

Expected distinct pages accessed (Standard) =
$$\frac{m}{P}(1 - (1 - \frac{P}{m})^k)$$

We will use this formula to compute the expected number of page faults for two reasonable cases. First, suppose you wanted a bloom filter to store 32,768 elements with an underlying bit vector of size $\times 10$ that. Note, most computer systems have a page size of 4096 bytes, so this works out to require exactly 10 pages of memory. Additionally, suppose we pick the optimal bloom filter parameter and set k=7. Under this scenario, we anticpate:

Expected distinct pages accessed (Standard) =
$$10(1 - 0.9^7) \approx 5.2$$

If we wanted to store 327,680 elements under a similar setting, then the number of pages faults would be:

Expected distinct pages accessed (Standard) =
$$100(1 - 0.99^7) \approx 6.8$$

As we can see, even using a relatively small bloom filter, we anticpate almost every bit to be located in an entirely different page. This means we can page fault multiple times during a single operation.

We will now analyze the hierarchical implementation. Our implementation limits bit setting and reading to a single page per insertion or query. Therefore, we access exactly one page!

Distinct page accessed (Hierarchical)
$$= 1$$

Thus, our proposed hierarchical solution page faults at most one time! Therefore, we anticpate much better performance.

3.2 False Positive Rate

In this section we will demonstrate that both implementations will have the same false positive rate in theory.

3.2.1 Standard False Positive Rate

As we have covered in class, the false positive rate of a standard bloom filter is as follows.

$$(1 - (1 - \frac{1}{m})^{nk})^k \approx (1 - e)^{kn/m} \tag{1}$$

Setting k = 7 will minimize the false positive rate, as discussed in class.

3.2.2 Hierarchical False Positive Rate

Suppose we have a allocated m bits in total chunked into bit vectors of size P and that we have to inserted n elements into our bloom filter.

We now want to compute the false positive rate of the hierarchical implementation. This can be computed by supposing the bloom filter has been filled with n elements and computing the probability that a querying a false key would result in a true response.

Assuming our hash function is uniform, any particular sub-bloom filter is anticpated to have $\frac{nl}{m/P} = \frac{nlP}{m}$ elements in it. There are m/P bloom filters and we choose l of them to check. As before k is the number of hash functions a particular bloom filter uses. Thus, the chance any one bloom filter is set is:

$$(1-(1-\frac{1}{P})^{lknP/m})^k$$

For our hierarchical implementation to return true, all l bloom filters selected must return a positive result. Thus, the false positive rate is:

$$((1-\frac{1}{P})^{lknP/m})^{kl}$$

We can use a well known identity for e^{-1} to approximate this false positive rate:

$$\approx (1 - e^{-lkn/m})^{lk}$$

Interestingly, this removes the dependence on the page size. However, it is important to note that the Euler identity is $e^{-1} = \lim_{x \to \infty} (1 - \frac{1}{P})^P$, so this identity fails as an approximation the smaller P becomes. Therefore, we should pick P to be the largest value where we expect speed ups, which would be the size of a physical page. More importantly, this formula becomes isomorphic to the false positive rate for the standard implementation. If we let k' := lk we see that our false positive rate for the hierarchical implementation is:

$$\approx (1 - e^{-k'/m})^{k'}$$

Therefore, as discussed for the standard implementation, the optimal selection for k' is 7. Therefore, either l = 1 and k = 7 or l = 7 and k = 1. Since our goal is reduce the number of pages accessed, the former is a more sensible choice.

Therefore, the best parameter selection for our hierarchical implementation would be to select 1 bloom filter the size of a bloom filter and set 7 bits in each one.

$$l = 1 : k = 7$$

3.3 Conclusion

Our theoretical work has justified the claim that we expect this implementation to be more performant while having the same positive rate as the standard implementation. Moreover, we have found the optimal parameters to use for our implementation.

4 Experiments

We will now emperically validate that the hierarchical implementation is more time efficient than the standard implementation without sacrificying accuracy. Two experiments will be conducted. First, we will **measure elapsed time as insertions scale** of each implementation. We anticpate that the hierarchical implementation will take less time than the standard implementation for any sufficiently large value of insertions. Second, we will **measure false positives as we scale bits allocated per element** of each implementation. We anticipate that the hierarchial and standard implementation will be approximately same as the theoretical false positive rate discussed before.

For both of these sections, we pseudorandomly generate keys to both insert and query. Discussion on exactly how these keys are generated is outlined in the appendix.

4.1 Comparing Time Efficiency

For this experiment, we seek to validate that the hierarchical implementation performs better than the standard implementation as the number of insertions grow. Hypothesis: The hierarchical bloom filter will take less time than the standard implementation to insert n keys for any n.

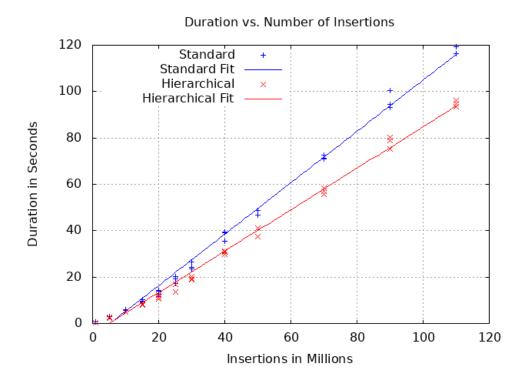
4.1.1 Experimental Settings

The experiment will run as follows. For each implementation run the following procedure:

- 1. Generate n random keys.
- 2. Generate a bloom filter of both varianets of size 10n. Use the optimal theoretical configuration for each bloom filter (i.e, k = 7, l = 1).
- 3. Time how long it takes to insert all n keys into each of the bloom filters. Report this number.
- 4. Repeat for various sizes of n.

Repeat this entire process 3 times.

4.1.2 Results



The slope of the line of best fits that are plotted are as follows whre n is millions of insertions:

- The best fit line for the standard implementation is: $t = (1.1078 \pm 0.01446) \cdot n (5.78611 \pm 0.7407)$
- The best fit line for the hierarchical implementation is: $t = (0.893221 \pm 0.01166) \cdot n (4.52759 \pm 0.597)$

We will disregard the constant as we care about how these data structures perform as input volume scales. We can compute the efficiency difference by dividing the slopes:

Hierarchical Implementation Slope/Standard Implementation Slope = Efficiency Difference

 $(0.893221 \pm 0.01166 \text{ seconds/operation})/(1.1078 \pm 0.01446 \text{ seconds/operation}) = 0.806301679 \approx 80\%$

In other words, our implementation takes 80% less time per operation to complete. Thus, for any time frame, if the standard implementation performs one operation, the hierarchical implementation is expected to perform 1/80% = 1.25 operations. Thus, our experiment shows that the hierarchical implementation performs 25% more operations per second than the standard implementation!

4.2 Comparing False Positive Rate

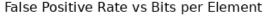
For this experiment, we seek to validate that the hierarchical implementation does not have a worse false positive rate than the standard implementation. Hypothesis: We expect them to have approximately the same false positive rate as the theoretical expectation discussed earlier.

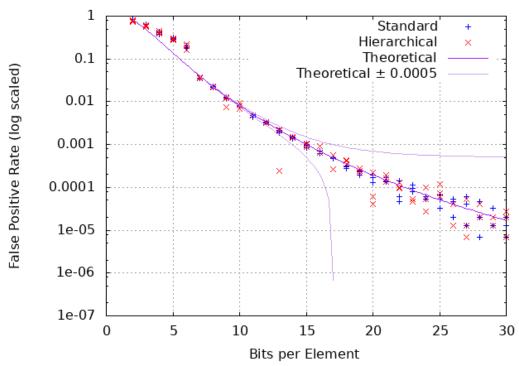
4.2.1 Experimental Settings

The experiment will run as follows.

- 1. Generate 150,000 random "insertion" keys (of length 16).
- 2. Generate 150,000 random "false" keys to query distinct from the insertion keys (of length 15).
- 3. For each implementation run the following procedure:
 - (a) Let BPE be the bits per element (e.g BPE = 1 or BPE = 10).
 - (b) Generate a bloom filter of size $150,000 \cdot BPE$.
 - (c) Insert all the 'insertion" keys and query all the "false" keys and measure how many of them the bloom filter return as being a member. Report this number.
 - (d) Repeat for various values of BPE.
- 4. Repeat this procedure again 3 times with different insertion and false keys.

4.2.2 Results





As we can see, both the standard and hierarchical implementation closely match the theoretical expectation. Both implementations do worse than theoretically expected if bloom filters are overpacked, but after Bits per Elements is greater than 6, both implementations are very close to theoretical expectation (± 0.0005) or better.

4.3 Conclusion

Our experiments have supported both of our theoretically justified hypothesises. We have demonstrated that the hierarchical implementation is more efficient than the standard implementation; it can perform 25% more operations per second! Additionally, we have verified our that our implementation is just as effective as the standard implementation.

5 Literature Survey

Note: I decided to do a literature survey of different data structures solving this problem after I explored this idea. I wanted to have the joy of discovering whether or not my idea would work on my own! Felix Putze et al. developed bloom filters with better cache efficiency and requiring less hash bits than the standard bloom filter [5]. One of their implementations is similar to mine. They discretize a bloom filter into cache size (64 bytes) chunks instead of page size chunks (4096 bytes). This leads to even faster improvements at the cost of a higher false positive rate. Remember, the smaller you make the sub-bloom filter, the worse the Euler-identity based approximation becomes. They also recompute random bit patterns: instead of setting k bits in the bloom filter, they hash once to create a mask that sets multiplem bits [5]. This saves them time while performing hash functions. The combination of these techniques gave them much better performance over the standard implementation with slight false positive rate increases. I would be interested in further exploring if some pattern masking can be applied to our bloom filter. This may require another level in our hirarchy to give us small filters that we can efficiently mask over.

Rafael P. Laufer et al. developed a "tamper proof" Generalized Bloom Filter [3] to counteract an inherent vulnerability in bloom filters. The authors call the exploitation of this vulnerability an "all one" attack, where a malicious agent floods the bloom filter with requests to set all the bits to one, artificially increasing the false positive rate. This may slow down the program that the bloom filter is employed in and marks elements as members of the set at a higher rate than it should. While this paper does not seek to solve a similar problem to our implementation, it is important to note that the hierarchy we imposed on our bloom filter may make our implementation more susceptible to this kind of attack. If a malicious agent wanted to cause a high false positive rate for a certain kind of element, they need not find a system to set all the bits. Instead, they would only have to exploit one hash function: the hash function that selects bloom filters. This may pose a security risk. Thus, for production uses, it may be wise to use a cryptographically secure hashing algorithm for this "choke point" hash function. However, we could also employ technieques discussed by Laufer, such as having multiple hash functions that set and reset bits accross the bit vector [3]. In any case, further research should be done to prevent our implementation from being exploited.

6 Conclusion

References

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