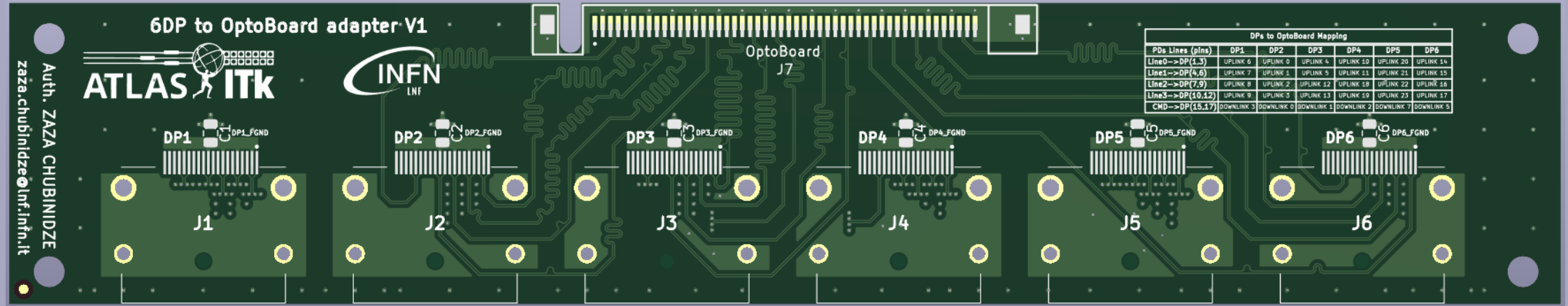


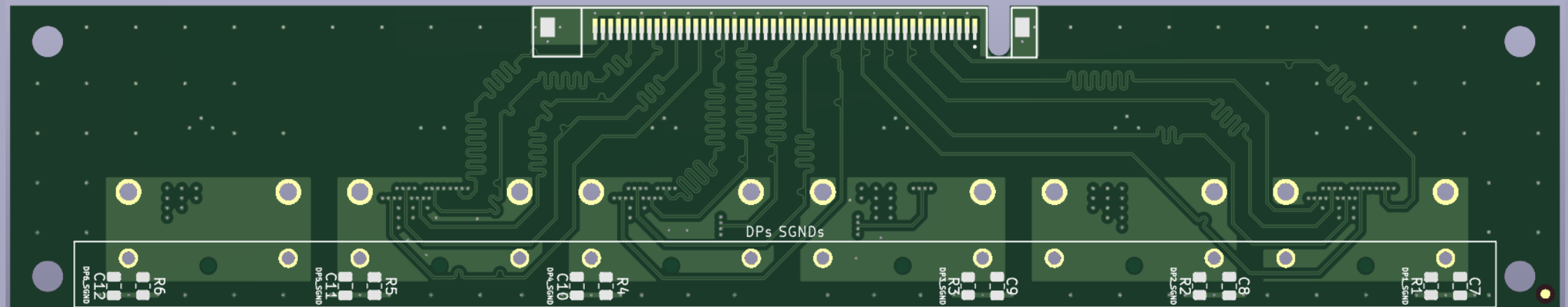
6DP to OptoBoard Adapter Card

Single board PCB Design

Top View

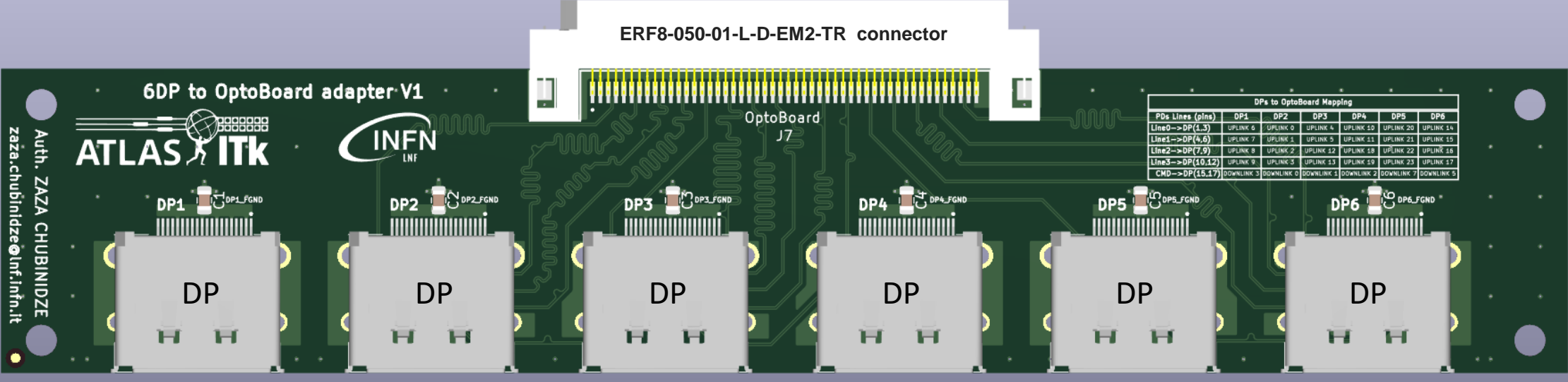


Bottom View

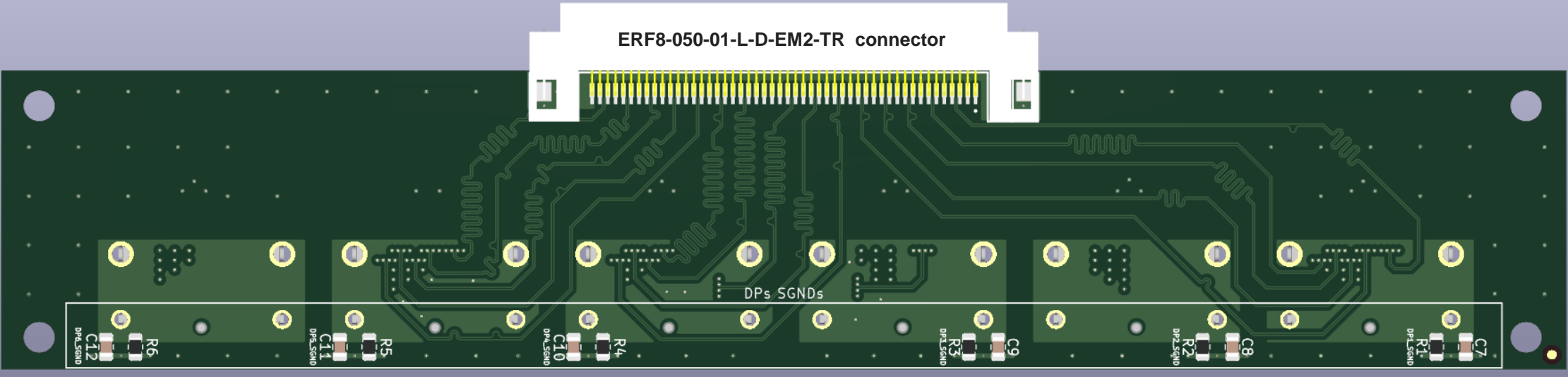


Single board Assembly

Top View



Bottom View



PCB production requirements

KiCAD PCB Layer StackUp

Copper layers: 4 ☒ Impedance controlled [Add Dielectric Layer...](#) [Remove Dielectric Layer...](#)

Layer	Id	Type	Material	Thickness	Color	Epsilon R	Loss Tan
F.Silkscreen	Top Silk Screen	Not specified			Not specified		
F.Paste	Top Solder Paste						
F.Mask	Top Solder Mask	Liquid Ink		0.02032 mm	Not specified	3.8	0
F.Cu	Copper			0.035 mm			
Dielectric 1	PrePreg	FR4		0.18034 mm	Not specified	4.6	0.02
In1.Cu	Copper			0.0152 mm			
Dielectric 2	Core	FR4		1.065 mm	Not specified	4.6	0.02
In2.Cu	Copper			0.0152 mm			
Dielectric 3	PrePreg	FR4		0.18034 mm	Not specified	4.6	0.02
B.Cu	Copper			0.035 mm			
B.Mask	Bottom Solder Mask	Liquid Ink		0.02032 mm	Not specified	3.8	0
B.Paste	Bottom Solder Paste						
B.Silkscreen	Bottom Silk Screen	Not specified			Not specified		

JLCPCB Standard

Layers: 4 Thickness: 1.6 Outer Copper Weight: 1 Inner Copper Weight: 0.5

No requirement	JLC04161H-7628	JLC04161H-3313	JLC04161H-1080
JLC04161H-7628A	JLC04161H-7628B	JLC04161H-3313A	JLC04161H-1080A
JLC04161H-2116A	JLC04161H-2116B	JLC04161H-2116C	

layer	Material Type	Thickness	
Layer	Copper	0.035mm	
Prepreg	7628*1	0.2104mm	
inner Layer	Copper	0.0152mm	1.1mm (with copper con
Core	Core	1.065mm	
inner Layer	Copper	0.0152mm	
Prepreg	7628*1	0.2104mm	
Layer	Copper	0.035mm	

Components for assembly

- C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12 --- 0.1uF SMD Capacitor (Package: C_0805)
- R1, R2, R3, R4, R5, R6 --- 10K SMD Resistance (Package: R_0805)
- J1, J2, J3, J4, J5, J6 --- DISPLAYPORT 20P SMD RA (Molex_472720024)
- J7 --- ERF8-050-01-L-D-EM2-TR