Shared-Variable Concurrency

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Parallel Composition (or Concurrency Composition)

Syntax:

$$(comm) c := ... | c_0 || c_1 | ...$$

Note we allow nested parallel composition, e.g., $(c_0; (c_1 \parallel c_2)) \parallel c_3$.

Operational Semantics:

$$\frac{(c_0,\,\sigma) \longrightarrow (c_0',\,\sigma')}{(c_0 \parallel c_1,\,\sigma) \longrightarrow (c_0' \parallel c_1,\,\sigma')} \qquad \frac{(c_1,\,\sigma) \longrightarrow (c_1',\,\sigma')}{(c_0 \parallel c_1,\,\sigma) \longrightarrow (c_0 \parallel c_1',\,\sigma')}$$

$$\frac{(c_i,\,\sigma) \longrightarrow (c_0 \parallel c_1,\,\sigma) \longrightarrow (c_0 \parallel c_1',\,\sigma')}{(c_i \parallel c_1,\,\sigma) \longrightarrow (abort,\,\sigma')} \qquad \frac{(c_i,\,\sigma) \longrightarrow (abort,\,\sigma')}{(c_0 \parallel c_1,\,\sigma) \longrightarrow (abort,\,\sigma')}$$

We have to use small-step semantics (instead of big-step semantics) to model concurrency.



Interference

Example:

$$y := x + 1;$$

 $x := y + 1$ $y := x + 1;$
 $x := x + 1$

Suppose initially $\sigma x = \sigma y = 0$. What are the possible results?

$$(1)y = 1, x = 2; (2)y = 1, x = 3; (3)y = 3, x = 3; (4)y = 2, x = 3$$

Two commands c_0 and c_1 are said to *interfere* if:

$$(\mathit{fv}(c_0) \cap \mathit{fa}(c_1)) \cup (\mathit{fv}(c_1) \cap \mathit{fa}(c_0)) \neq \emptyset$$

If c_0 and c_1 interfere, we say there are *race conditions* (or *races*) in $c_0 \parallel c_1$.

When c_0 and c_1 do not interfere, nor terminate by failure, the concurrent composition $c_0 \parallel c_1$ is determinate.



Another Example

A benign race:

```
k:=-1;

(newvar i:=0 in while i \le n \land k=-1 do

if f(i) \ge 0 then k:=i else i:=i+2

\parallel newvar i:=1 in while i \le n \land k=-1 do

if f(i) \ge 0 then k:=i else i:=i+2)
```

A problematic version:

$$k := -1$$
;
(newvar $i := 0$ in while $i \le n \land k = -1$ do
if $f(i) \ge 0$ then print (i) ; print $(f(i))$ else $i := i + 2$
 \parallel newvar $i := 1$ in while $i \le n \land k = -1$ do
if $f(i) \ge 0$ then print (i) ; print $(f(i))$ else $i := i + 2$)

Conditional Critical Regions

We could use a critical region to achieve mutual exclusive access of shared variables.

Syntax:

$$(comm)$$
 $c ::= await b then $\hat{c}$$

where \hat{c} is a sequential command (a command with no **await** and parallel composition).

Semantics:

$$\frac{\llbracket b \rrbracket_{boolexp}\,\sigma = \mathsf{true} \qquad (\hat{c},\,\sigma) \longrightarrow^* (\mathsf{Skip},\,\sigma')}{(\mathsf{await}\;b\;\mathsf{then}\;\hat{c},\,\sigma) \longrightarrow (\mathsf{Skip},\,\sigma')}$$

$$\frac{\llbracket b \rrbracket_{boolexp}\,\sigma = \mathsf{false}}{(\mathsf{await}\;b\;\mathsf{then}\;\hat{c},\,\sigma) \longrightarrow (\mathsf{Skip}\,;\,\mathsf{await}\;b\;\mathsf{then}\;\hat{c},\,\sigma)}$$

The second rule gives us a "busy-waiting" semantics. If we eliminate that rule, the thread will be blocked when the condition does not hold.

Achieving Mutual Exclusion

```
k:=-1; (newvar i:=0 in while i \le n \land k=-1 do (if f(i) \ge 0 then (await busy=0 then busy:=1); print(i); print(i); busy:= 0 else i:=i+2) \parallel newvar i:=1 in while i \le n \land k=-1 do (if i) i0 then (await i0 then i1 then i2 then i3 then i3 then i4 then i5 then i6 then i7 then i8 then i9 then i
```

Atomic Blocks

A syntactic sugar:

$$\operatorname{atomic}\{c\} \stackrel{\mathsf{def}}{=} \operatorname{await} \operatorname{true} \operatorname{then} c$$

We may also use the short-hand notation $\langle c \rangle$.

Semantics:

$$\frac{(c, \sigma) \longrightarrow^* (\mathsf{Skip}, \sigma')}{(\mathsf{atomic}\{c\}, \sigma) \longrightarrow (\mathsf{Skip}, \sigma')}$$

It gives the programmer control over the size of atomic actions.

Reynolds uses **crit** c instead of **atomic**{c}.



Deadlock

Fairness

$$k:=-1$$
;
(newvar $i:=0$ in while $k=-1$ do
if $f(i) \ge 0$ then $k:=i$ else $i:=i+2$
 \parallel newvar $i:=1$ in while $k=-1$ do
if $f(i) \ge 0$ then $k:=i$ else $i:=i+2$)

Suppose f(i) < 0 for all even number i. Then there's an infinite execution in the form of:

$$\ldots \longrightarrow (c_1 \parallel c', \sigma_1) \longrightarrow (c_2 \parallel c', \sigma_2) \longrightarrow \ldots \longrightarrow (c_n \parallel c', \sigma_n) \longrightarrow \ldots$$

An execution of concurrent processes is *unfair* if it does not terminate but, after some finite number of steps, there is an unterminated process that never makes a transition.



Fairness — More Examples

A fair execution of the following program would always terminate:

```
newvar y := 0 in (x := 0; ((while <math>y = 0 \text{ do } x := x + 1) || y := 1))
```

Stronger fairness is needed to rule out infinite execution of the following program:

```
newvar y:=0 in (x:=0; ((\text{while }y=0\text{ do }x:=1-x)\parallel(\text{await }x=1\text{ then }y:=1))
```

Trace Semantics

Can we give a denotational semantics to concurrent programs? The domain-based approach is complex. Here we use *transition traces* to model the execution of programs.

Execution of (c_0, σ_0) in a concurrent setting:

$$(c_0,\sigma_0) \longrightarrow (c_1,\sigma_0'), (c_1,\sigma_1) \longrightarrow (c_2,\sigma_1'), \ldots, (c_{n\text{-}1},\sigma_{n\text{-}1}) \longrightarrow (\text{Skip},\sigma_{n\text{-}1}')$$

The gap between σ'_i and σ_{i+1} reflects the intervention of the environment (other threads).

It could be infinite if (c_0, σ_0) does not terminate:

$$(c_0, \sigma_0) \longrightarrow (c_1, \sigma_1), (c_1, \sigma_1') \longrightarrow (c_2, \sigma_2), \dots$$

We omit the commands to get a transition trace:

$$(\sigma_0, \sigma'_0), (\sigma_1, \sigma'_1), \dots, (\sigma_{n-1}, \sigma'_{n-1})$$

 $(\sigma_0, \sigma'_0), (\sigma_1, \sigma'_1), \dots$

or

Interference-Free Traces

A trace
$$(\sigma_0, \sigma'_0), (\sigma_1, \sigma'_1), \dots, (\sigma_{n-1}, \sigma'_{n-1})$$
 (or $(\sigma_0, \sigma'_0), (\sigma_1, \sigma'_1), \dots$) is said to be *Interference-Free* iff $\forall i. \sigma'_i = \sigma_{i+1}$.

Operations over Traces

We use τ to represent individual transition traces, and $\mathcal T$ for a set of traces.

Note the difference between \mathcal{T}^* and \mathcal{T}^{ω} .



Trace Semantics — First Try

$$\mathcal{T}[\![x := e]\!] \qquad \qquad = \left\{ (\sigma, \sigma') \mid \sigma' = \sigma \{x \leadsto [\![e]\!]_{intexp} \sigma \} \right\}$$

$$\mathcal{T}[\![Skip]\!] \qquad \qquad = \left\{ (\sigma, \sigma) \mid \sigma \in \Sigma \right\}$$

$$\mathcal{T}[\![c_0; c_1]\!] \qquad \qquad = \mathcal{T}[\![c_0]\!]; \mathcal{T}[\![c_1]\!]$$

$$\mathcal{T}[\![if\ b\ then\ c_1\ else\ c_2]\!] = (\mathcal{B}[\![b]\!]; \mathcal{T}[\![c_1]\!]) \cup (\mathcal{B}[\![\neg b]\!]; \mathcal{T}[\![c_2]\!])$$

$$\text{where}\ \mathcal{B}[\![b]\!] = \left\{ (\sigma, \sigma) \mid [\![b]\!]_{boolexp} \sigma = \mathbf{true} \right\}$$

$$\mathcal{T}[\![while\ b\ do\ c]\!] \qquad = ((\mathcal{B}[\![b]\!]; \mathcal{T}[\![c]\!])^*; \mathcal{B}[\![\neg b]\!]) \cup (\mathcal{B}[\![b]\!]; \mathcal{T}[\![c]\!])^\omega$$

Trace Semantics (cont'd)

How to give semantics to **newvar**x := e **in** c?

Definition: local- $global(x, e, \tau, \hat{\tau})$ iff the following are true (suppose $\tau = (\sigma_0, \sigma_0'), (\sigma_1, \sigma_1'), \ldots$ and $\hat{\tau} = (\hat{\sigma_0}, \hat{\sigma_0'}), (\hat{\sigma_1}, \hat{\sigma_1'}), \ldots$):

- they have the same length;
- for all $x' \neq x$, $\sigma_i x' = \hat{\sigma}_i x'$ and $\sigma'_i x' = \hat{\sigma}'_i x'$;
- for all i, $\sigma_{i+1} x = \sigma'_i x$;
- for all i, $\hat{\sigma}_i x = \hat{\sigma}'_i x$;
- $\sigma_0 x = \llbracket e \rrbracket_{intexp} \hat{\sigma_0}$.

 $\mathcal{T}[\![$ **newvar** $x := e \text{ in } c]\!] = \{\hat{\tau} \mid \tau \in \mathcal{T}[\![c]\!] \text{ and } \textit{local-global}(x, e, \tau, \hat{\tau})\}$



Fair Interleaving

We view a trace τ as a function mapping indices to the corresponding transitions.

Definition: $fair-merge(\tau_1, \tau_2, \tau)$ iff there exist functions $f \in dom(\tau_1) \to dom(\tau)$ and $g \in dom(\tau_2) \to dom(\tau)$ such that the following are true:

f and g are monotone injections:

$$i < j \Longrightarrow (f i < f j) \land (g i < g j)$$

- $\operatorname{ran}(f) \cap \operatorname{ran}(g) = \emptyset$ and $\operatorname{ran}(f) \cup \operatorname{ran}(g) = \operatorname{dom}(\tau)$;
- $\bullet \forall i. \, \tau_1(i) = \tau(f \, i) \wedge \tau_2(i) = \tau(g \, i)$

Then
$$\mathcal{T}_{fair}[\![c_1 \parallel c_2]\!] = \{\tau \mid \exists \tau_1 \in \mathcal{T}_{fair}[\![c_1]\!], \tau_2 \in \mathcal{T}_{fair}[\![c_2]\!]. \text{ fair-merge}(\tau_1, \tau_2, \tau)\}$$



Unfair Interleaving

Definition: *unfair-merge*(τ_1, τ_2, τ) if one of the following are true:

- fair-merge (τ_1, τ_2, τ)
- τ_1 is infinite and there exist τ_2' and τ_2'' such that $\tau_2 = \tau_2' + + \tau_2''$ and fair-merge(τ_1, τ_2', τ)
- τ_2 is infinite, and there exist τ_1' and τ_1'' such that $\tau_1 = \tau_1' + \tau_1''$ and $fair-merge(\tau_1', \tau_2, \tau)$

$$\begin{split} \mathcal{T}_{\textit{unfair}} \llbracket c_1 \parallel c_2 \rrbracket \\ &= \{\tau \mid \exists \tau_1 \in \mathcal{T}_{\textit{unfair}} \llbracket c_1 \rrbracket, \tau_2 \in \mathcal{T}_{\textit{unfair}} \llbracket c_2 \rrbracket. \, \textit{unfair-merge}(\tau_1, \tau_2, \tau) \} \end{split}$$

Trace Semantics for await

```
\mathcal{T}[\![\mathbf{await}\ b\ \mathbf{then}\ c]\!] = \\ (\mathcal{B}[\![\neg b]\!]; \mathcal{T}[\![\mathbf{Skip}]\!])^*; \\ \{(\sigma,\sigma')\mid [\![b]\!]_{boolexp}\ \sigma = \mathbf{true} \\ \text{and there exist } \sigma'_0,\sigma_1,\sigma'_1,\ldots,\sigma_n \text{ such that } \\ (\sigma,\sigma'_0),(\sigma_1,\sigma'_1),\ldots,(\sigma_n,\sigma')\in\mathcal{T}[\![c]\!]\} \\ \cup (\mathcal{B}[\![\neg b]\!]; \mathcal{T}[\![\mathbf{Skip}]\!])^\omega
```

Trace Semantics (cont'd)

The semantics is equivalent to the following:

$$\mathcal{T}\llbracket c \rrbracket \stackrel{\text{def}}{=} \\ \{(\sigma_0, \sigma'_0), \dots, (\sigma_n, \sigma'_n) \mid \\ \text{there exist } c_0, \dots, c_n \text{ such that } c_0 = c, \\ \forall i \in [0, n-1]. \ (c_i, \sigma_i) \longrightarrow (c_{i+1}, \sigma'_i), \\ \text{and } (c_n, \sigma_n) \longrightarrow (\mathbf{Skip}, \sigma'_n)\} \\ \cup \{(\sigma_0, \sigma'_0), (\sigma_1, \sigma'_1), \dots \mid \\ \text{there exist } c_0, c_1, \dots \text{ such that } c_0 = c, \\ \text{and for all } i, (c_i, \sigma_i) \longrightarrow (c_{i+1}, \sigma'_i)\}$$

Problem with This Semantics

The trace semantics we just defined is not abstract enough. It distinguishes the following programs (which should be viewed equivalent):

$$x := x+1$$

 $x := x+1$; Skip
Skip; $x := x+1$

Also consider the following two programs:

$$x := x+1$$
; $x := x+1$
($x := x+1$; $x := x+1$) choice $x := x+2$

Stuttering and Mumbling

Stuttering and Mumbling (cont'd)

The new semantics $\mathcal{T}^*[\![c]\!]$ is equivalent to the following:

```
 \begin{split} \mathcal{T} \llbracket c \rrbracket &\stackrel{\text{def}}{=} \\ & \{ (\sigma_0, \sigma'_0), \dots, (\sigma_n, \sigma'_n) \mid \\ & \text{there exist } c_0, \dots, c_n \text{ such that } c_0 = c, \\ & \forall i \in [0, n-1]. \ (c_i, \sigma_i) \longrightarrow^* (c_{i+1}, \sigma'_i), \\ & \text{and } (c_n, \sigma_n) \longrightarrow^* (\mathbf{Skip}, \sigma'_n) \} \\ & \cup \{ (\sigma_0, \sigma'_0), (\sigma_1, \sigma'_1), \dots \mid \\ & \text{there exist } c_0, c_1, \dots \text{ such that } c_0 = c, \\ & \forall i. \ (c_i, \sigma_i) \longrightarrow^* (c_{i+1}, \sigma'_i), \\ & \text{and for infinitely many } i \geq 0, (c_i, \sigma_i) \longrightarrow^+ (c_{i+1}, \sigma'_i) \} \end{split}
```