```
[inside hotspot] 汇编模板解释器(Template Interpreter)和字节码执行
    1.模板解释器
    hotspot解释器模块(hotspot\src\share\vm\interpreter)有两个实现:基于C++的解释器和基于汇编的模板解释器。hotspot默认使用比较快的模板解释器。
6
    C++解释器 = bytecodeInterpreter* + cppInterpreter*
7
     模板解释器 = templateTable* + templateInterpreter*
8
    它们前者负责字节码的解释,后者负责解释器的运行时,共同完成解释功能。这里我们只关注模板解释器。
    模板解释器又分为三个组成部分:
10
11
    templateInterpreterGenerator 解释器生成器
13
    templateTable 字节码实现
    templateInterpreter 解释器
14
     可能看起来很奇怪,为什么有一个解释器生成器和字节码实现。进入解释器实现:
15
16
    class TemplateInterpreter: public AbstractInterpreter {
      friend class VMStructs;
18
      friend class InterpreterMacroAssembler;
19
      friend class TemplateInterpreterGenerator;
      friend class TemplateTable;
21
      friend class CodeCacheExtensions;
22
      // friend class Interpreter;
     public:
24
      enum MoreConstants {
25
        number_of_return_entries = number_of_states,
                                                                // number of return entry points
        number_of_deopt_entries = number_of_states,
                                                                // number of deoptimization entry points
27
28
        number_of_return_addrs
                                = number_of_states
                                                                // number of return addresses
29
30
     protected:
32
33
                       _throw_ArrayIndexOutOfBoundsException_entry;
      static address
34
      static address
                       _throw_ArrayStoreException_entry;
35
      static address
                       _throw_ArithmeticException_entry;
36
      static address
                       throw ClassCastException entry;
37
      static address
                       _throw_NullPointerException_entry;
      static address
38
                       _throw_exception_entry;
39
40
      static address
                       _throw_StackOverflowError_entry;
41
42
      static address
                       _remove_activation_entry;
                                                               // continuation address if an exception is not handled by current frame
43
    #ifdef HOTSWAP
44
      static address
                       _remove_activation_preserving_args_entry; // continuation address when current frame is being popped
45
    #endif // HOTSWAP
46
47
    #ifndef PRODUCT
48
      static EntryPoint _trace_code;
49
    #endif // !PRODUCT
50
      static EntryPoint _return_entry[number_of_return_entries];
                                                                // entry points to return to from a call
51
      static EntryPoint _earlyret_entry;
                                                                // entry point to return early from a call
52
      static EntryPoint _deopt_entry[number_of_deopt_entries];
                                                                // entry points to return to from a deoptimization
53
      static EntryPoint _continuation_entry;
54
      static EntryPoint _safept_entry;
56
      static address _invoke_return_entry[number_of_return_addrs];
                                                                        // for invokestatic, invokespecial, invokevirtual return entries
      static address _invokeinterface_return_entry[number_of_return_addrs]; // for invokeinterface return entries
58
      static address _invokedynamic_return_entry[number_of_return_addrs];
                                                                        // for invokedynamic return entries
59
                                                                               dispatch table (used by the interpreter for dispatch)
60
      static DispatchTable _active_table;
                                                                // the active
61
      static DispatchTable _normal_table;
                                                                // the normal
                                                                               dispatch table (used to set the active table in normal mode)
62
      static DispatchTable _safept_table;
                                                                // the safepoint dispatch table (used to set the active table for safepoints)
63
                          _wentry_point[DispatchTable::length];
                                                                // wide instructions only (vtos tosca always)
      static address
65
66
     public:
67
68
      static int InterpreterCodeSize;
69
70
     里面很多address变量,EntryPoint是一个address数组,DispatchTable也是。
71
     模板解释器就是由一系列例程(routine)组成的,即address变量,它们每个都表示一个例程的入口地址,比如异常处理例程,invoke指令例程,用于gc的
     safepoint例程...
72
     举个形象的例子,我们都知道字节码文件长这样:
    public void f();
                                                                                                0: aload_0
    1: invokespecial #5
                                       // Method A.f:()V
    4: getstatic
                    #2
                                       // Field java/lang/System.out:Ljava/io/PrintStream;
77
    7: ldc
                    #6
                                       // String ff
78
    9: invokevirtual #4
                                       // Method java/io/PrintStream.println:(Ljava/lang/String;)V
80
    如果要让我们写解释器,可能基本上就是一个循环里面switch,根据不同opcode派发到不同例程,例程的代码都是一样的模板代码,对aload 0的处理永远是取局
     部变量槽0的数据放到栈顶,那么完全可以在switch派发字节码前准备好这些模板代码,templateInterpreterGenerator就是做的这件事,它的generate_all()函
     数初始化了所有的例程:
    void TemplateInterpreterGenerator::generate_all() {
```

```
83
        // 设置slow_signature_handler例程
 84
        { CodeletMark cm(_masm, "slow signature handler");
 85
          AbstractInterpreter::_slow_signature_handler = generate_slow_signature_handler();
 86
        // 设置error exit例程
 87
 88
        { CodeletMark cm(_masm, "error exits");
 29
          _unimplemented_bytecode
                                    = generate_error_exit("unimplemented bytecode");
 90
          __illegal_bytecode_sequence = generate_error_exit("illegal bytecode sequence - method not verified");
 91
 92
 93
 94
      另外,既然已经涉及到机器码了,单独的templateInterpreterGenerator显然是不能完成这件事的,它还需要配合
 95
       hotspot\src\cpu\x86\vm\templateInterpreterGenerator_x86.cpp&&hotspot\src\cpu\x86\vm\templateInterpreterGenerator_x86_64.cpp一起做事(我的机器
       是x86+windows)。
 96
 97
      使用-XX:+UnlockDiagnosticVMOptions -XX:+PrintInterpreter -XX:+LogCompilation -XX:LogFile=file.log保存结果到文件,可以查看生成的这些例程。
 98
       随便举个例子,模板解释器特殊处理java.lang.Math里的很多数学函数,使用它们不需要建立通常意义的java栈帧,且使用sse指令可以得到极大的性能提升:
 99
100
      // hotspot\src\cpu\x86\vm\templateInterpreterGenerator_x86_64.cpp
101
      address TemplateInterpreterGenerator::generate_math_entry(AbstractInterpreter::MethodKind kind) {
102
        // rbx.: Method<sup>3</sup>
103
        // rcx: scratrch
104
        // r13: sender sp
105
        if (!InlineIntrinsics) return NULL; // Generate a vanilla entry
106
        address entry_point = _
                               pc();
107
108
        if (kind == Interpreter::java_lang_math_fmaD) {
109
          if (!UseFMA) {
110
            return NULL; // Generate a vanilla entry
111
          __ movdbl(xmm0, Address(rsp, wordSize));
112
113
          __ movdbl(xmm1, Address(rsp, 3 * wordSize));
          __ movdbl(xmm2, Address(rsp, 5 * wordSize));
114
115
             fmad(xmm0, xmm1, xmm2, xmm0);
116
        } else if (kind == Interpreter::java_lang_math_fmaF) {
117
          if (!UseFMA) {
118
            return NULL; // Generate a vanilla entry
          }
119
          __ movflt(xmm0, Address(rsp, wordSize));
121
          __ movflt(xmm1, Address(rsp, 2 * wordSize));
          __ movflt(xmm2, Address(rsp, 3 * wordSize));
122
             fmaf(xmm0, xmm1, xmm2, xmm0);
123
        } else if (kind == Interpreter::java_lang_math_sqrt) {
124
125
             sqrtsd(xmm0, Address(rsp, wordSize));
126
        } else if (kind == Interpreter::java_lang_math_exp) {
127
             movdbl(xmm0, Address(rsp, wordSize));
          if (StubRoutines::dexp() != NULL) {
128
129
               call(RuntimeAddress(CAST_FROM_FN_PTR(address, StubRoutines::dexp())));
          } else {
130
            __ call_VM_leaf0(CAST_FROM_FN_PTR(address, SharedRuntime::dexp));
131
132
133
        } else if (kind == Interpreter::java_lang_math_log) {
134
             movdbl(xmm0, Address(rsp, wordSize));
          if (StubRoutines::dlog() != NULL) {
135
               call(RuntimeAddress(CAST_FROM_FN_PTR(address, StubRoutines::dlog())));
136
137
          } else {
            __ call_VM_leaf0(CAST_FROM_FN_PTR(address, SharedRuntime::dlog));
138
139
140
        } else if (kind == Interpreter::java lang math log10) {
141
             movdbl(xmm0, Address(rsp, wordSize));
142
          if (StubRoutines::dlog10() != NULL) {
143
               call(RuntimeAddress(CAST_FROM_FN_PTR(address, StubRoutines::dlog10())));
144
          } else {
             _ call_VM_leaf0(CAST_FROM_FN_PTR(address, SharedRuntime::dlog10));
145
146
147
        } else if (kind == Interpreter::java_lang_math_sin) {
148
             movdbl(xmm0, Address(rsp, wordSize));
149
          if (StubRoutines::dsin() != NULL) {
150
               call(RuntimeAddress(CAST_FROM_FN_PTR(address, StubRoutines::dsin())));
151
          } else {
152
               call_VM_leaf0(CAST_FROM_FN_PTR(address, SharedRuntime::dsin));
154
        } else if (kind == Interpreter::java_lang_math_cos) {
155
             movdbl(xmm0, Address(rsp, wordSize));
          if (StubRoutines::dcos() != NULL) {
157
               call(RuntimeAddress(CAST FROM FN PTR(address, StubRoutines::dcos())));
          } else {
158
            __ call_VM_leaf0(CAST_FROM_FN_PTR(address, SharedRuntime::dcos));
159
160
161
        } else if (kind == Interpreter::java_lang_math_pow) {
          __ movdbl(xmm1, Address(rsp, wordSize));
162
             movdbl(xmm0, Address(rsp, 3 * wordSize));
163
          if (StubRoutines::dpow() != NULL) {
165
               call(RuntimeAddress(CAST_FROM_FN_PTR(address, StubRoutines::dpow())));
166
          } else {
            __ call_VM_leaf0(CAST_FROM_FN_PTR(address, SharedRuntime::dpow));
```

```
168
169
       } else if (kind == Interpreter::java_lang_math_tan) {
170
           movdbl(xmm0, Address(rsp, wordSize));
171
         if (StubRoutines::dtan() != NULL) {
172
              call(RuntimeAddress(CAST_FROM_FN_PTR(address, StubRoutines::dtan())));
         } else {
173
           __ call_VM_leaf0(CAST_FROM_FN_PTR(address, SharedRuntime::dtan));
174
175
176
       } else {
           fld_d(Address(rsp, wordSize));
177
178
         switch (kind) {
179
         case Interpreter::java_lang_math_abs:
180
           __ fabs();
           break;
181
182
         default:
183
           ShouldNotReachHere();
185
            subptr(rsp, 2*wordSize);
186
         // Round to 64bit precision
         __ fstp_d(Address(rsp, 0));
188
189
         __ movdbl(xmm0, Address(rsp, 0));
            addptr(rsp, 2*wordSize);
190
191
192
193
         pop(rax);
       __ mov(rsp, r13);
194
       __ jmp(rax);
195
196
197
       return entry_point;
198
199
      我们关注java.lang.math.Pow()方法,加上-XX:+PrintInterpreter查看生成的例程:
200
201
      else if (kind == Interpreter::java_lang_math_pow) {
         __ movdbl(xmm1, Address(rsp, wordSize));
202
203
            movdbl(xmm0, Address(rsp, 3 * wordSize));
204
         if (StubRoutines::dpow() != NULL) {
205
             call(RuntimeAddress(CAST_FROM_FN_PTR(address, StubRoutines::dpow())));
206
         } else {
207
           __ call_VM_leaf0(CAST_FROM_FN_PTR(address, SharedRuntime::dpow));
208
209
       }
210
     method entry point (kind = java_lang_math_pow) [0x000001bcb62feaa0, 0x000001bcb62feac0] 32 bytes
211
213
       0x000001bcb62feaa0: vmovsd 0x8(%rsp),%xmm1
        0x000001bcb62feaa6: vmovsd 0x18(%rsp),%xmm0
214
215
        0x000001bcb62feaac: callq 0x000001bcb62f19d0
216
       0x000001bcb62feab1: pop
                                 %rax
        0x000001bcb62feab2: mov
                                 %r13,%rsp
217
218
       0x000001bcb62feab5: jmpq
                                 *%rax
219
        0x000001bcb62feab7: nop
220
        0x000001bcb62feab8: add
                                 %al,(%rax)
221
       0x000001bcb62feaba: add
                                 %al,(%rax)
222
        0x000001bcb62feabc: add
                                 %al,(%rax)
223
       0x000001bcb62feabe: add
                                 %al,(%rax)
224
      callq会调用hotspot\src\cpu\x86\vm\stubGenerator_x86_64.cpp的address generate_libmPow(),感兴趣的可以去看一下,这里就不展开了。
225
226
      现在我们知道了模板解释器其实是由一堆例程构成的,但是,字节码的例程的呢?看看上面TemplateInterpreter的类定义,有个static DispatchTable
      _active_table;,它就是我们要找的东西了。具体来说templateInterpreterGenerator会调用TemplateInterpreterGenerator::set_entry_points()为每个字节码
      设置例程,该例程通过templateTable::template_for()获得。同样,这些代码需要关心cpu架构,所以自己每个字节码的例程也是由
      hotspot\src\cpu\x86\vm\templateTable_x86.cpp+templateTable共同完成的。
       字节码太多了,这里也随便举个例子,考虑istore,它负责将栈顶数据出栈并存放到当前方法的局部变量表,实现如下:
228
229
230
      void TemplateTable::istore() {
        transition(itos, vtos);
       locals_index(rbx);
       __ movl(iaddress(rbx), rax);
233
234
235
      合情合理的实现
      等等,当使用-XX:+PrintInterpreter查看istore的合情合理的例程时却得到了一大堆汇编:
238
239
240
     istore 54 istore [0x00000192d1972ba0, 0x00000192d1972c00] 96 bytes
241
242
        0x00000192d1972ba0: mov
                                 (%rsp),%eax
243
       0x00000192d1972ba3: add
                                 $0x8,%rsp
244
        0x00000192d1972ba7: movzbl 0x1(%r13),%ebx
        0x00000192d1972bac: neg
246
        0x00000192d1972baf: mov
                                 %eax,(%r14,%rbx,8)
247
        0x00000192d1972bb3: movzbl 0x2(%r13),%ebx
248
        0x00000192d1972bb8: add
                                 $0x2,%r13
249
        0x00000192d1972bbc: movabs $0x7fffd56e0fa0,%r10
        0x00000192d1972bc6: jmpq
                                *(%r10,%rbx,8)
```

```
0x00000192d1972bca: mov
251
                                 (%rsp),%eax
       0x00000192d1972bcd: add
                                 $0x8,%rsp
253
        0x00000192d1972bd1: movzwl 0x2(%r13),%ebx
254
        0x00000192d1972bd6: bswap %ebx
255
       0x00000192d1972bd8: shr
                                 $0x10,%ebx
        0x00000192d1972bdb: neg
256
                                 %rbx
       0x00000192d1972bde: mov
                                 %eax,(%r14,%rbx,8)
258
        0x00000192d1972be2: movzbl 0x4(%r13),%ebx
259
        0x00000192d1972be7: add
                                 $0x4,%r13
260
        0x00000192d1972beb: movabs $0x7fffd56e0fa0, %r10
261
        0x00000192d1972bf5: jmpq
                                 *(%r10,%rbx,8)
                                 0x0(%rax)
262
        0x00000192d1972bf9: nopl
                         %eax,(%r14,%rbx,8)对应__ movl(iaddress(n), rax);,但是多出来的代码怎么回事。
263
      虽然勉强能看出mov
264
       要回答这个问题,需要点其他知识。
265
266
      之前提到
267
      templateInterpreterGenerator会调用TemplateInterpreterGenerator::set_entry_points()为每个字节码设置例程
269
270
      可以从set entry points出发看看它为istore做了什么特殊的事情:
271
272
273
274
        // 指令是否存在
275
        if (Bytecodes::is_defined(code)) {
276
         Template* t = TemplateTable::template_for(code);
         assert(t->is_valid(), "just checking");
277
278
         set_short_entry_points(t, bep, cep, sep, aep, iep, lep, fep, dep, vep);
279
        .
// 指令是否可以扩宽,即wide
280
281
        if (Bytecodes::wide_is_defined(code)) {
         Template* t = TemplateTable::template_for_wide(code);
282
283
         assert(t->is_valid(), "just checking");
284
          set_wide_entry_point(t, wep);
285
       }
286
287
288
      中间有一句话:
289
      Template* t = TemplateTable::template_for(code);
291
      从模板表中的查找Bytecodes::Code常量得到的是一个Template, Template描述了一个指定的字节码对应的代码的一些属性
292
293
      // A Template describes the properties of a code template for a given bytecode
294
      // and provides a generator to generate the code template.
295
296
297
      // hotspot\src\share\vm\utilities\globalDefinitions.hpp
298
      // TosState用来描述一个字节码或者方法执行前后的状态。
299
     enum TosState {
                            // describes the tos cache contents
300
       htos = 0,
                            // byte, bool tos cached
                            // byte, bool tos cached
301
       ztos = 1,
302
       ctos = 2,
                            // char tos cached
303
        stos = 3,
                            // short tos cached
304
       itos = 4,
                            // int tos cached
       ltos = 5,
                            // long tos cached
305
       ftos = 6,
                            // float tos cached
306
307
       dtos = 7,
                            // double tos cached
308
        atos = 8,
                            // object cached
       vtos = 9,
309
                            // tos not cached
310
       number_of_states,
311
       ilgl
                            // illegal state: should not occur
312
313
      // hotspot\src\share\vm\interpreter\templateTable.hpp
      class Template VALUE_OBJ_CLASS_SPEC {
314
315
      private:
316
       enum Flags {
                                                    // 是否需要字节码指针(bcp)?
317
         uses_bcp_bit,
318
         does_dispatch_bit,
                                                    // 是否需要dispatch?
                                                    // 是否调用了虚拟机方法?
319
         calls_vm_bit,
320
         wide bit
                                                    // 能否扩宽,即加wide
321
                                                   // 字节码代码生成器, 其实是一个函数指针
323
        typedef void (*generator)(int arg);
324
                                                    // 就是↑描述的flag
                 flags;
        TosState _tos_in;
                                                    // 执行字节码前的栈顶缓存状态
326
                                                    // 执行字节码的栈顶缓存状态
327
        TosState _tos_out;
328
        generator _gen;
                                                    // 字节码代码生成器
                                                    // 字节码代码生成器参数
329
                 _arg;
330
      然后找到istore对应的模板定义:
331
332
        //hotspot\src\share\vm\interpreter\templateTable.cpp
333
      void TemplateTable::initialize() {
334
335
                                           interpr. templates
       // Java spec bytecodes
                                           ubcp|disp|clvm|iswd in
                                                                     out
                                                                           generator
                                                                                                argument
```

```
337
                                            , ubcp
        def(Bytecodes::_istore
                                                       |clvm|
                                                                _, itos, vtos, istore
                                                                                                                 );
        def(Bytecodes::_lstore
                                           , ubcp
338
                                                                _, ltos, vtos, lstore
                                                                                                                 );
339
        def(Bytecodes::_fstore
                                           , ubcp
                                                              __, ftos, vtos, fstore
                                                                                                                 );
        def(Bytecodes::_dstore
                                            , ubcp
                                                                 , dtos, vtos, dstore
                                                                                                                 );
);
340
                                                       clvm
                                                                _, vtos, vtos, astore
341
        def(Bytecodes:: astore
                                            , ubcp
342
343
        // wide Java spec bytecodes
344
        def(Bytecodes::_istore
                                                        ____|iswd, vtos, vtos, wide_istore
                                           , ubcp
                                            , ubcp
345
        def(Bytecodes::_lstore
                                                            iswd, vtos, vtos, wide_lstore
                                                                                                                 );
346
        def(Bytecodes:: fstore
                                           , ubcp
                                                                                                                 );
                                                            iswd, vtos, vtos, wide_fstore
                                           , ubcpl
347
        def(Bytecodes::_dstore
                                                            |iswd, vtos, vtos, wide_dstore
                                                                                                                 );
348
        def(Bytecodes::_astore
                                           , ubcp
                                                            |iswd, vtos, vtos, wide_astore
                                                                                                                 );
349
        def(Bytecodes::_iinc
                                           , ubcp
                                                            |iswd, vtos, vtos, wide_iinc
                                                                                                                 );
350
        def(Bytecodes::_ret
                                            , ubcp|disp|
                                                            iswd, vtos, vtos, wide_ret
                                                                                                                 );
                                            , ubcp|disp|clvm|
                                                                                                                 );
351
        def(Bytecodes:: breakpoint
                                                                _, vtos, vtos, _breakpoint
352
353
354
355
      这里定义的意思就是, istore使用无参数的生成器istore函数生成例程, 这个生成器正是之前提到的那个很短的汇编代码:
356
357
      void TemplateTable::istore() {
358
        transition(itos, vtos);
359
        locals_index(rbx);
360
        __ movl(iaddress(rbx), rax);
361
      ubcp表示使用字节码指针,所谓字节码指针指的是该字节码的操作数是否存在于字节码里面,一图胜千言:
362
363
365
      Operation
                 store int into local variable
                 istore
366
      Format
367
                 index
368
369
      istore的index紧跟在istore(0x36)后面,所以istore需要移动字节码指针以获取index。
371
      istore还规定执行前栈顶缓存int值(itos),执行后不缓存(vtos),且istore还有一个wide版本,这个版本使用两个字节的index。
372
373
      有了这些信息,可以试着解释多出的汇编是怎么回事了。set_entry_points()为istore和wide版本的istore生成代码,
374
       我们选择普通版本的istore解释,wide版本的依样画葫芦即可。它又进一步调用了set_short_entry_points():
375
376
      void TemplateInterpreterGenerator::set_entry_points(Bytecodes::Code code) {
377
378
        if (Bytecodes::is_defined(code)) {
379
          Template* t = TemplateTable::template_for(code);
          assert(t->is_valid(), "just checking");
380
          set_short_entry_points(t, bep, cep, sep, aep, iep, lep, fep, dep, vep);
381
382
383
        if (Bytecodes::wide_is_defined(code)) {
384
          Template* t = TemplateTable::template_for_wide(code);
          assert(t->is_valid(), "just checking");
385
386
          set_wide_entry_point(t, wep);
387
        }
388
389
      }
390
      void TemplateInterpreterGenerator::set_short_entry_points(Template* t, address& bep, address& sep, address& aep, address& iep,
391
      address& lep, address& fep, address& dep, address& vep) {
392
        assert(t->is_valid(), "template must exist");
393
        switch (t->tos_in()) {
394
         case btos:
395
          case ztos:
396
          case ctos:
397
          case stos:
398
            ShouldNotReachHere(); // btos/ctos/stos should use itos.
            break:
          case atos: vep = __pc(); __pop(atos); aep = __pc(); generate_and_dispatch(t); break;
case itos: vep = __pc(); __pop(itos); iep = __pc(); generate_and_dispatch(t); break;
400
401
402
          case ltos: vep = __ pc(); __ pop(ltos); lep = __ pc(); generate_and_dispatch(t); break;
          case ftos: vep = __pc(); __pop(ftos); fep = __pc(); generate_and_dispatch(t); break;
case dtos: vep = __pc(); __pop(dtos); dep = __pc(); generate_and_dispatch(t); break;
403
404
          case vtos: set_vtos_entry_points(t, bep, cep, sep, aep, iep, lep, fep, dep, vep);
405
                                                                                               break:
406
          default : ShouldNotReachHere():
                                                                                          break;
407
        }
408
409
      set_short_entry_points会根据该指令执行前是否需要栈顶缓存pop数据,istore使用了itos缓存,所以需要pop:
410
411
      // hotspot\src\cpu\x86\vm\interp masm x86.cpps
      void InterpreterMacroAssembler::pop_i(Register r) {
412
413
        // XXX can't use pop currently, upper half non clean
414
        movl(r, Address(rsp, 0));
415
        addptr(rsp, wordSize);
416
417
      稍微需要注意的是这里说的pop是一个弹出的概念,实际生成的代码是mov,试着解释那一大堆汇编:
418
       mov指令
419
420
      istore 54 istore [0x00000192d1972ba0, 0x00000192d1972c00] 96 bytes
```

```
422
        : 获取栈顶int缓存
423
        0x00000192d1972ba0: mov
                                   (%rsp),%eax
424
        0x00000192d1972ba3: add
                                   $0x8,%rsp
425
        0x00000192d1972ba7: movzbl 0x1(%r13),%ebx
426
427
        0x00000192d1972bac: neg
                                   %rbx
428
        0x00000192d1972baf: mov
                                   %eax,(%r14,%rbx,8)
429
        0x00000192d1972bb3: movzbl 0x2(%r13),%ebx
430
        0x00000192d1972bb8: add
                                   $0x2,%r13
431
        0x00000192d1972bbc: movabs $0x7fffd56e0fa0,%r10
432
        0x00000192d1972bc6: jmpq
                                   *(%r10,%rbx,8)
433
        0x00000192d1972bca: mov
                                   (%rsp),%eax
434
        0x00000192d1972bcd: add
                                   $0x8,%rsp
435
        0x00000192d1972bd1: movzwl 0x2(%r13),%ebx
436
        0x00000192d1972bd6: bswap
                                   %ebx
437
        0x00000192d1972bd8: shr
                                   $0x10,%ebx
438
        0x00000192d1972bdb: neg
                                   %rbx
439
        0x00000192d1972bde: mov
                                   %eax,(%r14,%rbx,8)
440
        0x00000192d1972be2: movzbl 0x4(%r13),%ebx
441
        0x00000192d1972be7: add
                                   $0x4,%r13
442
        0x00000192d1972beb: movabs $0x7fffd56e0fa0,%r10
                                   *(%r10,%rbx,8)
443
        0x00000192d1972bf5: jmpq
444
        0x00000192d1972bf9: nopl
                                   0x0(%rax)
445
      接着generate_and_dispatch()又分为执行前(dispatch_prolog)+执行字节码(t->generate())+执行后三部分(dispatch_epilog):
446
447
      void TemplateInterpreterGenerator::generate_and_dispatch(Template* t, TosState tos_out) {
448
449
        int step = 0;
450
        if (!t->does_dispatch()) {
451
          step = t->is_wide() ? Bytecodes::wide_length_for(t->bytecode()) : Bytecodes::length_for(t->bytecode());
452
          if (tos_out == ilgl) tos_out = t->tos_out();
453
          // compute bytecode size
          {\tt assert(step > 0, "just checkin'");} \\
454
          // setup stuff for dispatching next bytecode
455
456
          if (ProfileInterpreter && VerifyDataPointer
457
              && MethodData::bytecode_has_profile(t->bytecode())) {
458
            __ verify_method_data_pointer();
459
          }
          __ dispatch_prolog(tos_out, step);
460
461
462
        // generate template
        t->generate(_masm);
463
464
        // advance
465
        if (t->does_dispatch()) {
466
      #ifdef ASSERT
467
          // make sure execution doesn't go beyond this point if code is broken
468
             should_not_reach_here();
469
      #endif // ASSERT
470
        } else {
471
          // dispatch to next bytecode
472
          __ dispatch_epilog(tos_out, step);
473
        }
474
475
      x86的字节码执行前不会做任何事, 所以没有其他代码:
476
477
478
      istore 54 istore [0x00000192d1972ba0, 0x00000192d1972c00] 96 bytes
479
        ;获取栈顶int缓存
480
        0x00000192d1972ba0: mov
                                   (%rsp),%eax
481
        0x00000192d1972ba3: add
                                   $0x8,%rsp
        ; 执行istore, 即移动bcp指针获取index, 放入局部变量槽
482
483
        0x00000192d1972ba7: movzbl 0x1(%r13),%ebx
484
        0x00000192d1972bac: neg
                                   %rbx
        0x00000192d1972baf: mov
                                   %eax,(%r14,%rbx,8)
486
487
        0x00000192d1972bb3: movzbl 0x2(%r13),%ebx
488
        0x00000192d1972bb8: add
                                   $0x2,%r13
489
        0x00000192d1972bbc: movabs $0x7fffd56e0fa0,%r10
490
        0x00000192d1972bc6: jmpq
                                   *(%r10,%rbx,8)
491
        0x00000192d1972bca: mov
                                   (%rsp),%eax
492
        0x00000192d1972bcd: add
                                   $0x8,%rsp
493
        0x00000192d1972bd1: movzwl 0x2(%r13),%ebx
494
        0x00000192d1972bd6: bswap
                                   %ebx
495
        0x00000192d1972bd8: shr
                                   $0x10,%ebx
        0x00000192d1972bdb: neg
496
                                   %rbx
497
        0x00000192d1972bde: mov
                                   %eax,(%r14,%rbx,8)
        0x00000192d1972be2: movzbl 0x4(%r13),%ebx
498
499
        0x00000192d1972be7: add
                                   $0x4,%r13
500
        0x00000192d1972beb: movabs $0x7fffd56e0fa0,%r10
        0x00000192d1972bf5: jmpq
501
                                   *(%r10,%rbx,8)
502
        0x00000192d1972bf9: nopl
                                   0x0(%rax)
503
      执行后调用的是dispatch_prolog:
504
505
      void InterpreterMacroAssembler::dispatch_epilog(TosState state, int step) {
506
        dispatch_next(state, step);
507
```

```
508
509
     void InterpreterMacroAssembler::dispatch_next(TosState state, int step) {
510
       // load next bytecode (load before advancing _bcp_register to prevent AGI)
511
       load_unsigned_byte(rbx, Address(_bcp_register, step));
512
       // advance _bcp_register
       increment(_bcp_register, step);
513
514
       dispatch_base(state, Interpreter::dispatch_table(state));
515
517
     void InterpreterMacroAssembler::dispatch base(TosState state,
518
                                                address* table.
519
                                                bool verifyoop) {
520
       verify_FPU(1, state);
521
       if (VerifyActivationFrameSize) {
         Label L;
523
         mov(rcx, rbp);
524
         subptr(rcx, rsp);
525
         int32_t min_frame_size =
526
           (frame::link_offset - frame::interpreter_frame_initial_sp_offset) *
527
528
         cmpptr(rcx, (int32 t)min frame size);
529
         jcc(Assembler::greaterEqual, L);
530
         stop("broken stack frame");
531
         bind(L);
532
       if (verifyoop) {
533
534
         verify_oop(rax, state);
535
536
     #ifdef LP64
537
       // 防止意外执行到死代码
538
       lea(rscratch1, ExternalAddress((address)table));
539
       jmp(Address(rscratch1, rbx, Address::times_8));
540
     #else
541
       Address index(noreg, rbx, Address::times_ptr);
542
       ExternalAddress tbl((address)table);
543
       ArrayAddress dispatch(tbl, index);
544
       jump(dispatch);
545
     #endif // _LP64
546
     }
547
      ______
548
     istore 54 istore [0x00000192d1972ba0, 0x00000192d1972c00] 96 bytes
       ; 获取栈顶int缓存
549
550
       0x00000192d1972ba0: mov
                                (%rsp),%eax
       0x00000192d1972ha3: add
551
                                $0x8,%rsp
552
553
       ; 执行istore, 即移动bcp指针获取index, 放入局部变量槽
554
       0x00000192d1972ba7: movzbl 0x1(%r13),%ebx
       0x00000192d1972bac: neg
                                %rbx
556
       0x00000192d1972baf: mov
                                %eax,(%r14,%rbx,8)
557
       ;加载下一个字节码,istore后面一个字节是index,所以需要r13+2
558
559
       0x00000192d1972bb3: movzbl 0x2(%r13),%ebx
560
       0x00000192d1972bb8: add
                                $0x2,%r13
561
       ; 防止意外执行到死代码
562
       0x00000192d1972bbc: movabs $0x7fffd56e0fa0,%r10
563
564
       0x00000192d1972bc6: jmpq *(%r10,%rbx,8)
565
566
       ; 之前提到istore有一个wide版本的也会一并生成,wide istore格式如下
567
       ; wide istore byte1, byte2 [四个字节]
568
       570
       0x00000192d1972bca: mov
571
                                (%rsp),%eax
572
       0x00000192d1972bcd: add
                                $0x8,%rsp
573
        ; 获取两个字节的index
574
                                                     ;除两个字节的index外0填充,比如当前index分别为2,2,扩展后ebx=0x000000202
575
       0x00000192d1972bd1: movzwl 0x2(%r13),%ebx
       0x00000192d1972bd6: bswap %ebx
                                                     ; 4个字节反序, ebx=0x02020000
576
                                                     ; ebx=0x00000202
577
       0x00000192d1972bd8: shr
                                $0x10,%ebx
578
       0x00000192d1972bdb: neg
                                                     ; 取负数
                                %rbx
       0x00000192d1972bde: mov
579
                                %eax,(%r14,%rbx,8)
                                                     ; r14-rbx*8,
580
581
       ;加载下一个字节码,wide istore byte1,byte2 所以r13+4
       0x00000192d1972be2: movzbl 0x4(%r13),%ebx
582
583
       0x00000192d1972be7: add
                                $0x4,%r13
584
       : 防止意外执行到死代码
585
586
       0x00000192d1972beb: movabs $0x7fffd56e0fa0,%r10
       0x00000192d1972bf5: jmpq
                                *(%r10,%rbx,8)
587
       0x00000192d1972bf9: nopl
                                0x0(%rax)
```