Addressing Mode RTL for 1 Operand Instructions and Conditional Jumps

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Dst	Clk Count	RTL	AddressMux 0 = MDB 1 = SP-2 2 = Address 3 = R[dst]	Address Latch	srcM 0 = R[src] 1 = PC 2 = dataFwd 3 = MDB	src Latch	dstM 0 = R[dst] 1 = Branch 2 = dataFwd 3 = MDB	dst Latch	Indirect	Ex	MR	MW	MO 0 = None 1 = IF 2 = Offset 3 = SP--
Rm	1	dst <- R[dst]	-	0	-	0	0	1	0	1	0	0	1
@Rm @Rm+	3	dst <- M[Rdst], Adr <- R[dst]	3	1	-	0	3	1	1	1	0	1	0
		None (MDB <- FU)	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
x(Rm) EDE &EDE	4	dst <- R[dst], ADR <- Offset	0	1	-	0	0	1	0	0	1	0	2
		dst <- MDB, Adr <- Address	2	1	-	0	3	1	0	1	0	1	0
		None (MDB <- FU)	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
PUSH	**Clk Count**												
Rm	3	dst. <- R[dst], Adr <- SP-2	1	1	-	0	0	1	0	0	0	1	3
		None (MDB <- FU = dst)	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
@Rm @Rm+ #N	3	dst <- M[R[dst]], Adr <- SP-2	1	1	-	0	3	1	1	0	0	1	3
		None (MDB <- FU = dst)	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
x(Rm) EDE &EDE	4	dst <- R[dst], Adr <- Offset	0	1	-	0	0	1	0	0	1	0	2
		dst <- M[R[dst]+Address], Adr <- Sp-2	1	1	-	0	3	1	0	0	0	1	3
		None (MDB <- FU = dst)	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
Call	**Clk Count**												
Rm	4	dst <- R[dst]	-	0	-	0	0	1	0	0	0	0	0
		src <- PC, Adr <- SP-2	1	1	1	1	-	0	0	1	0	1	3
		None (MDB <- FU = src)	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
@Rm @Rm+ #N	4	dst <- M[R[dst]]	-	0	-	0	3	1	1	0	0	0	0
		src <- PC, Adr <- SP-2	1	1	1	1	-	0	0	1	0	1	3
		None (MDB <- FU = src)	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
x(Rm) EDE &EDE	5	dst <- R[dst], Adr <- Offset	0	1	-	0	0	1	0	0	1	0	2
		dst <- M[R[dst] + Adr]	-	0	-	0	3	1	0	0	0	0	0
		src <- PC, Adr <- SP-2	1	1	1	1	-	0	0	1	0	1	3
		None (MDB <- FU = src)	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
RETI	**Clk Count**												
RETI	5	src <- M[SP], resA <- SR	-	0	3	1	-	0	1	1	0	0	0
		None	-	0	-	0	-	0	0	0	0	0	0
		src <- M[SP], resA <- PC	-	0	3	1	-	0	0	1	0	0	0
		None	-	0	-	0	-	0	0	0	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1
Jumps	**Clk Count**												
Jxx	2	src <- PC, dst <- Branch	-	0	1	1	1	1	0	1	0	0	0
		Instruction Fetch	-	0	-	0	-	0	0	0	0	0	1