



→ can use a multiplexor to reuse the multiplier with appropriate control signals.

Total Resources:

- 11 ADDERS
- 7 COMPARATORS
- 1 MULTIPLIER
- 1 SUBTRACTOR

Latency: 6

Through put = 1

Clock Period: $\max(T_+, T_-, T_x, T_{\text{comp}})$

Area: 8

Optimality: High performance but large area.