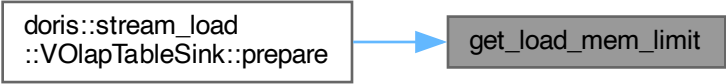


```
doris::stream_load  
::VOlapTableSink::prepare
```



```
graph LR; A["doris::stream_load  
::VOlapTableSink::prepare"] --> B["get_load_mem_limit"]
```

```
get_load_mem_limit
```