

LittleEndian::Store16

BigEndian::Store16

UNALIGNED_STORE16

```
graph LR; A[LittleEndian::Store16] --> C[UNALIGNED_STORE16]; B[BigEndian::Store16] --> C;
```

The diagram illustrates a common code path for 16-bit store operations. Two functions, `LittleEndian::Store16` and `BigEndian::Store16`, are shown on the left. Both functions have arrows pointing to a single, shaded box on the right labeled `UNALIGNED_STORE16`. This indicates that regardless of the endianness, the underlying hardware store operation is the same: an unaligned 16-bit store.