

name	TP_process_cross_sm		
clock	clk	posedge	
reset_signal	res	positive	
reset_state	IDLE	anyvalue	
implied_loopback	1	•	
INPUTS			
start_proc			Start processing tracklets
clk			Pipeline clock
res			Reset
zero_a			No more tracklets in block A
zero_b			No more tracklets in block B
zero_c			No more tracklets in block C
OUTPUTS			
proc_bsy	1	comb	Actively processing a crossing, asserted in all except IDLE
pipe_in	0	comb	Inject a 1 into the pipeline
cnt_en_a	0	comb	Enable counter A
cnt_en_b	0	comb	Enable counter B
cnt_en_c	0	comb	Enable counter C
STATES			
proc_bsy	1	output	Actively processing a crossing, asserted in all except IDLE
pipe_in	0	output	Inject a 1 into the pipeline
cnt_en_a	0	output	Enable counter A
cnt_en_b	0	output	Enable counter B
cnt_en_c	0	output	Enable counter C
TRANSITIONS			
equation	1	def_type	
priority	1000		