

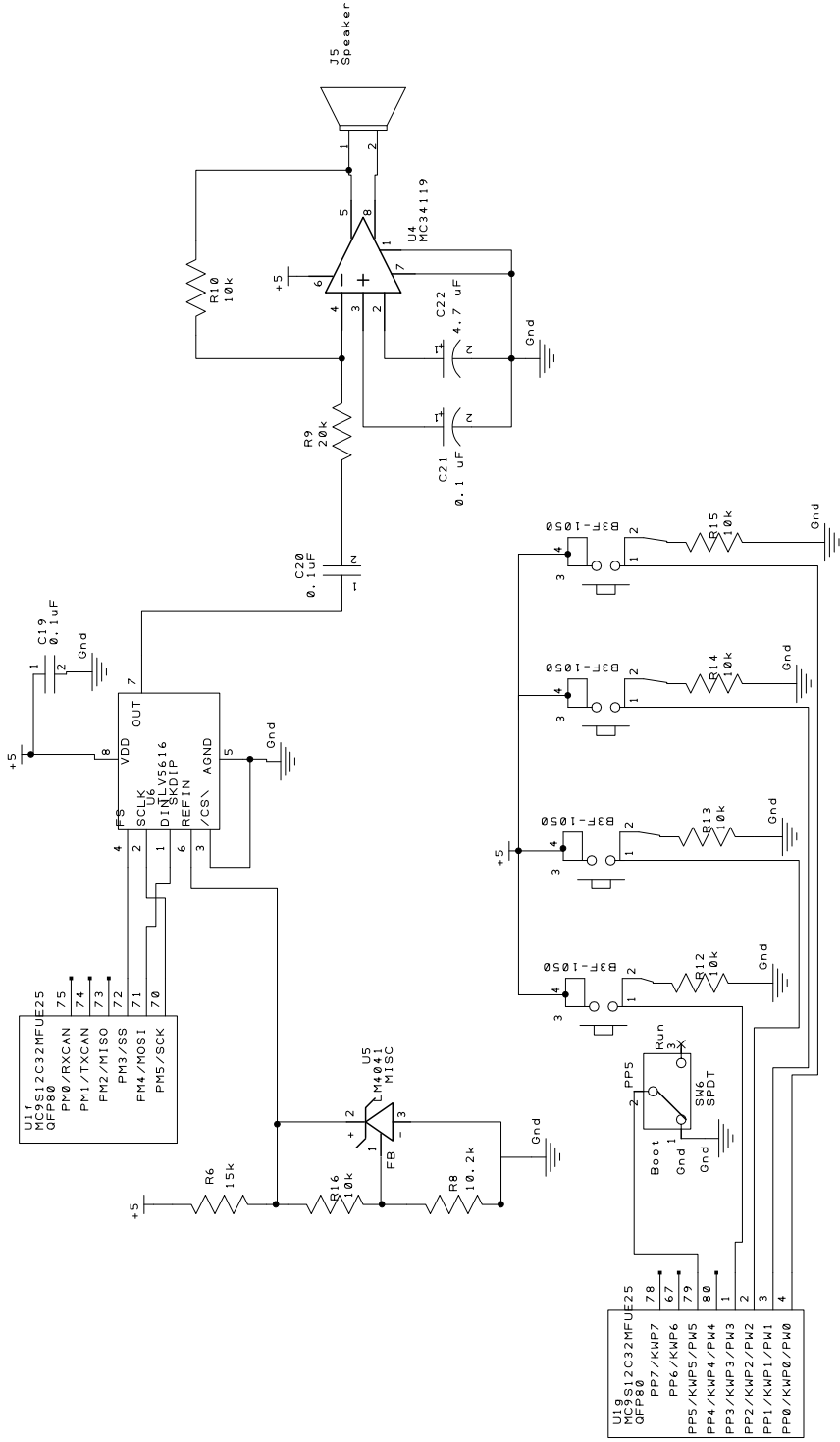
U11	
MC9S12C32MFUE25	QFP80
P77/IOC7	14
PT6/IOC6	13
PT5/IOC5	12
PT4/IOC4/PM4	11
PT3/IOC3/PM3	8
PT2/IOC2/PM2	7
PT1/IOC1/PM1	6
PT0/IOC0/PM0	5

U1B	
MC9S12C32MFUE25	QFP80
PAD07/AN07	58
PAD06/AN06	57
PAD05/AN05	56
PAD04/AN04	55
PAD03/AN03	54
PAD02/AN02	53
PAD01/AN01	52
PAD00/AN00	51

U1S	
MC9S12C32MFUE25	QFP80
PJ7/KWJ7	68
PJ6/KWJ6	69

U18	
MC9S12C32MFUE25	QFP80
PA8	41
PA1	42
PA2	43
PA3	44
PA4	45
PA5	46
PA6	47
PA7	48

U1S	
MC9S12C32MFUE25	QFP80
PB7	23
PB6	22
PB5	21
PB4	20
PB3	19
PB2	18
PB1	17
PB0	16

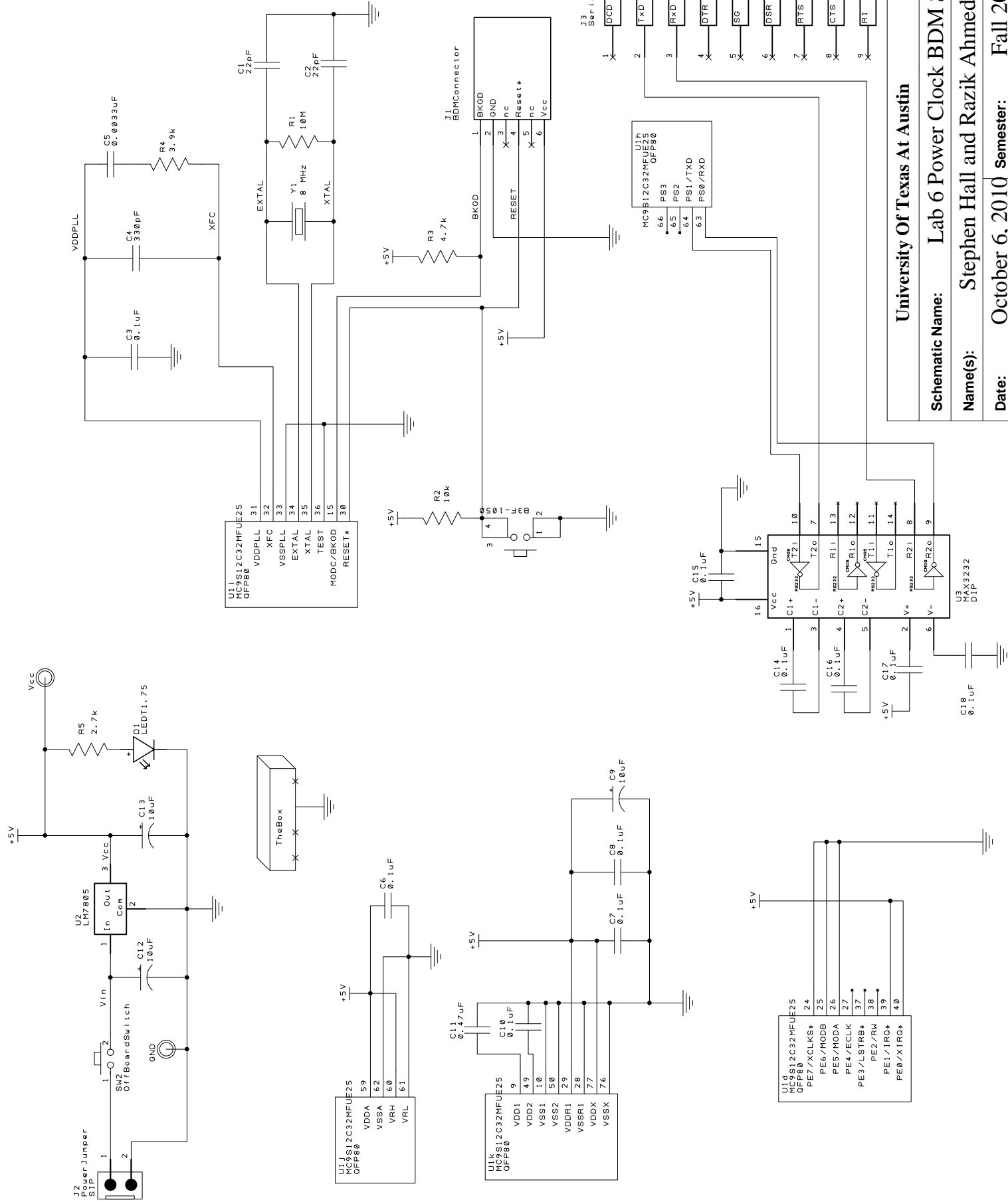


University Of Texas At Austin

Schematic Name: Lab 6 solution

Name(s): Stephen Hall and Razik Ahmed

Date: October 6, 2010 Semester: Fall 2010



University Of Texas At Austin

Schematic Name: Lab 6 Power Clock BDM Serial

Name(s): Stephen Hall and Razik Ahmed

Date: October 6, 2010 Semester: Fall 2010