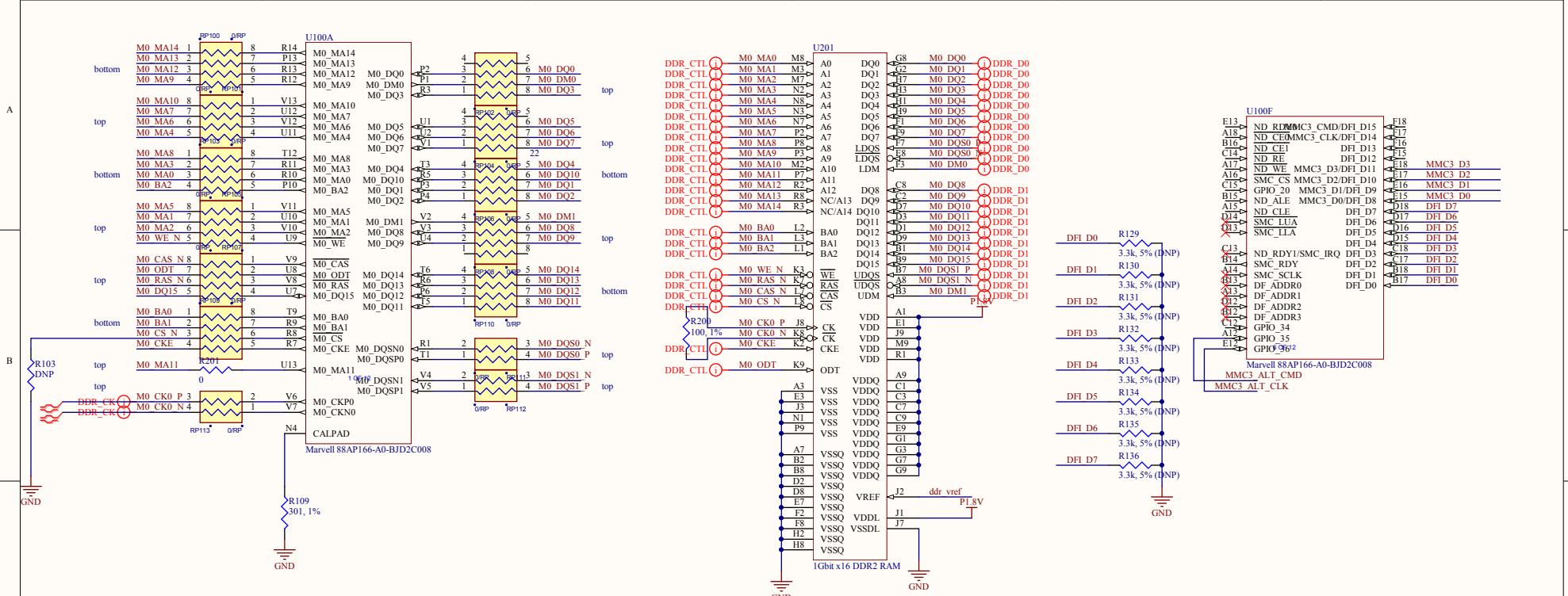




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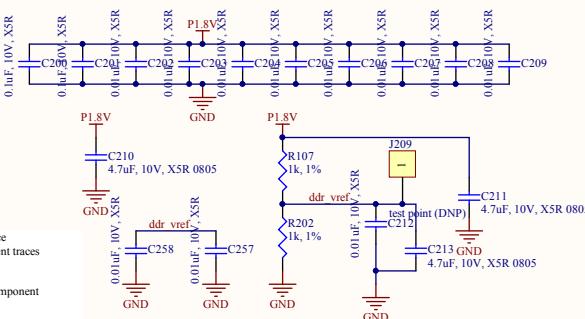


Rules:

Traces within byte lane: +/- 60 mil match
 All traces with respect to clock: +/- 200 mil match
 Within a differential pair: +/- 10 mil match

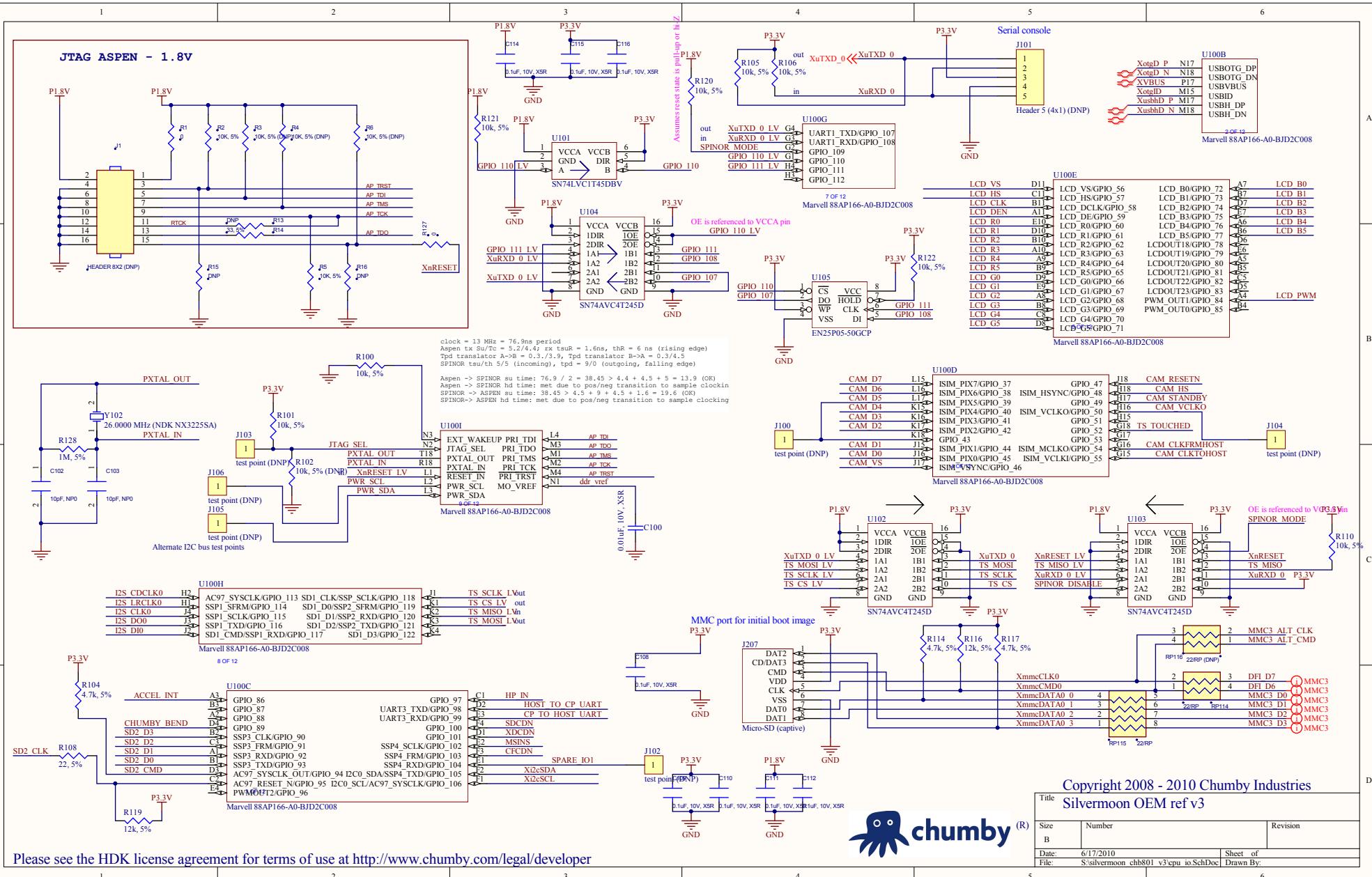
Use 4 mil routing stack from JEDEC Standard No. 21C, Page 4.20.13-46

- Route VREF with a 20-25 mil minimum trace to reduce inductance
- Maintain at least a 15-25 mil clearance from VREF to other adjacent traces
- Place a 0.1 μ F capacitor between VREF and VDDQ
- Place a 0.1 μ F capacitor between VREF and VSSQ
- Decouple at each device or connector to minimize noise at each component (Per Micron TN-46-14)



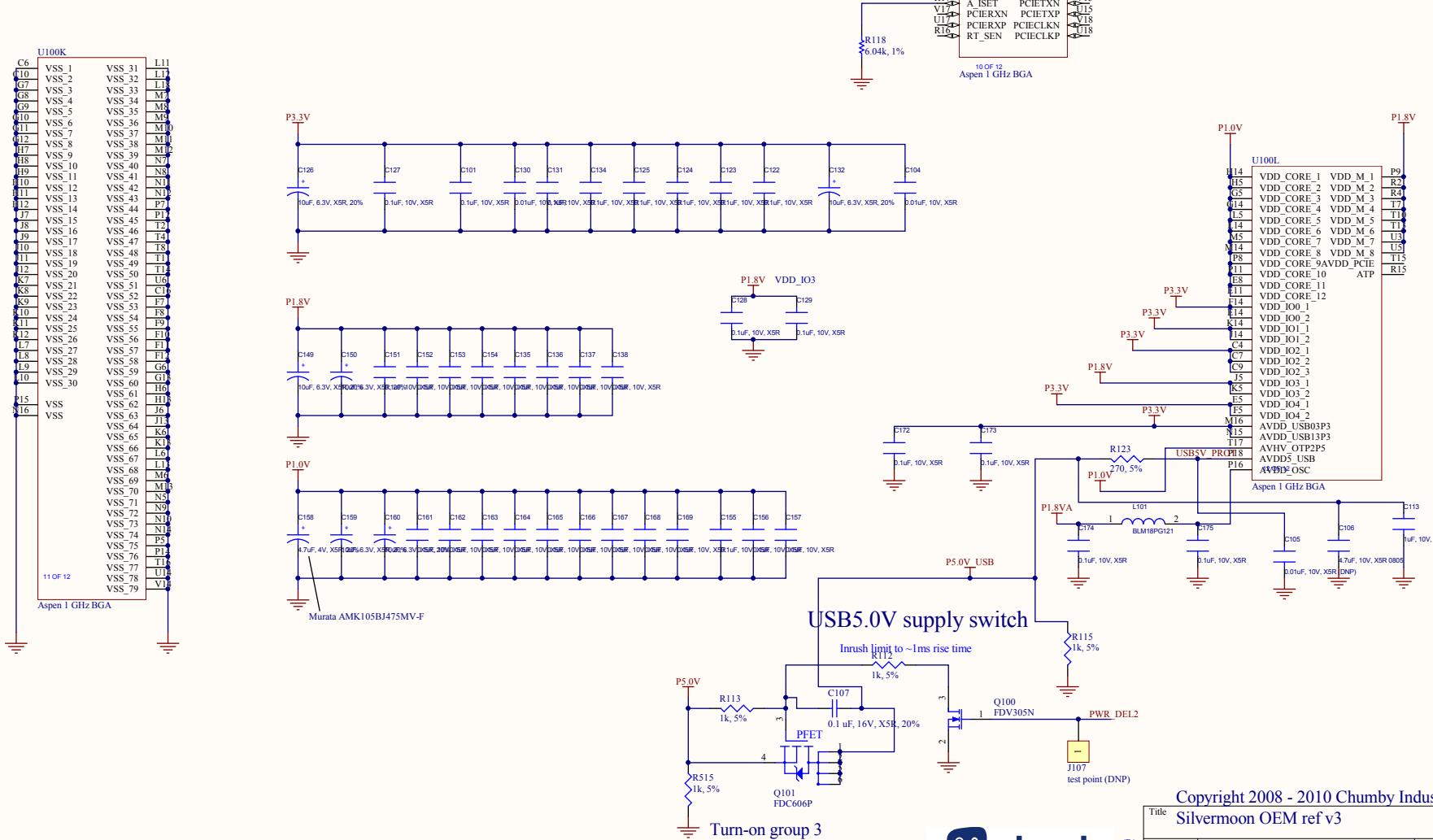
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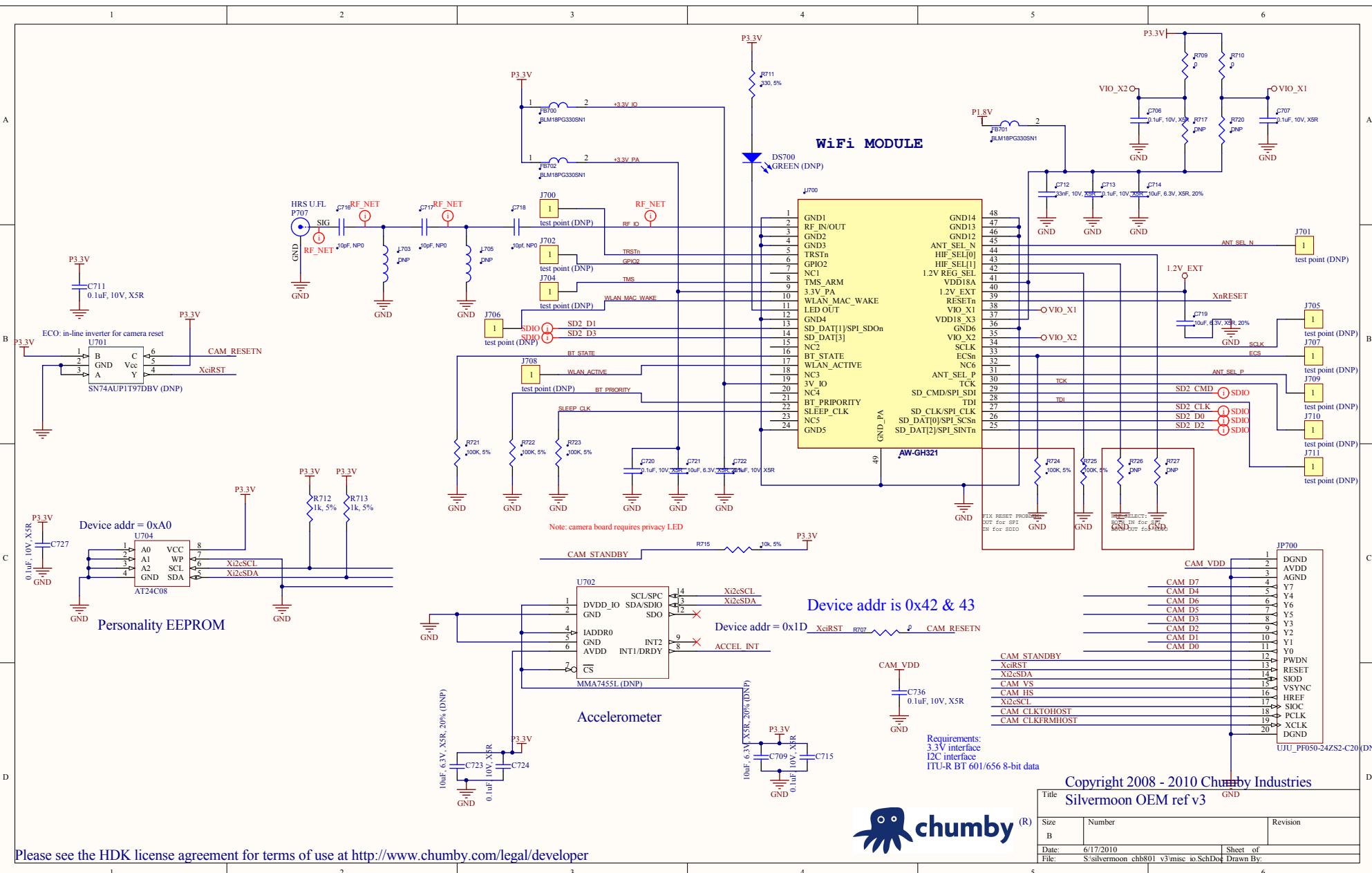


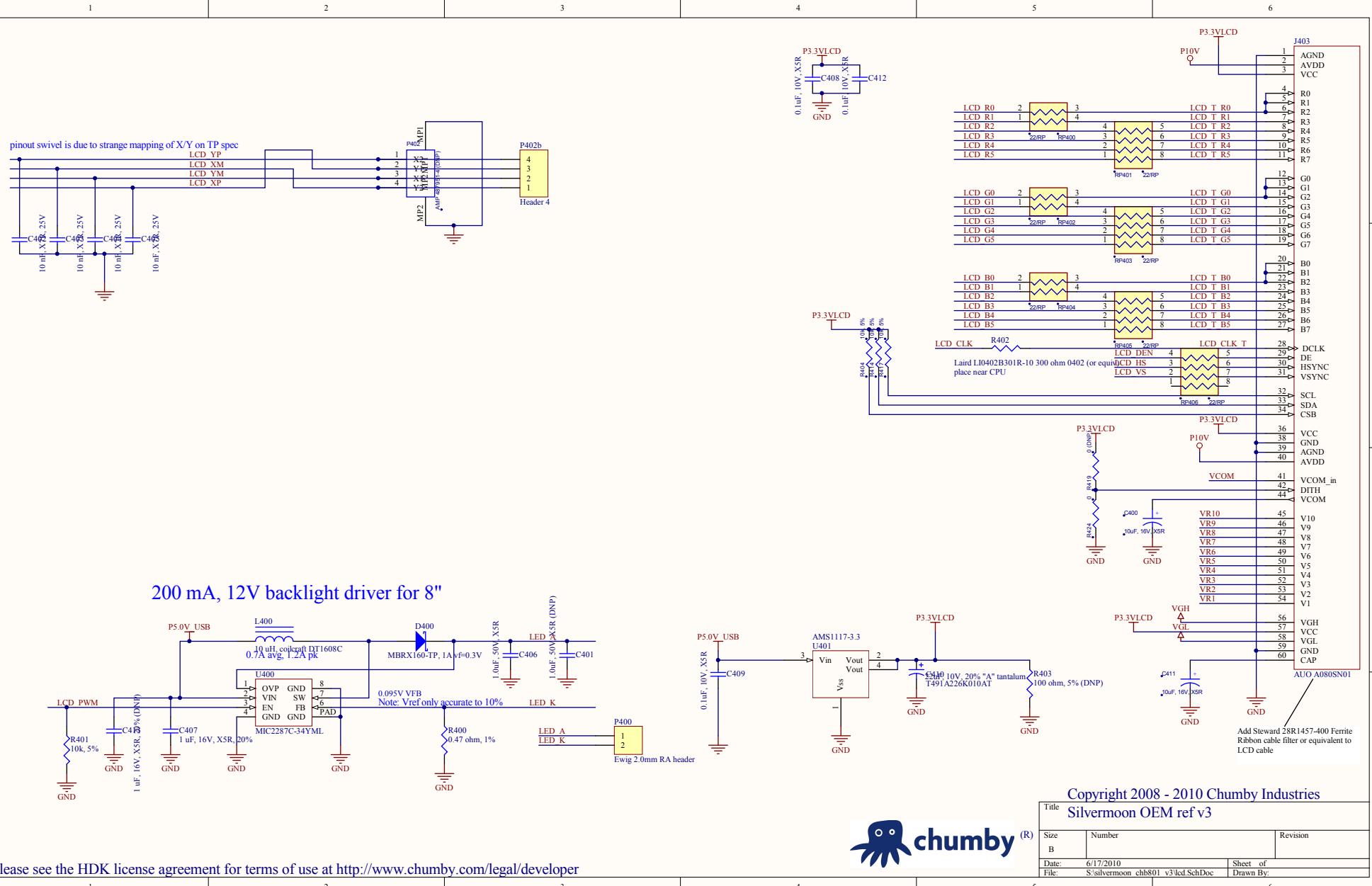


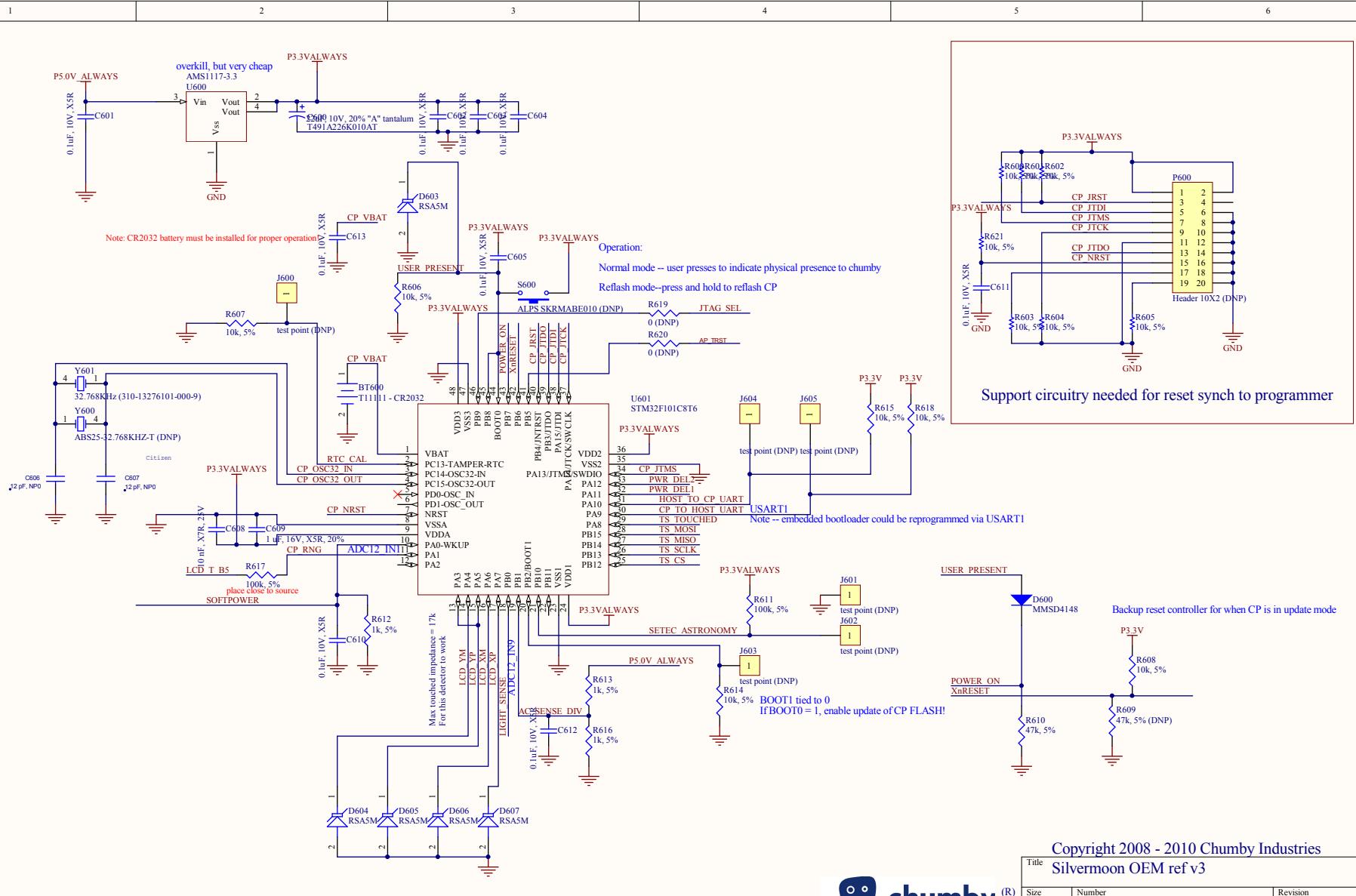
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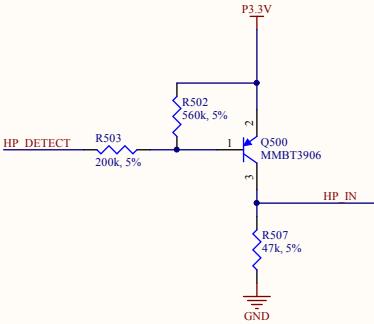
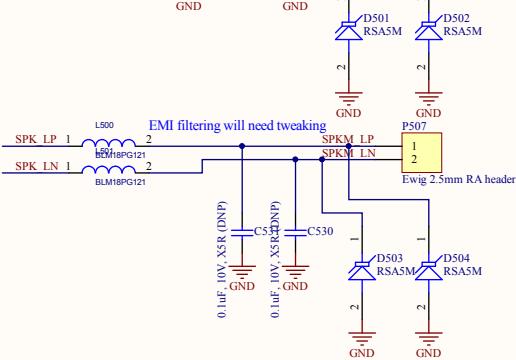
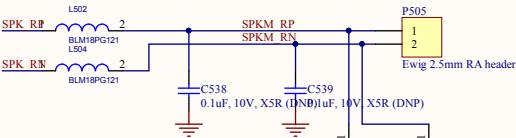
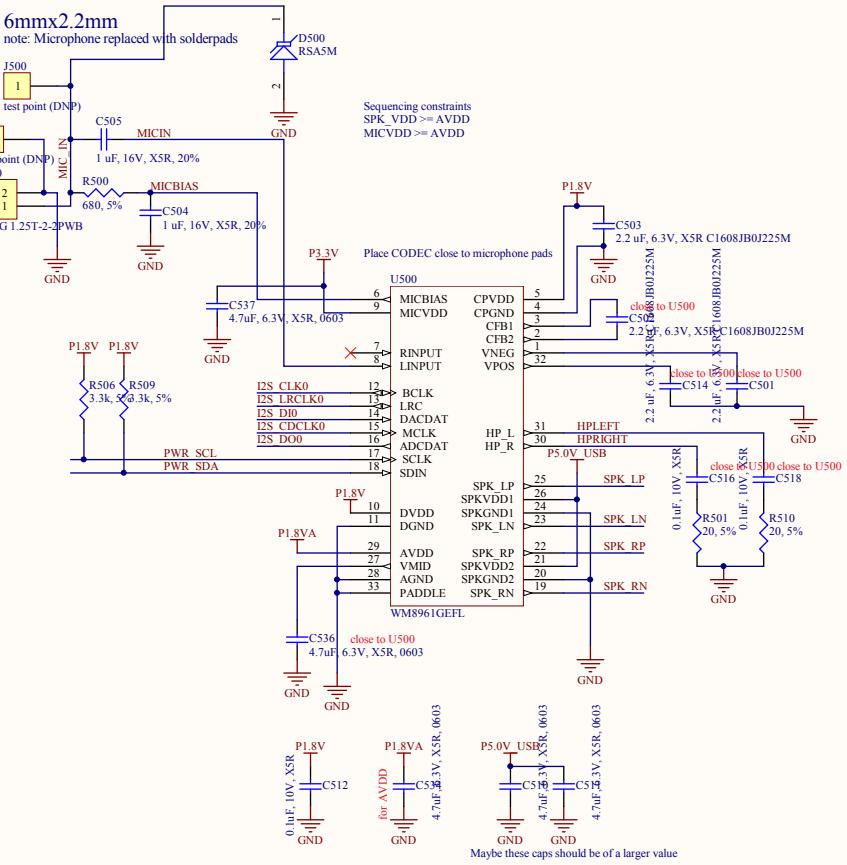






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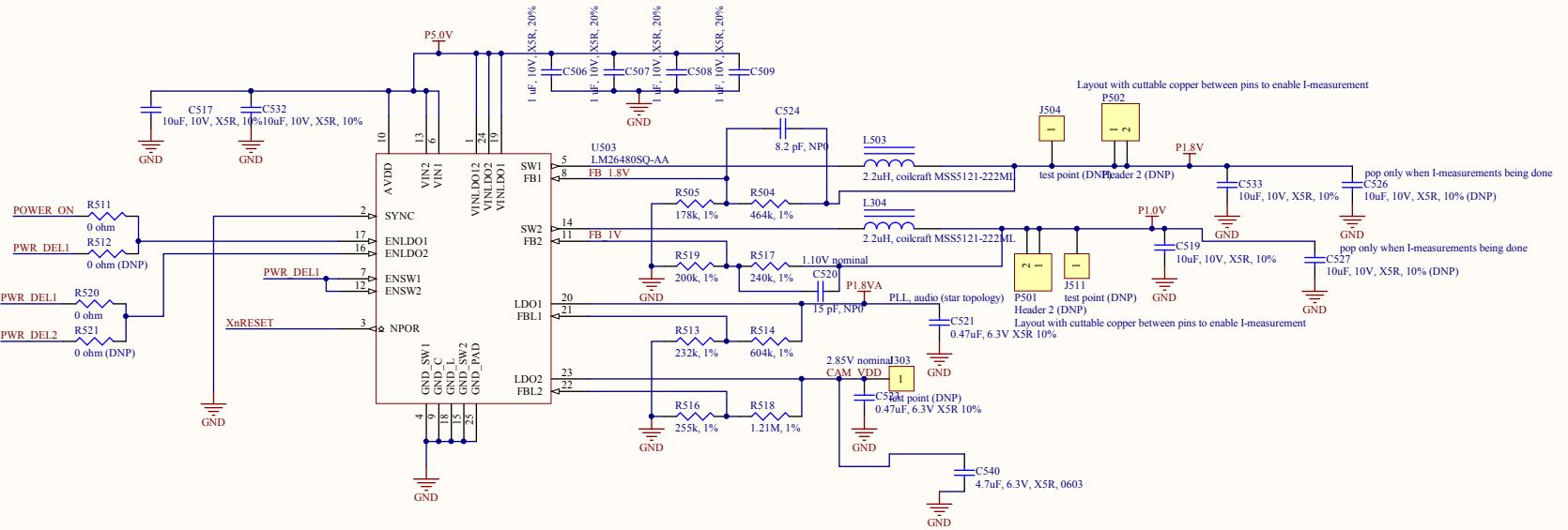
1 2 3 4 5 6



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1 2 3 4 5 6



+1.8V

CPU_VDD

wifi - 230 mA

CPU - 1500 mA pk

DDR - 440 mA pk; 320 mA avg

CPU - 550 mA pk

Power on sequence shall be as follows:

3.3V comes up first. AVDD PLL and AVDD for codec is also turned on now.

Then CPU VDD (P1.0V) and P1.8V comes up next, ideally simultaneously. Camera is also powered on at this time.

Then USB 5.0V rail is powered up.

The diagram illustrates the power supply section of the circuit. It starts with a **POWER ON** input connected to pin 1 of the **U504 MIC5259-3.3YML** IC. Pin 2 is connected to **P5.0V** through a **C525 1 uF, 16V, X5R, 20%** capacitor. Pin 3 is connected to **GND**. Pin 4 is connected to **EN**, which is also connected to **P5.0V** through a **C515 4.7nF, 10V, X5R, 0.05%** capacitor. Pin 5 is connected to **BYP**, which is connected to **GND**. Pin 6 is connected to **IN** and **OUT** through a **C522 1 uF, 16V, X5R, 20%** capacitor. Pin 7 is connected to **GND**. Pin 8 is connected to **test point (DNP)**. The **Header 2 (DNP)** is connected to pins 1 and 2 of the U504 IC. Pin 1 of Header 2 is connected to **P3.3V** through a **C528 10uF, 10V, X5R, 10% (DNP)** capacitor and **GND**. Pin 2 of Header 2 is connected to **GND**. A callout box labeled "Layout with cuttable copper between pins to enable I-measurement" points to the area between pins 1 and 2 of Header 2.

+3.3V
wifi - 200 mA
CPU - 100 mA p

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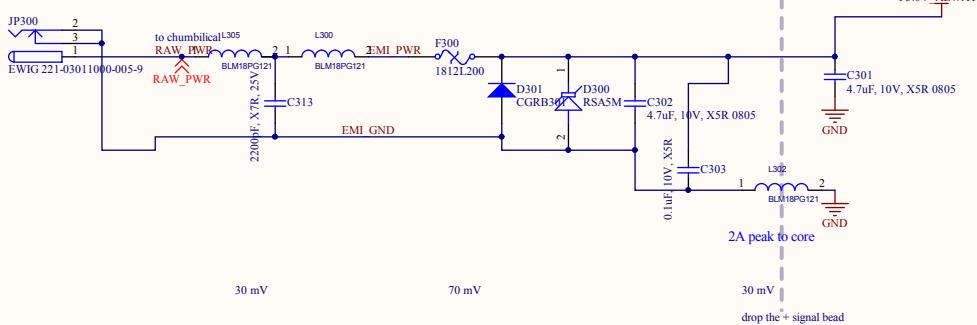
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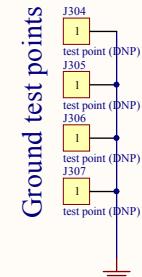
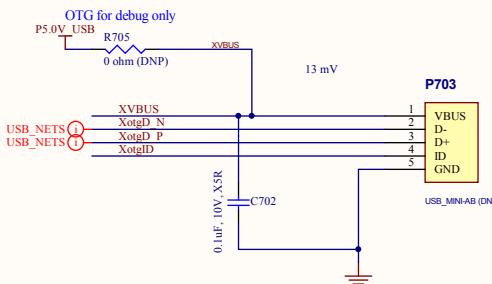
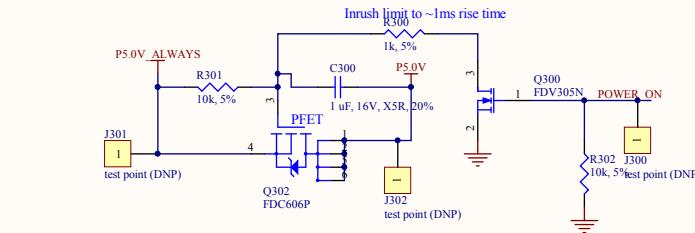
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200 ohm @ 100 MHz, 1-turn ferrite core required on cable for EMC

5V @ 2.5A peak +/- 5% from AC



Main supply switch



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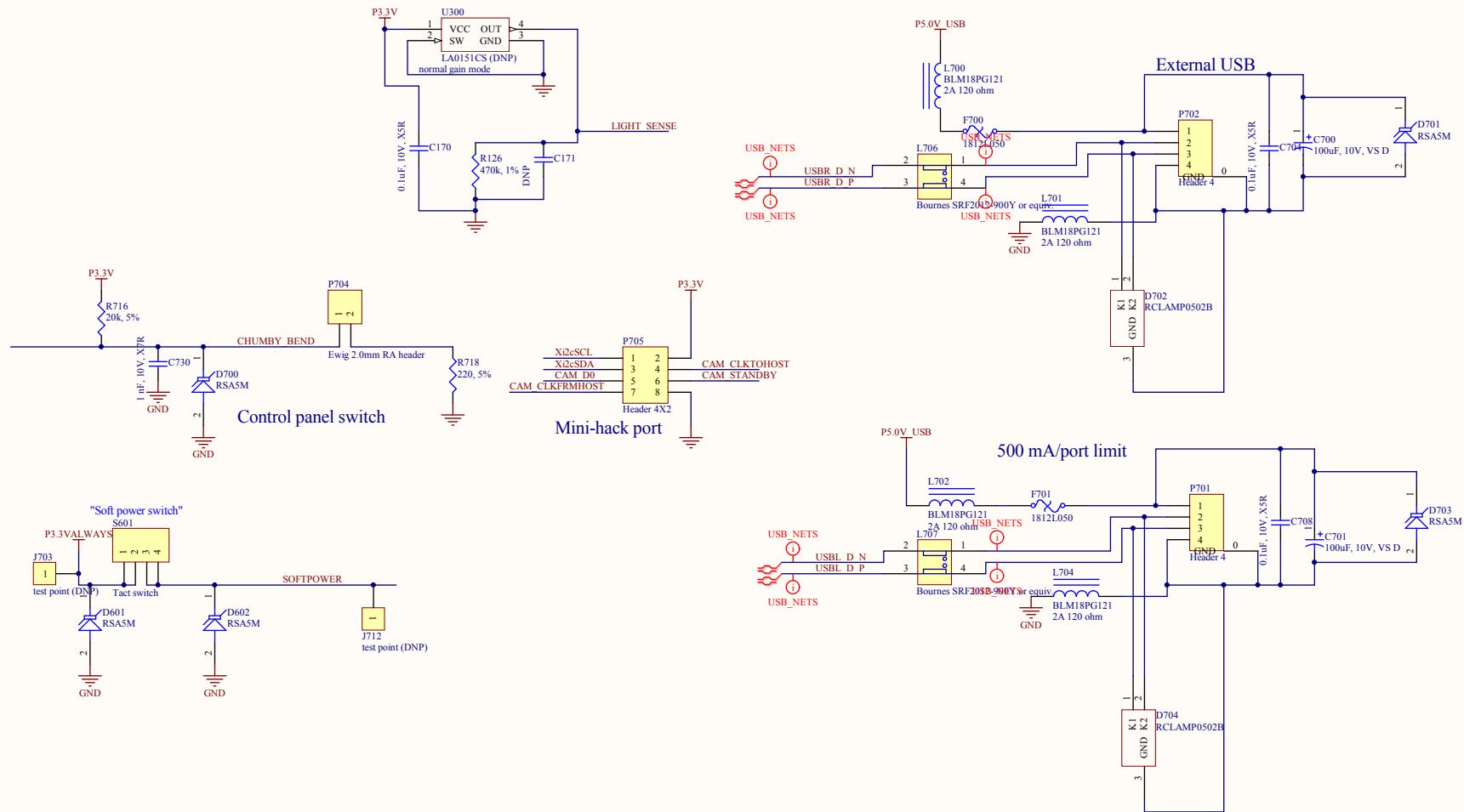
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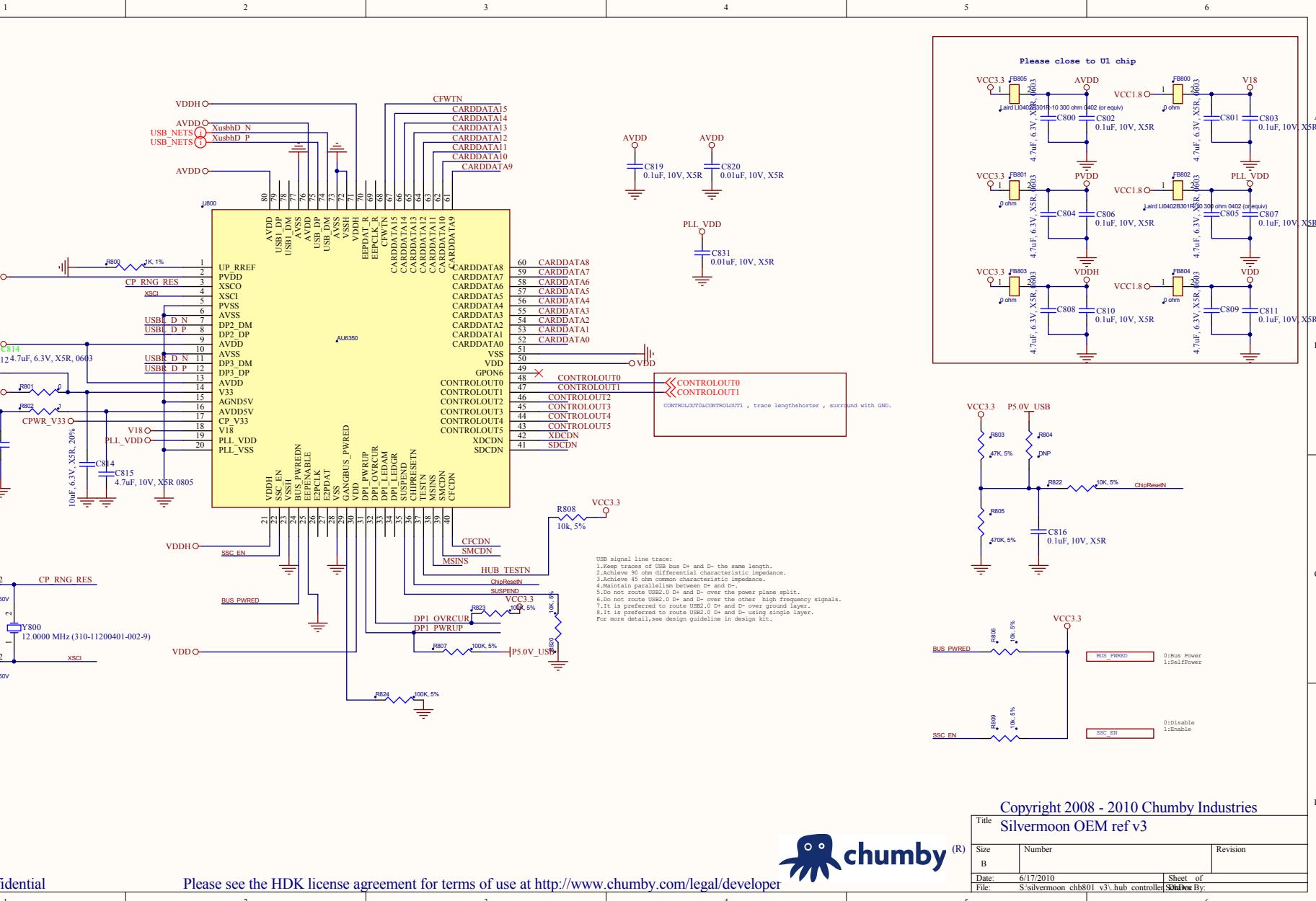
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