Christopher D. Sebesta

chris.sebesta@gmail.com ● chrissebesta.com
(713) 679-8582 ● 70 Maujer #2, Brooklyn, NY 11206

Programming focused electrical engineer experienced in building new-to-the-world, patented products from the ground up. Experienced in solving technically challenging problems and designing, testing, and deploying solutions. Transitioning from a hardware development role to software development with a focus in Android and Java.

Java – Android – XML – Android Studio – C – Embedded Development – COMSOL – LabView – CAD – Altium

PROFESSIONAL EXPERIENCE

ANDROID DEVELOPMENT, SELF EMPLOYED – New York, NY   12/15 - Present

- Android app ideation and development
    - NYC Bicycle Crash Map – An app to show motor vehicle collisions involving cyclists in NYC. Integration with NYC Open Data API, Google maps integration, Google play services, dynamic marker loading, SQLite database for information storage and offline data access, daily background data updates
    - Garden Helper – A simple gardening information app. Dynamic layouts, transitions, and animations to leverage material design practices, fragment based design, and tablet/landscape UI optimization
- Full-stack development for the android platform using Android Studio
- Development in Java, XML, and SQLite with code management in Git
- Interaction with Google Maps, Google Play Services, Services, UI Thread optimization
- Interfacing applications with APIs, JSON parsing, and data extraction

PRODUCT ENGINEER, OXO – New York, NY   6/15 – 12/15

- Maintained and updated C code for prototype testing, debugging and release
- Lead electrical side of product design for kitchen electrics group
- Managed overseas manufacturing house for large scale production and design for manufacturing
- Troubleshooting and code revision for existing product designs and software

ADVANCED PRODUCT DEVELOPMENT ENGINEER, 3M – Austin, TX   08/11 to 3/15

- Programmed and deployed automated data acquisition to increase productivity and accuracy of lab experiments
- Modeled and analyzed product designs using COMSOL to reduce time and cost while avoiding wasted tooling
- Stress tested prototypes and products in the lab to gauge field compatibility and performance (IEEE and IEC test methods)
- Product design, development and testing for release with smart grid, medium voltage, and high voltage teams
- Led cross-disciplinary, multi-location research team to identify and create necessary materials and sensors for product development and fabrication
- Tested, optimized and deployed new to the world designs and non-linear stress control materials to improve product performance
- Root cause analysis on product failures to improve future design iterations
- Presented product designs and technical concepts at conferences to increase customer demand and knowledge
- Conducted prior art searches, technical claims writing, and patent review to protect intellectual property and avoid infringement during the development phase

PATENTS

- WO2014209739, WO2015095158, WO2015095150, WO2016065574

EDUCATION

- **Bachelors of Science in Electrical Engineering |** UNIVERSITY OF TEXAS – Austin, TX (GPA: 3.88/4.0)
- University Honors, Spring 2008 – Spring 2011, Graduated cum laude
- Recipient of Janelle and Henry Holman Endowed Presidential Scholarship in Engineering, 2010-2011